

# KA78MXX

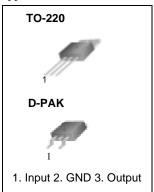
# 3-Terminal 0.5A Positive Voltage Regulator

## **Features**

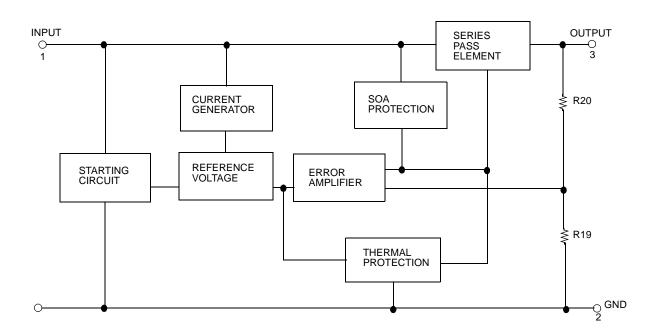
- Output Current up to 0.5A
- Output Voltages of 5, 6, 8, 10, 12, 15, 18, 24V
- Thermal Overload Protection
- Short Circuit Protection
- Output Transistor Safe Operating Area (SOA) Protection

## **Description**

The KA78MXX series of three terminal positive regulators are available in the TO-220/D-PAK package with several fixed output voltages making it useful in a wide range of applications.



## **Internal Block Diagram**



# **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Input Voltage (for V <sub>O</sub> = 5V to 18V) (for V <sub>O</sub> = 24V)	VI VI	35 40	V V
Thermal Resistance Junction-Cases (Note1) TO-220	R <sub>θ</sub> JC	2.5	°C/W
Thermal Resistance Junction-Air (Note1,2) TO-220 D-PAK	ReJA	66 92	°C/W
Operating Temperature Range	TOPR	0 ~ +125	°C
Storage Temperature Range	TSTG	-65 ~ +150	°C

# **Electrical Characteristics (KA78M05/KA78M05R)**

(Refer to the test circuits,  $0 \le TJ \le +125^{\circ}C$ , IO=350mA, VI=10V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Con	ditions	Min.	Тур.	Max.	Unit
		T <sub>J=+25°</sub> C		4.8	5	5.2	
Output Voltage	Vo	IO = 5 to 350m V <sub>I</sub> = 7 to 20V	IO = 5 to 350mA V <sub>I</sub> = 7 to 20V		5	5.25	V
Line Regulation (Note2)	ΔVο	IO = 200mA	V <sub>I</sub> = 7 to 25V	-	-	100	mo
Line Regulation (Note3)	ΔνΟ	TJ =+25°C	V <sub>I</sub> = 8 to 25V	-	-	50	me
Load Population (Note2)	4)/0	IO = 5mA  to  0.9	5A, TJ =+25°C	-	-	100	mV
Load Regulation (Note3)	ΔVO	I <sub>O</sub> = 5mA to 20	0mA, T <sub>J</sub> =+25°C	-	-	50	IIIV
Quiescent Current	lQ	TJ=+25°C	TJ=+25°C		4.0	6.0	mA
		IO = 5mA to 35	I <sub>O</sub> = 5mA to 350mA		-	0.5	
Quiescent Current Change	ΔlQ	IO = 200mA V <sub>I</sub> = 8 to 25V		-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +125°	°C	-	-0.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100	)KHz	-	40	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 8 to 18V		62	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ=+25°C, VI= 35V		-	300	-	mA
Peak Current	IPK	T <sub>J</sub> =+25°C		-	700	-	mA

- Thermal resistance test board Size: 76.2mm \* 114.3mm \* 1.6mm(1S0P) JEDEC standard: JESD51-3, JESD51-7
- 2. Assume no ambient airflow
- 3. Load and line regulation are specified at constant junction temperature. Change in V<sub>0</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M06/KA78M06R) (Continued)

(Refer to the test circuits,  $0 \le TJ \le +125$ °C, IO=350mA, VI=11V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Co	onditions	Min.	Тур.	Max.	Unit
		TJ=+25°C		5.75	6	6.25	
Output Voltage	Vo	IO = 5 to 350 V <sub>I</sub> = 8 to 21V	IO = 5 to 350mA VI= 8 to 21V		6	6.3	V
Line Population (Note1)	۸۷/۵	Io = 200mA	VI= 8 to 25V	-	-	100	mV
Line Regulation (Note1)	ΔVO	TJ =+25°C	V <sub>I</sub> = 9 to 25V	-	-	50	IIIV
Load Population (Note1)	41/0	IO = 5mA to 0	).5A, TJ =+25°C	-	-	120	mV
Load Regulation (Note1)	ΔVΟ	IO = 5mA to 2	200mA, TJ =+25°C	-	-	60	IIIV
Quiescent Current	IQ	TJ =+25°C	TJ =+25°C		4.0	6.0	mA
		$I_O = 5mA \text{ to } 3$	IO = 5mA to 350mA		-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 9 to 25V		-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +12	5°C	-	-0.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10	00KHz	-	45	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 11.5 to 21.5V		59	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ= +25°C, VI = 35V		-	300	-	mA
Peak Current	IPK	TJ =+25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M08/KA78M08R) (Continued)

(Refer to the test circuits,  $0 \le T_J \le +125^{\circ}C$ , IO=350mA, VI=14V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Co	nditions	Min.	Тур.	Max.	Unit
		T <sub>J</sub> = +25°C	T <sub>J</sub> = +25°C		8	8.3	
Output Voltage	Vo	IO = 5 to 350m V <sub>I</sub> = 10.5 to 23\		7.6	8	8.4	V
Line Regulation (Note1)	ΔVΩ	IO = 200mA	V <sub>I</sub> = 10.5 to 25V	-	-	100	mV
Line Regulation (Note1)	ΔνΟ	TJ =+25°C	V <sub>I</sub> = 11 to 25V	-	-	50	IIIV
Load Regulation (Note1)	41/0	IO = 5mA  to  0.5	5A, TJ =+25°C	-	-	160	m\/
Load Regulation (Note1)	ΔVO	I <sub>O</sub> = 5mA to 20	0mA, TJ =+25°C	-	-	80	mV
Quiescent Current	IQ	TJ=+25°C	TJ=+25°C		4.0	6.0	mA
		I <sub>O</sub> = 5mA to 350mA		-	-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 10.5 to 25	V	-	-	0.8	mA
Output Voltage Drift	RR	IO = 5mA T <sub>J</sub> = 0 to +125°	°C	-	-0.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100	)KHz	-	52	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA VI = 9 to 19V		56	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	T <sub>J</sub> =+25°C, V <sub>I</sub> = 35V		-	300	-	mA
Peak Current	IPK	T <sub>J</sub> =+25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M10) (Continued)

(Refer to the test circuits,  $0 \le T_J \le +125^{\circ}C$ , IO=350mA, VI=17V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Cor	nditions	Min.	Тур.	Max.	Unit
		TJ= +25°C	T <sub>J</sub> = +25°C		10	10.4	
Output Voltage	Vo	~	IO = 5 to 350mA VI= 12.5 to 25V		10	10.5	V
Line Regulation (Note1)	4\/0	Io = 200mA	IO = 200mA		-	100	mV
Line Regulation (Note1)	ΔνΟ	$\Delta V_{O}$ $T_{J} = +25^{\circ}C$ $V_{I} = 13 \text{ to } 25V$ -	-	50	IIIV		
Load Population (Note1)	ΔVο	IO = 5mA to 0	0.5A, TJ =+25°C	-	-	200	mV
Load Regulation (Note1)	ΔνΟ	I <sub>O</sub> = 5mA to 2	200mA, TJ =+25°C	-	-	100	IIIV
Quiescent Current	IQ	TJ=+25°C	TJ=+25°C		4.1	6.0	mA
		$I_0 = 5mA \text{ to } 3$	IO = 5mA to 350mA		-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 12.5 to 2	5V	-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +125	5°C	-	-0.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10	00KHz	-	65	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 13 to 23V		55	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ= +25°C, VI= 35V		-	300	-	mA
Peak Current	IPK	TJ =+25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M12/KA78M12R) (Continued)

(Refer to the test circuits,  $0 \le T_J \le +125^{\circ}C$ , IO=350mA, VI=19V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Cor	nditions	Min.	Тур.	Max.	Unit
		T <sub>J=+25°</sub> C	T <sub>J</sub> =+25°C		12	12.5	
Output Voltage	Vo	IO = 5 to 350 V <sub>I</sub> = 14.5 to 2		11.5	12	12.6	V
Line Population (Note1)	4\/0	IO = 200mA	VI= 14.5 to 30V	-	-	100	mV
Line Regulation (Note1)	$\Delta$ VO $T_J = +25^{\circ}C$ $V_I = 16 \text{ to } 30V$	-	-	50	IIIV		
Load Population (Note1)	4\/0	IO = 5mA to 0	).5A, TJ =+25°C	-	-	240	mV
Load Regulation (Note1)	ΔVΟ	I <sub>O</sub> = 5mA to 2	200mA, TJ =+25°C	-	-	120	IIIV
Quiescent Current	IQ	TJ=+25°C		-	4.1	6.0	mA
		$I_O = 5mA \text{ to } 3$	I <sub>O</sub> = 5mA to 350mA		-	0.5	
Quiescent Current Change	ΔlQ	IO = 200mA V <sub>I</sub> = 14.5 to 3	0V	-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +12	5°C	-	-0.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10	00KHz	-	75	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 15 to 25V		55		-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ= +25°C, V = 35V		-	300	-	mA
Peak Current	IPK	T <sub>J</sub> = +25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M15) (Continued)

(Refer to the test circuits,  $0 \le T_J \le +125^{\circ}C$ , IO=350mA, VI=23V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		T <sub>J=+25°</sub> C	T <sub>J</sub> =+25°C		15	15.6	
Output Voltage	Vo	IO = 5 to 350 V <sub>I</sub> = 17.5 to 3		14.25	15	15.75	V
Line Regulation (Note1)	ΔVο	IO = 200mA	V <sub>I</sub> = 17.5 to 30V	-	-	100	mV
Line Regulation (Note I)	ΔνΟ	TJ =+25°C	V <sub>I</sub> = 20 to 30V	-	-	50	IIIV
Load Population (Note1)	ΔVο	IO = 5mA to (	0.5A, TJ =+25°C	-	-	300	mV
Load Regulation (Note1)	ΔνΟ	$I_O = 5mA \text{ to } 2$	200mA, TJ =+25°C	-	-	150	IIIV
Quiescent Current	IQ	TJ=+25°C		-	4.1	6.0	mA
		IO = 5mA to 350mA		-	-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 17.5 to 3	30V	-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +12	25°C	-	-1	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 1	00KHz	-	100	-	μV/Vo
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 18.5 to 28.5V		54	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ= +25°C, VI= 35V		-	300	-	mA
Peak Current	IPK	T <sub>J</sub> = + 25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M18) (Continued)

(Refer to the test circuits,  $0 \le TJ \le +125$ °C, IO=350mA, VI=26V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		T <sub>J</sub> =+25°C		17.3	18	18.7	
Output Voltage	Vo	IO = 5 to 350m/ V <sub>I</sub> = 20.5 to 33V	1	17.1	18	18.9	V
Line Regulation (Note1)	ΔVο	Io = 200mA	VI= 21 to 33V	-	-	100	mV
Line Regulation (Note I)	ΔνΟ	T <sub>J</sub> =+25°C	V <sub>I</sub> = 24 to 33V	-	-	50	1111
Load Pogulation (Note1)	ΔVΟ	IO = 5mA to 0.5	A, TJ =+25°C	-	-	360	mV
Load Regulation (Note1)	Δ۷Ο	I <sub>O</sub> = 5mA to 200	mA, T <sub>J</sub> =+25°C	-	-	180	1111
Quiescent Current	lQ	TJ =+25°C		-	4.2	6.0	mA
		I <sub>O</sub> = 5mA to 350	)mA	-	-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 21 to 33V		-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO =5mATJ =0 t	o 125°C	-	-1.1	-	mV/°C
Output Noise Voltage	VN	f=10Hz to 100K	Hz	-	100	-	μV/Vo
Ripple Rejection	RR	f=120Hz, I <sub>O</sub> =300mA , V <sub>I</sub> =22 to 32V		53	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> =500mA		-	2	-	V
Short Circuit Current	Isc	TJ =+25°C, V <sub>I</sub> =	TJ =+25°C, VI=35V		300	-	mA
Peak Current	IPK	TJ =+25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (KA78M24) (Continued)

(Refer to the test circuits,  $0 \le T_J \le +125^{\circ}C$ , IO=350mA, VI=33V, unless otherwise specified, CI =0.33 $\mu$ F, CO=0.1 $\mu$ F)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		T <sub>J</sub> = +25°C	$T_J = +25^{\circ}C$		24	25	
Output Voltage	Vo	IO = 5 to 350 V <sub>I</sub> = 27 to 38		22.8	24	25.2	V
Line Regulation	41/0	Io = 200mA	V <sub>I</sub> = 27 to 38V	-	-	100	mV
Line Regulation	ΔVO	T <sub>J</sub> =+25°C	V <sub>I</sub> = 28 to 38V	-	-	50	IIIV
Load Population	41/0	IO = 5mA to	0.5A, TJ =+25°C	-	-	480	mV
Load Regulation	ΔVΟ	Io = 5mA to	200mA, T <sub>J</sub> =+25°C	-	-	240	IIIV
Quiescent Current	IQ	TJ = +25°C		-	4.2	6	mA
		$I_O = 5mA$ to	I <sub>O</sub> = 5mA to 350mA		-	0.5	
Quiescent Current Change	ΔlQ	I <sub>O</sub> = 200mA V <sub>I</sub> = 27 to 38	V	-	-	0.8	mA
Output Voltage Drift	ΔV/ΔΤ	IO = 5mA T <sub>J</sub> = 0 to +12	25°C	-	-1.2	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 1	00KHz	-	170	-	μV
Ripple Rejection	RR	f = 120Hz, I <sub>O</sub> = 300mA V <sub>I</sub> = 28 to 38V		50	-	-	dB
Dropout Voltage	VD	T <sub>J</sub> =+25°C, I <sub>O</sub> = 500mA		-	2	-	V
Short Circuit Current	Isc	TJ = +25 °C,	VI = 35V	-	300	-	mA
Peak Current	IPK	T <sub>J</sub> =+25°C		-	700	-	mA

<sup>1.</sup> Load and line regulation are specified at constant, junction temperature. Change in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# **Typical Applications**

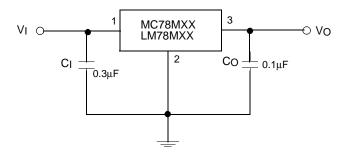


Figure 1. Fixed Output Regulator

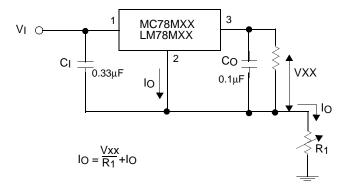


Figure 2. Constant Current Regulator

- 1. To specify an output voltage, substitute voltage value for "XX"
- 2. Although no output capacitor is needed for stability, it does improve transient response.
- 3. Required if regulator is located an appreciable distance from power Supply filter

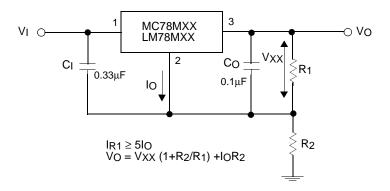


Figure 3. Circuit for Increasing Output Voltage

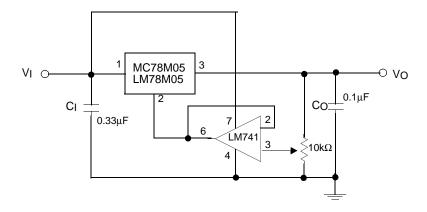


Figure 4. Adjustable Output Regulator (7 to 30V)

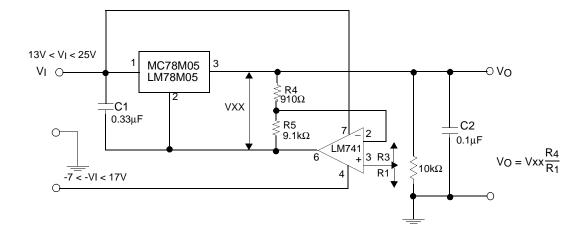


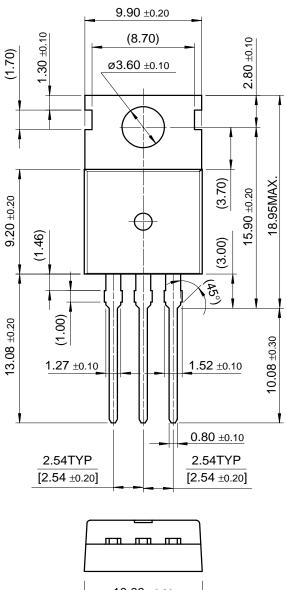
Figure 5. 0.5 to 10V Regulator

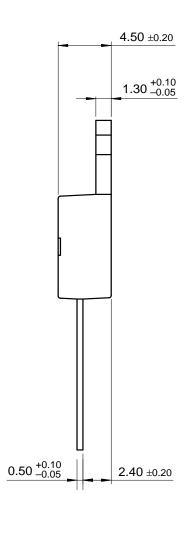
## **Mechanical Dimensions**

## **Package**

## **Dimensions in millimeters**

**TO-220** 



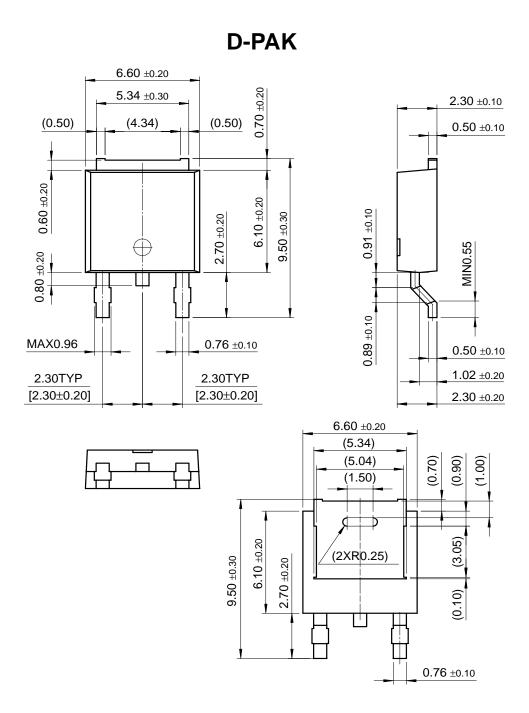




## **Mechanical Dimensions** (Continued)

## **Package**

## **Dimensions in millimeters**



## **Ordering Information**

Product Number	Package	Operating Temperature
KA78M05		
KA78M06		
KA78M08		
KA78M10	TO-220	
KA78M12	10-220	
KA78M15		
KA78M18		0 ~ +125°C
KA78M24		
KA78M05R		
KA78M06R		
KA78M08R	D-PAK	
KA78M12R		
KA78M08AR		

#### **DISCLAIMER**

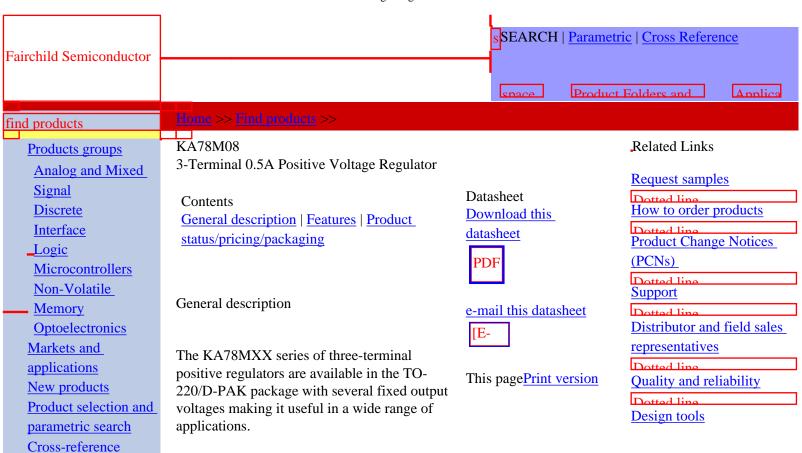
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**Features** 

- Output Current up to 0.5A
- Output Voltage of 5, 6, 8, 10, 12, 15, 18, 20, 24V
- Thermal Overload Protection
- Short Circuit Protection
- Output Transistor Safe Operating area (SOA)Protection

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## Product status/pricing/packaging

Product	Product status	Package type	Leads	Packing method
KA78M08RTF	Full Production	TO-252(DPAK)	2	TAPE REEL
KA78M08TSTU	Full Production	TO-220	3	RAIL
KA78M08TU	Full Production	TO-220	3	RAIL
KA78M08	Full Production	TO-220	3	BULK
KA78M08RTM	Full Production	TO-252(DPAK)	2	TAPE REEL

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