

SPECIFICATION FOR APPROVAL

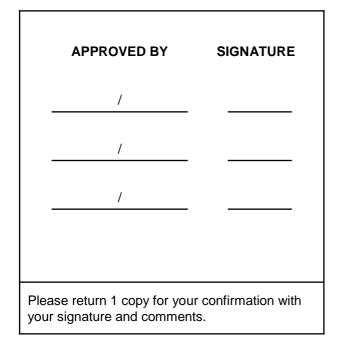
- () Preliminary Specification
- (**♦**) Final Specification
 - Title

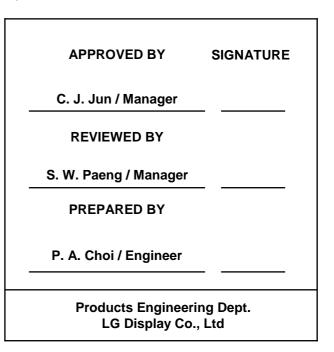
Customer	HP
MODEL	

10.1"	HD	TFT	LCD
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SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP101WH1		
Suffix	TLB4		

*When you obtain standard approval, please use the above model name without suffix







<u>Contents</u>

No	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	
3-1	ELECTRICAL CHARACTREISTICS	6
3-2	INTERFACE CONNECTION	7
3-3	LVDS SIGNAL TIMING SPECIFICATIONS	8
3-4	SIGNAL TIMING SPECIFICATIONS	10
3-5	SIGNAL TIMING WAVEFORMS	10
3-6	COLOR INPUT DATA REFERNECE	11
3-7	POWER SEQUENCE	12
4	OPTICAL SFECIFICATIONS	13
5	MECHANICAL CHARACTERISTICS	16
6	RELIABLITY	20
7	INTERNATIONAL STANDARDS	
7-1	SAFETY	21
7-2	EMC	21
8	PACKING	
8-1	DESIGNATION OF LOT MARK	22
8-2	PACKING FORM	22
9	PRECAUTIONS	23
Α	APPENDIX. Enhanced Extended Display Identification Data	25



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
1.0	Jun. 16. 2009	All	First Draft (Final Specification)	-

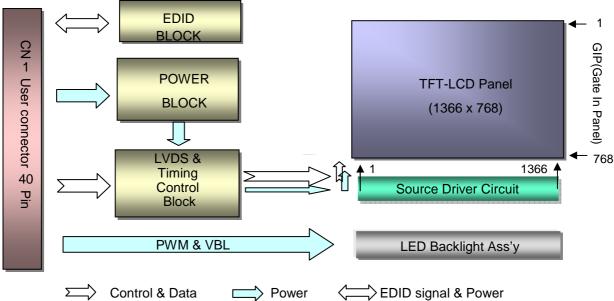


1. General Description

The LP101WH1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 10.1inches diagonally measured active display area with HD resolution(1366 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP101WH1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP101WH1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP101WH1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	10.1 inches diagonal
Outline Dimension	235(H) × 143(V) × .5.2(D,Max.) [mm]
Pixel Pitch	0.16305mm × 0.16305 mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.5 point)
Power Consumption	Total 3.46 Watt(Typ.) @ LCM circuit 1.16 Watt(Typ.), B/L input 2.3 Watt(Typ.) (W/O LED Driver)
Weight	200g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Anti-Glare treatment of the front polarizer
RoHS Comply	Yes
Ver. 1.0	Jun. 16, 2009 4 / 27



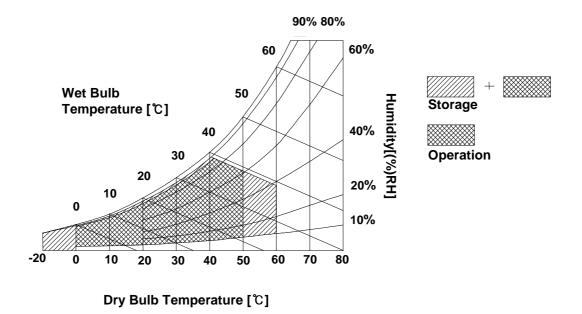
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Parameter	Sumbol	Val	ues	Units	Notes	
Falanletei	Symbol	Min	Max	Units		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 \pm 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Table 1. ABSOLUTE MAXIMUM RATINGS

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39°C Max, and no condensation of water.





3. Electrical Specifications

3-1. Electrical Characteristics

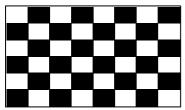
The LP101WH1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Deremeter	Sumbol		l la it	Nistas			
Parameter	Symbol	Min Typ		Max	Unit	Notes	
MODULE :							
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V _{DC}	[
Power Supply Input Current	I _{cc}	-	350	402	mA	1	
Power Consumption	Pc	-	1.16	1.33	Watt	1	
Differential Impedance	Zm	90	100	110	Ohm	2	
LED Backlight (With LED Driver):							
LED Driver (@12V)	P _{DRIVER}	-	0.21	0.25	Watt	[
Operating Voltage	V _{LED}	-	28.85	32.4	V	[
Operating Current per string	I _{LED}	-	20		mA	3	
Power Consumption	P _{BL}	-	2.3	2.592	Watt	4	
Life Time		10,000		1	Hrs	5	

Table 2. ELECTRICAL CHARACTERISTICS

Note)

1. The specified current and power consumption are under the Vcc = 3.3V, 25 °C, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
- I_{LED} is the current of each LED's string, LED backlight has 4 strings on it.
- 4. The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
- 5. The life time is determined as the time at which brightness of LED is 50% compare to that of initial value at the typical LED current.



3-2. Interface Connection

This LCD employs one interface connection, a 40 pin connector is used for the module electronics interface.

Pin Symbol 1 CT1/NC 2 VDD 3 VDD 4 Vedit 5 Test 6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	Description Connector Test/No Connection(Reserved) +3.3V Power Supply +3.3V Power Supply +3.3V EDID Power Panel Self Test EDID Clock Input EDID Data Input LVDS differential data input CVDS differential data input Ground	Notes 1, Interface chips 1.1 LCD : SiW, 1port including LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector 2.1 LCD :I-PEX 20455-040E-0*
2 VDD 3 VDD 4 VEDID 5 Test 6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	+3.3V Power Supply +3.3V Power Supply +3.3V EDID Power Panel Self Test EDID Clock Input EDID Data Input LVDS differential data input LVDS differential data input	 1.1 LCD : SiW, 1port including LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector
3 VDD 4 V _{EDID} 5 Test 6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	+3.3V Power Supply +3.3V EDID Power Panel Self Test EDID Clock Input EDID Data Input LVDS differential data input LVDS differential data input	 1.1 LCD : SiW, 1port including LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector
4 V _{EDID} 5 Test 6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	+3.3V EDID Power Panel Self Test EDID Clock Input EDID Data Input LVDS differential data input LVDS differential data input	 1.1 LCD : SiW, 1port including LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector
5 Test 6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	Panel Self Test EDID Clock Input EDID Data Input LVDS differential data input LVDS differential data input	 1.1 LCD : SiW, 1port including LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector
6 CLK _{EDID} 7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	EDID Clock Input EDID Data Input LVDS differential data input LVDS differential data input	LVDS Receiver 1.2 System : * Pin to Pin compatible with LVDS 2. Connector
7 DATA _{EDID} 8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	EDID Data Input LVDS differential data input LVDS differential data input	1.2 System :* Pin to Pin compatible with LVDS2. Connector
8 RxIN0- 9 RxIN0+ 10 GND 11 RxIN1-	LVDS differential data input LVDS differential data input	* Pin to Pin compatible with LVDS 2. Connector
9 RxIN0+ 10 GND 11 RxIN1-	LVDS differential data input	
10 GND 11 RxIN1-		
11 RxIN1-	Ground	2.1 LOD .1-1 LA 20433-040L-0
• · · · · · · • • • · · · · · · · · · ·		(Locking type)
40 DuiNi4	LVDS differential data input	or equivalent
12 RxIN1+	LVDS differential data input	2.2 Moting -
13 GND	Ground	2.2 Mating : 2.3 Connector pin arrangement
14 RxIN2-	LVDS differential data input	
15 RxIN2+	LVDS differential data input	40 1 П ПП П
16 GND	Ground	,Ш.Ш Ш.Ш ₋
17 RxCLKIN-	LVDS differential clock input	
18 RxCLKIN+	LVDS differential clock input	[LCD Module Rear View]
19 GND	Ground	
20 NC	No Connection	
21 NC	No Connection	
22 GND	Ground	
23 NC	No Connection	
24 NC	No Connection	
25 GND	Ground	
26 NC	No Connection	
27 NC	No Connection	
28 GND	Ground	
29 NC	No Connection	
30 NC	No Connection	
31 VLED_GND	LED Ground	
32 VLED_GND	LED Ground	
33 VLED_GND	LED Ground	
34 CT2/NC	Connector Test/No Connection(Reserved)	
35 S_PWMIN	System PWM signal input(+3.3V swing)	
36 BL_ON	LED Enable(3.3V Input) [Note 1]	
37 NC	No Connection	
38 VLED	7~21V LED Power Supply	
39 VLED	7~21V LED Power Supply	[Note 1] On: 2.0V↑,Off:0~0.4V
40 VLED	7~21V LED Power Supply	On. 2.0V ⊨,On.0~0.4V

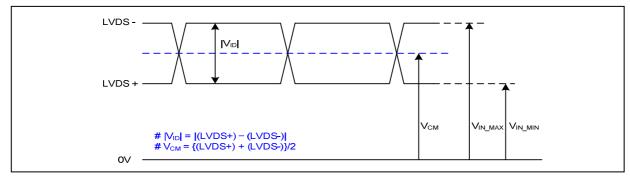
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Ver. 1.0



3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



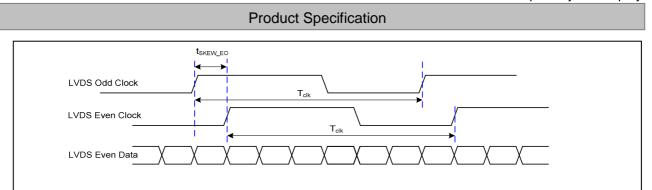
Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

3-3-2. AC Specification

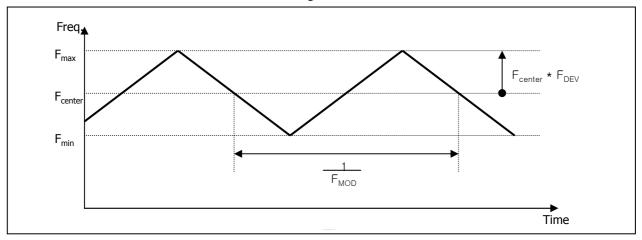
$LVDS Clock$ $LVDS Data$ $t_{SKEW}(F_{clk} = 1/T_{clk})$ $t_{SKEW}(F_{clk} = 1/T_{clk})$ $1) 85MHz > Fclk \ge 65MHz : -400 \sim +400$ $t_{SKEW} = 25MHz : -600 \sim +600$						
Description	Symbol	Min	Max	Unit	Notes	
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz	
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz	
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-	
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-	
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-	



LP101WH1 Liquid Crystal Display



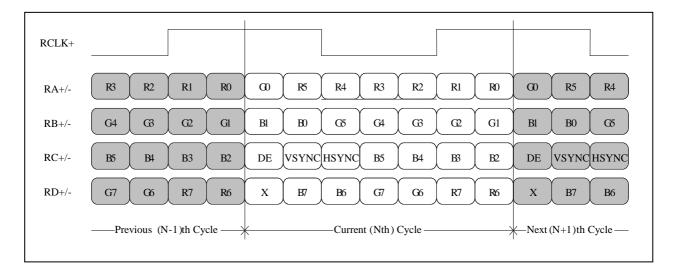
< Clock skew margin between channel >





3-3-3. Data Format





< LVDS Data Format >

Condition : VCC =3.3V



Product Specification

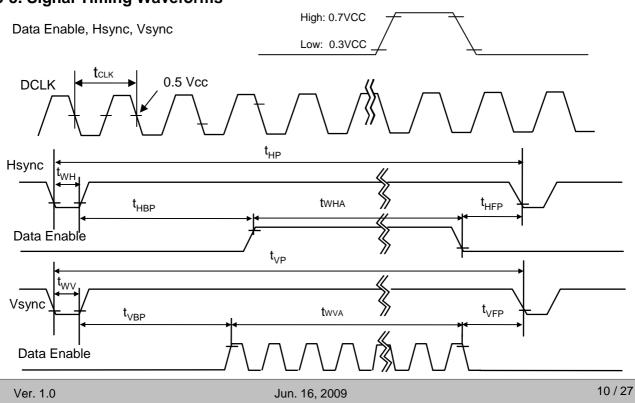
3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

	1 0.0						
ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	72.3	-	MHz	
	Period	Thp	1470	1526	1586		
Hsync	Width	t _{wH}	23	32	40	tCLK	
	Width-Active	t _{wha}	1366	1366	1366		
	Period	t _{vP}	779	790	801	tHP	
Vsync	Width	t _{wv}	2	5	8		
	Width-Active	t _{wva}	768	768	768		
	Horizontal back porch	t _{HBP}	72	80	124	FCI K	
Data	Horizontal front porch	t _{HFP}	8	48	48	tCLK	
Enable	Vertical back porch	t _{vBP}	8	14	20		
	Vertical front porch	t _{vFP}	1	3	5	tHP	

Table 5. TIMING TABLE

3-5. Signal Timing Waveforms





3-6. Color Input Data Reference

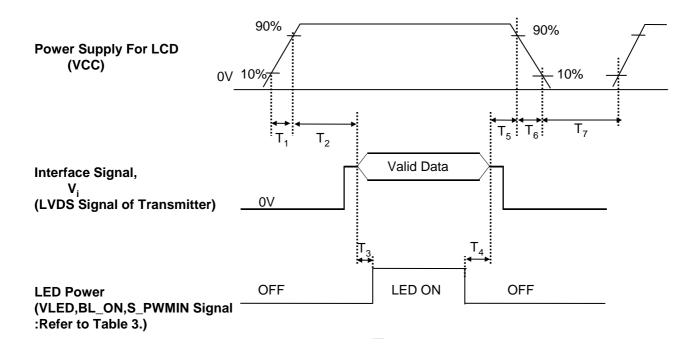
The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

									Inp	out Co	olor D	ata							
	Color			R	ED					GRE	EEN					BL	UE		
		MSE						MSE					LSB						LSB
	1	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0	0	0 	0	0	0 	0	0	0	0	0	0 	0	0	0	0	0
	Red	1 	1 	1	1 	1 1	1	0 	0	0	0	0	0	0 	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1		1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN										•••••			• • • • • •			· · · · · · · · · · · · · · · · · · ·	 		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	 1
BLUE					•••••					•••••	 					· · · · · ·	 		
	BLUE (62)	0	0	0	0	0	0	 0	0	0	0	0	0	 1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	 0	0	0	0	0	0	 1				 1	 1

Table 7. COLOR DATA REFERENCE



3-7. Power Sequence



Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T_4	200	-	-	(ms)
Т ₅	0	-	50	(ms)
T ₆	3	-	10	(ms)
T ₇	400	-	-	(ms)

Table 7. POWER SEQUENCE TABLE

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

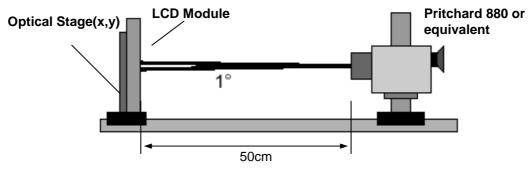


FIG. 1 Optical Characteristic Measurement Equipment and Method

			1a = 200, 000	$-5.5^{\circ}, 1^{\circ}-00$	112, ICLK-	$72.311 \text{Hz}, 1_{\text{BL}} = 20 \text{Hz}$
Deremeter	Symbol		Values		Units	Notoo
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	300	-	-		1
Surface Luminance, white	L _{WH}	170	200	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	Tr _R + Tr _D	-	16	25	ms	4
Color Coordinates					1	
RED	RX	0.564	0.594	0.624	1	
	RY	0.323	0.353	0.383		
GREEN	GX	0.297	0.327	0.357	[
	GY	0.554	0.584	0.614	[
BLUE	BX	0.121	0.151	0.181	[
	BY	0.081	0.111	0.141	[
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359	[
Viewing Angle					1	5
x axis, right(Φ =0°)	Θr	30	-		degree	
x axis, left (Φ =180°)	ΘΙ	30	-	-	degree	
y axis, up (Φ =90°)	Θu	10	-		degree	
y axis, down (Φ =270°)	Θd	20	-	-	degree	
Gray Scale			2.2			6

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 72.3MHz, I_{BL}= 20 mA



LP101WH1 Liquid Crystal Display

Product Specification

Note)

1. Contrast Ratio(CR) is defined mathematically as Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

 $L_{WH} = Average(L_1, L_2, \dots, L_5)$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

 $\delta_{\text{WHITE}} = \frac{\text{Maximum}(L_1, L_2, \dots, L_{13})}{\text{Minimum}(L_1, L_2, \dots, L_{13})}$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6.	Gray	scale	specification	
----	------	-------	---------------	--

 $f_{V} = 60$ Hz

Gray Level	Luminance [%] (Typ)
LO	0.24
L7	0.74
L15	2.57
L23	6.75
L31	15.48
L39	32.89
L47	56.63
L55	76.95
L63	100



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

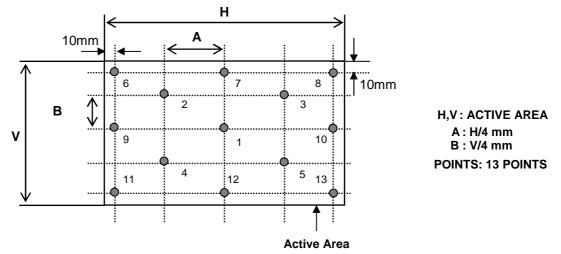
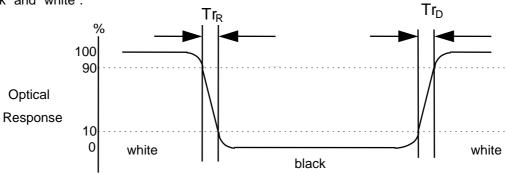


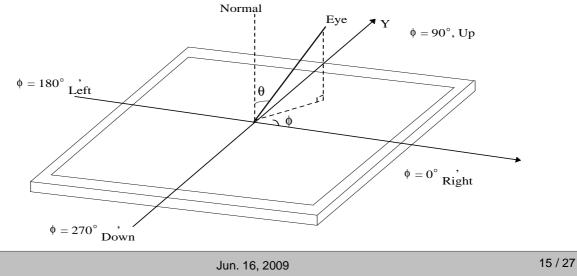
FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





Ver. 1.0

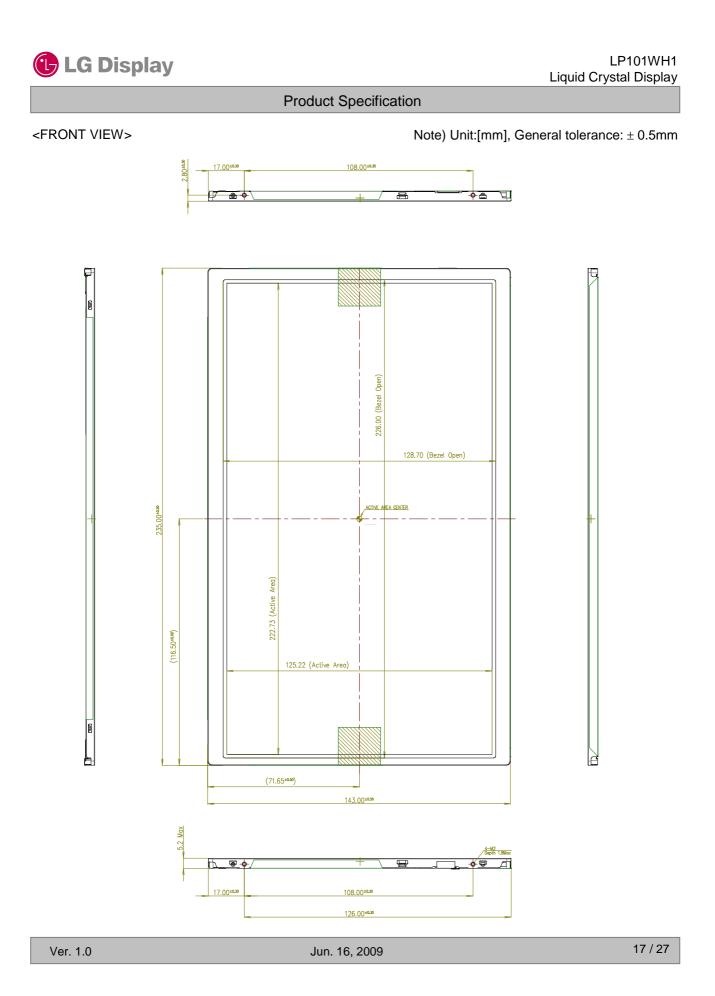




5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP101WH1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	$235.0\pm0.5~\text{mm}$		
Outline Dimension	Vertical	$143.0\pm0.5~\text{mm}$		
	Thickness	5.2mm (max)		
Bezel Area	Horizontal	226.00 mm		
Dezer Area	Vertical	128.70 mm		
Active Display Area	Horizontal	222.73 mm		
Active Display Area	Vertical	125.22 mm		
Weight	200g (Max.)			
Surface Treatment	Anti-Glare treatment of the front pola	rizer		



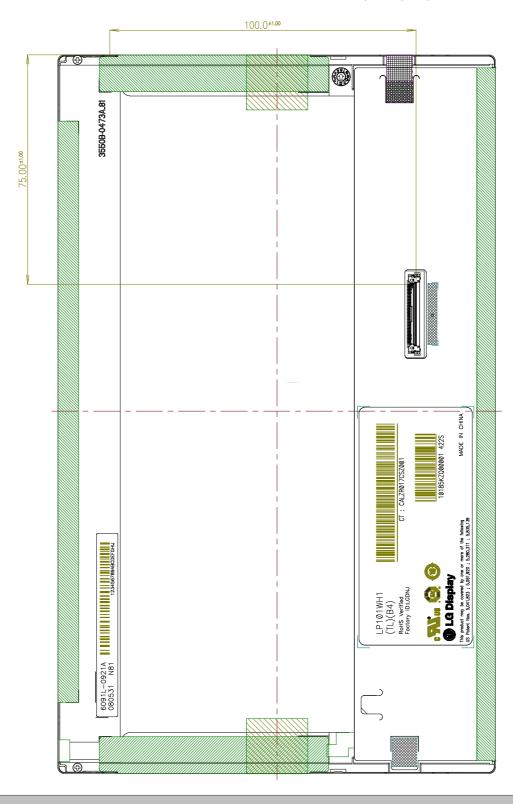


LP101WH1 Liquid Crystal Display

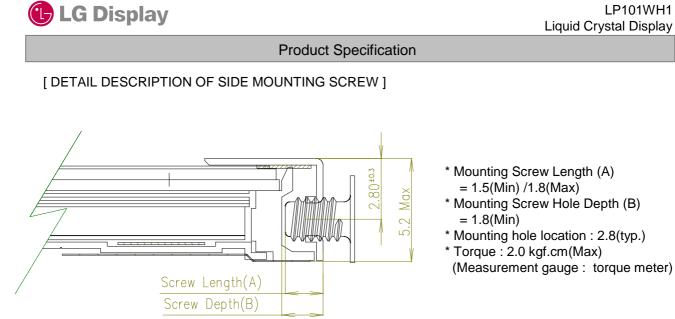
Product Specification

<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5 mm



Jun. 16, 2009



Section A-A

Notes : 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.



6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
c) EN 60950-1:2001, First Edition, European Committee for Electrotechnical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.

7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992

b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.

c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)
E : MONTH

D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 40 pcs
- b) Box Size : 395mm × 390mm × 309mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental)
- to the polarizer.)(7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.

Please carefully peel off the protection film without rubbing it against the polarizer.

- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 1/3

	Byte	Byte	Field Name and Comments	Value	Value
	(Dec) 0	(Hex) 00	Header	(Hex) 00	(Bin) 00000000
Header	1		Header	FF	11111111
	2		Header	FF	11111111
	3		Header	FF	11111111
	4		Header	FF	11111111
	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
	9	09	EISA manufacture code (Compressed ASC II)	E4	11100100
<u> </u>	10	0A	Panel Supplier Reserved - Product Code 0248h	48	01001000
Vendor / Product EDID Version	11	0B	(Hex. LSB first)	02	00000010
2 2	12	00	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
22	13 14	0D 0E	LCD Module Serial No - Preferred but Optional ("0" If not used) LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
E A	14		LCD Module Senal No - Preferred but Optional ("0" If not used) LCD Module Senal No - Preferred but Optional ("0" If not used)	00	00000000
26	16	10	Week of Manufacture 0 weeks	00	0000000
S 🖬 🛛	17	11		13	00010011
-			Year of Manufacture 2009years	01	00000001
	18	12	EDID structure version # = 1		
	19	13	EDID revision # = 3	03	00000011
5	20	14	Video input Definition = Digital signal	80	10000000
a 5	21	15	Max H image size (Rounded cm) = 22 cm	16	00010110
Display tramete	22	16	Max V image size (Rounded cm) = 13 cm	0D	00001101
	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
Display Parameters	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGE color display, Timing BLK 1,no_GTF)	0A	00001010
	25	19	Red/Green Low Bits (RxRy/GxGy)	1 E	00011110
	26	IA	Blue/White Low Bits (BxBy/WxWy)	E5	11100101
	27	1B	Red X Rx = 0.594	98	10011000
2 3	27	10		5A	01011010
10					
24	29	1D	Green X Gx = 0.327	53	01010011
28	30	lE	Green Y Gy =0.584	95	10010101
Panel Color Coordinates	31	1F	Blue X Bx = 0.151	26	00100110
	32	20	Blue Y By = 0.111	1C	00011100
	33	21	White X Wx =0.313	50	01010000
	34	22	White Y Wy=0.329	54	01010100
red	35	23	Established timing 1 (00h if not used)	00	00000000
Established Timings	36	24	Established timing 2 (00h if not used)	00	00000000
Est Ti	37		Manufacturer's timings (00h if not used)	00	00000000
	38		Standard timing ID1 (01h if not used)	01	00000001
	39	27	Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID2 (01h if not used)	01	00000001
8	41	29	Standard timing ID2 (01h if not used)	01	00000001
H.	42		Standard timing ID3 (01h if not used)	01	00000001
	43	2B	Standard timing ID3 (01h if not used)	01	00000001
	44	2C	Standard timing ID4 (01h if not used)	01	00000001
Standard Timing ID	45		Standard timing ID4 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
2	46	2E 2F	Standard timing ID5 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
da da	47 48	2F 30	Standard timing ID5 (01h if not used) Standard timing ID6 (01h if not used)	01	00000001
8	48	30	Standard timing ID6 (01h if not used) Standard timing ID6 (01h if not used)	01	00000001
- 5	49 50	31	Standard timing ID7 (01h if not used) Standard timing ID7 (01h if not used)	01	00000001
	51	32	Standard timing ID7 (01h if not used) Standard timing ID7 (01h if not used)	01	00000001
	52	33	Standard timing IDS (01h if not used)	01	00000001
	53	34	Standard timing IDs (01h if not used)	01	00000001
			herman and the form the accel	VI	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	Pixel Clock/10,000 (LSB) 69.3 MHz @ 60Hz	12	00010010
	55	37	Pixel Clock/10,000 (MSB)	1B	00011011
	56	38	Horizontal Active (lower 8 bits) 1366 Pixels	56	01010110
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 96 Pixels	60	01100000
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
~	59	3B	Vertical Avtive 768 Lines	00	00000000
Timing Descriptor #1	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
â	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	0011000
	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	0011000
	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	0010000
9	64	40	Vertical Sync Offset(Tvfp) : Sync Width (VSPW) 3 Lines : 5 Lines	35	0011010
Se -	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	0000000
	66	42	Horizontal Image Size (mm) 224 mm	EO	1110000
	67	43	Vertical Image Size (mm) 126 mm	7E	0111111
	68	44	Horizontal Image Size / Vertical Image Size	00	0000000
	69	44	Horizontal Border = 0 (Zero for Notebook LCD)	00	0000000
	70	45	Vertical Border = 0 (Zero for Notebook LCD)	00	0000000
			Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_POS), DE only note :		
	71	47	LSB is set to '1' if panel is DE-timing only. H/V can be ignored.	1B	0001101
	72	48	Flag	00	0000000
	73	49	Flag	00	0000000
	74	4A	Flag	00	0000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	0000000
	76	4C	Flag	00	0000000
2	77	4D	Descriptor Defined by manufacturer	00	0000000
** -	78	4E	Descriptor Defined by manufacturer	00	0000000
	79	4F	Descriptor Defined by manufacturer	00	0000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	0000000
S	81	51	Descriptor Defined by manufacturer	00	0000000
	82	52	Descriptor Defined by manufacturer	00	0000000
- š	83	53	Descriptor Defined by manufacturer	00	0000000
	84	54	Descriptor Defined by manufacturer	00	0000000
	85	55	Descriptor Defined by manufacturer	00	0000000
_	86	56	Descriptor Defined by manufacturer	00	0000000
	87	57	Descriptor Defined by manufacturer	00	0000000
	88	58	Descriptor Defined by manufacturer	00	0000000
	89	59	Descriptor Defined by manufacturer	00	0000000
	90	5A	Flag	00	0000000
	91	5B	Flag	00	0000000
	92	5C	Flag	00	0000000
	93	5D	Data Type Tag (ASCII String)	FE	1111111
	94	5E	Flag	00	0000000
~	95	5F	ASCII String L	4C	0100110
Timing Descriptor #3	96	60	ASCII String G	47	0100011
- S -	97	61	ASCII String	20	0010000
* F	98	62	ASCII String D	44	0100010
8 H	99 99	63	ASCII String i	69	0110100
	100	64		73	0111000
		65	ASCII String s	70	0111001
	101		ASCII String p		011000
E	102	66	ASCII String 1	6C	
	103	67	ASCII String a	61	0110000
	104	68	ASCII String y	79	0111100
_	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	0A	0000101
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	0010000
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	0010000



APPENDIX A. Enhanced Extended Display Identification Data (EEDID[™]) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #4	108	~ ~	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Monitor Name, stored as ASCII)	FC	11111100
	112	70	Flag	00	00000000
	113	71	Monitor Name, stored as ASCII L	4 C	01001100
	114	72	Monitor Name, stored as ASCII P	50	01010000
	115	73	Monitor Name, stored as ASCII 1	31	00110001
	116	74	Moritor Name, stored as ASCII 0	30	00110000
	117	75	Monitor Name, stored as ASCII 1	31	00110001
	118	76	Monitor Name, stored as ASCII W	57	01010111
	119	77	Monitor Name, stored as ASCII H	48	01001000
	120	78	Monitor Name, stored as ASCII 1	31	00110001
	121	79	Monitor Name, stored as ASCII -	2 D	00101101
	122	7A	Monitor Name, stored as ASCII T	-54	01010100
	123	7B	Monitor Name, stored as ASCII L	4C	01001100
	124	7C	Monitor Name, stored as ASCII B	42	01000010
	125	7D	Monitor Name, stored as ASCII 4	- 34 -	00110100
Checksum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	14	00010100