

PJSOT05CFN3

E.S.D. Dual Protection Diode Array

This Dual Unidirectional ESD Protector Array family have been designed to protect sensitive equipment against ESD in high speed transmission buses, operating at 5V. This dual array offers an integrated solution to protect up to 2 data lines in a unidirectional mode or, 1 data line in a bi-directional mode, in application where the board space is a premium, in our DFN 3L package version.

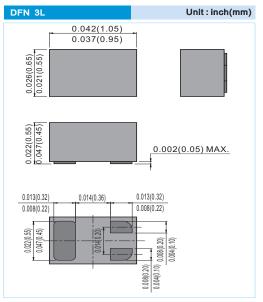
FEATURES

- IEC61000-4-2 ESD 15kV air, 8kV Contact Compliance
- Low Leakage Current, Maximum of 0.5µA at rated voltage
- Maximum Capacitance of 10pF per device at 0Vdc 1MHz
- + Peak Power Dissipation of 20W 8/20 μs Waveform
- Pin to pin compatible with standard DFN 3L
- In compliance with EU RoHS 2002/95/EC directives

MECHANICAL DATA

- Case: DFN 3L, Molded Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Marking: AF





MAXIMUM RATINGS (Per Device)

Parameter	Symbol	Value	Units
Peak Pulse Power (8/20µs Waveform)	Ррр	20	W
Peak Pulse Current (8/20µs Waveform)	Іррм	2	А
ESD Voltage (HBM Per MIL STD883C-Method 3015-6)	Vesd	20	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Тѕтс	-55 to +150	°C

ELECTRAICAL CHARACTERISTICS (Per Device)TJ=25°C

Parameter	Symbol	Test Condition	MIN.	TYP.	MAX.	Units
Reverse Stand-Off Voltage	Vwrm	-	-	-	5.0	V
Reverse Breakdown Voltage	Vbr	I br=1mA	6.2	-	7.2	V
Reverse Leakage Current	l r	V _R =5V	-	-	0.5	μA
Clamping Voltage (8/20µs)	Vc	I pp=2A	-	-	10	V
Off State Junction Capacitance (Note 1)	Сэ	0Vdc Bias f=1MHz between pin 1,2 to 3 (Gnd)	-	9	10	рF

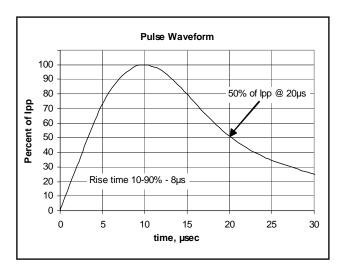
Note : 1.Capacitance between pins 1 and 2 is half of the value, in a bi-directional configuration.

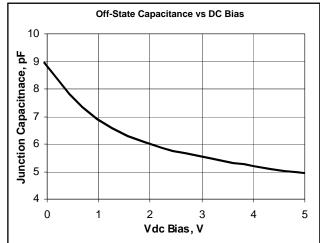
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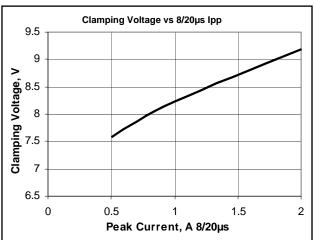


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TYPICAL CHARACTERISTIC CURVES (Per Device)TJ=25°C

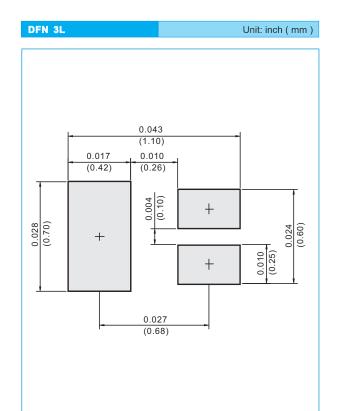








MOUNTING PAD LAYOUT



ORDER INFORMATION

· Packing information

T/R - 8K per 7" plastic Reel

LEGAL STATEMENT

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