



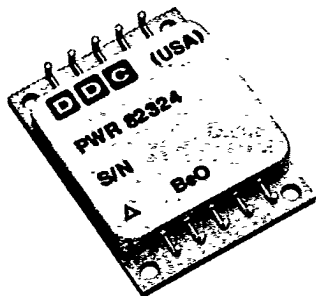
PWR-82324 SERIES

HIGH CURRENT H-BRIDGE WITH FLYBACK DIODES

T-23-07

FEATURES

- SMALL SIZE
- HIGH CURRENT CAPABILITY
- VERY LOW ON-RESISTANCE
- HIGH EFFICIENCY
- $\theta_{j-c} < 0.85^{\circ}\text{C/W}$
- FULL COMPLIANCE TO MIL-STD-883C (OPTIONAL)



DESCRIPTION

The PWR-82324 Series are H-bridge Power Hybrids featuring up to 12A output current and low channel "ON" resistance. Each hybrid contains two N-channel and two P-channel power MOSFETS connected into full H-bridge configuration and shunted by antiparallel fast recovery diodes. The PWR-82324 series is manufactured in accordance with MIL-M-38510 and MIL-STD-883C.

High thermal conductivity materials are used to ensure low junction to case thermal resistance $\theta_{j-c} < 0.85^{\circ}\text{C/W}$. Other features include high blocking voltage up to 200 volts, high switching frequency, and shunt power diodes to clamp inductive flyback. Packaged in a small case, the PWR-82324 series is an excellent choice for high power, high frequency inverters and motor drives in missiles and avionics equipment applications.

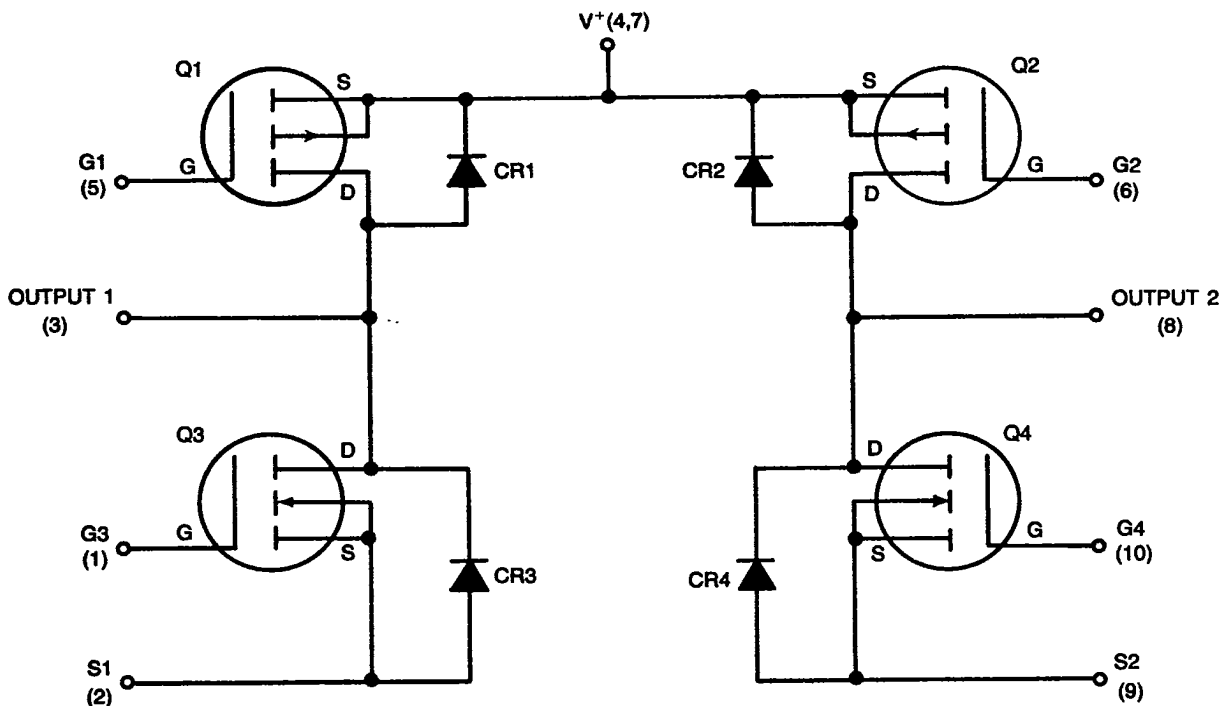


FIGURE 1. PWR-82324 SERIES SCHEMATIC

TABLE 1. PWR-82324 SERIES SPECIFICATIONS (At $T_{case} = +25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	UNITS	PWR-82324		PWR-82325		TEST CONDITIONS
			MIN	MAX	MIN	MAX	
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MOSFET TRANSISTORS¹ Maximum Gate-Source Voltage Maximum Continuous Drain Current Drain-Source Breakdown Voltage ² Gate Threshold Voltage Gate-Source Leakage Current Zero Gate Voltage Drain Current	V_{GS} I_D BV_{DSS} $V_{GS(th)}$ I_{GSS} I_{DSS}	V A V V nA μ A	100 2	± 20 12 4 ± 500 250 1000	200 2	± 20 7 4 ± 500 250 1000	$T_{case} = 100^{\circ}C$ $V_{GS} = 0V, I_D = 1mA$ $V_{DS} \geq V_{GS}, I_D = 1mA$ $V_{GS} = \pm 20V$ $V_{DS} = \text{Max. Rating}, V_{GS} = 0V$ $V_{DS} = \text{Max. Rating} \times 0.8$ $V_{GS} = 0V, T_c = +125^{\circ}C$
Static Drain-Source On-State Resistance ³	N-CH	$R_{DS(on)}$	Ω	0.1			$V_{GS} = 10V, I_D = 12A$
	P-CH			0.2			
	N-CH			0.18			
	P-CH			0.6			
Input Capacitance	N-CH	C_{iss}	pF	1600		1600	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$
	P-CH			1300		1300	
Output Capacitance	N-CH	C_{oss}	pF	800		750	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$
	P-CH			700		450	
Turn-On Delay Time		$t_{d(on)}$	ns	30		30	$V_{DD} = 24V, I_D = 10A, Z_0 = 4.7\Omega$
Rise Time	N-CH	t_r	ns	60		60	
	P-CH			15		15	
Turn-Off Delay Time	N-CH	$t_{d(off)}$	ns	80		80	
	P-CH			20		20	
Fall Time	N-CH	t_f	ns	30		60	
	P-CH			12		12	
DIODES¹ Repetitive Peak Reverse Voltage Average DC Current Reverse Leakage Current Instantaneous Forward Voltage Drop ³ Reverse Recovery Time	V_{RR} I_O I_R V_F t_{rr}	V A μ A mA V ns		100 12 10 1.0 0.9 50		200 7 10 1.0 0.8 50	$T_c = +100^{\circ}C$ $T_j = +25^{\circ}C$ $T_j = +125^{\circ}C$ $I_{peak} = 12A$ $I_{peak} = 7A$ $I_F = 1A, I_R = 1A$
THERMAL Thermal Resistance Junction to Case Maximum Temperature For Lead Soldering Maximum Junction Temperature Range Maximum Case Temperature Range Operating Storage	R_{θ_j-c} T_S T_j T_{∞} T_{cs}	$^{\circ}C/W$ $^{\circ}C$ $^{\circ}C$ $^{\circ}C$ $^{\circ}C$		0.85 250 -55 to +150 -55 to +125 -55 to +150		0.85 250 -55 to +150 -55 to +125 -55 to +150	$1/8"$ from case for 5 seconds.

Notes:
 1. Data applies to each separate die. 2. $T_j = +25^{\circ}C$ to $+150^{\circ}C$. 3. Pulse Test: Pulse Width $< 300\mu s$, Duty Cycle $\leq 2\%$.



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ORDERING INFORMATION

PWR-8232X - 1 1 0

Reliability Grade:

- 0 = Standard DDC procedures.
- 1 = Fully compliant with MIL-STD-883.
- 2 = Screened to MIL-STD-883 but without QCI testing.

Configuration

- 4 = 100V N/P-Channel H-Bridge
- 5 = 200V N/P-Channel H-Bridge

TABLE 2. PWR-82324 PIN FUNCTIONS			
PIN	FUNCTION	PIN	FUNCTION
1	G3	6	G2
2	S1	7	V+
3	OUTPUT 1	8	OUTPUT 2
4	V+	9	S2
5	G1	10	G4

Dimensions in inches (mm)

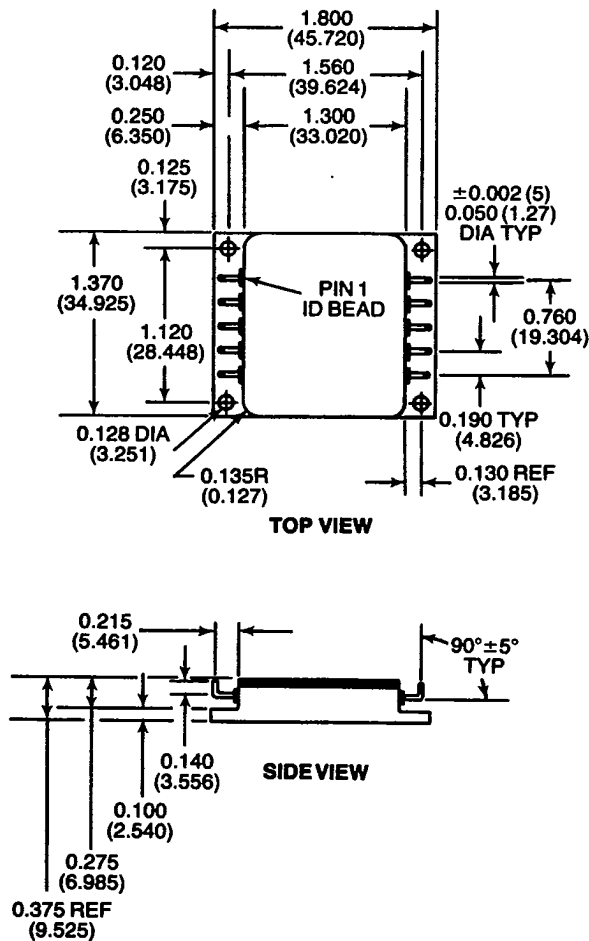


FIGURE 2. PWR-82324 SERIES MECHANICAL OUTLINE

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