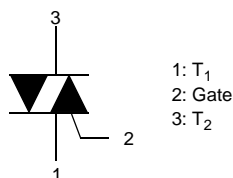
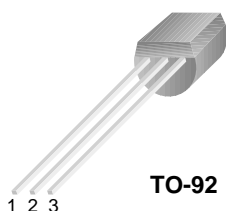


FKN2L60

Application Explanation

- Switching mode power supply, light dimmer, electric flasher unit, hair drier
- TV sets, stereo, refrigerator, washing machine
- Electric blanket, solenoid driver, small motor control
- Photo copier, electric tool



Bi-Directional Triode Thyristor Planar Silicon

Absolute Maximum Ratings $T_C=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Rating | Units |
|------------------|---|--------|-------|
| V_{DRM} | Repetitive Peak Off-State Voltage ^(Note 1) | 600 | V |

| Symbol | Parameter | Conditions | Rating | Units | |
|---------------------|---|---|------------|------------------------|---|
| $I_{\text{T(RMS)}}$ | RMS On-State Current | Commercial frequency, sine full wave 360° conduction, $T_C=65^\circ\text{C}$ | 1.5 | A | |
| I_{TSM} | Surge On-State Current | Sinewave 1 full cycle, peak value, non-repetitive | 50Hz | 9 | A |
| | | | 60Hz | 10 | A |
| I^2t | I^2t for Fusing | Value corresponding to 1 cycle of halfwave, surge on-state current, $t_p=10\text{ms}$ | 0.4 | A^2s | |
| di/dt | Critical Rate of Rise of On-State Current | $I_G = 2x I_{\text{GT}}$, $t_r \leq 100\text{ns}$ | 50 | $\text{A}/\mu\text{s}$ | |
| P_{GM} | Peak Gate Power Dissipation | | 1 | W | |
| $P_{\text{G(AV)}}$ | Average Gate Power Dissipation | | 0.1 | W | |
| V_{GM} | Peak Gate Voltage | | 6 | V | |
| I_{GM} | Peak Gate Current | | 0.5 | A | |
| T_J | Junction Temperature | | - 40 ~ 125 | $^\circ\text{C}$ | |
| T_{STG} | Storage Temperature | | - 40 ~ 125 | $^\circ\text{C}$ | |

Thermal Characteristic

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|----------------------|--------------------|--------------------------------------|------|------|------|---------------------------|
| $R_{\text{th(J-C)}}$ | Thermal Resistance | Junction to case ^(Note 4) | - | - | 40 | $^\circ\text{C}/\text{W}$ |

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

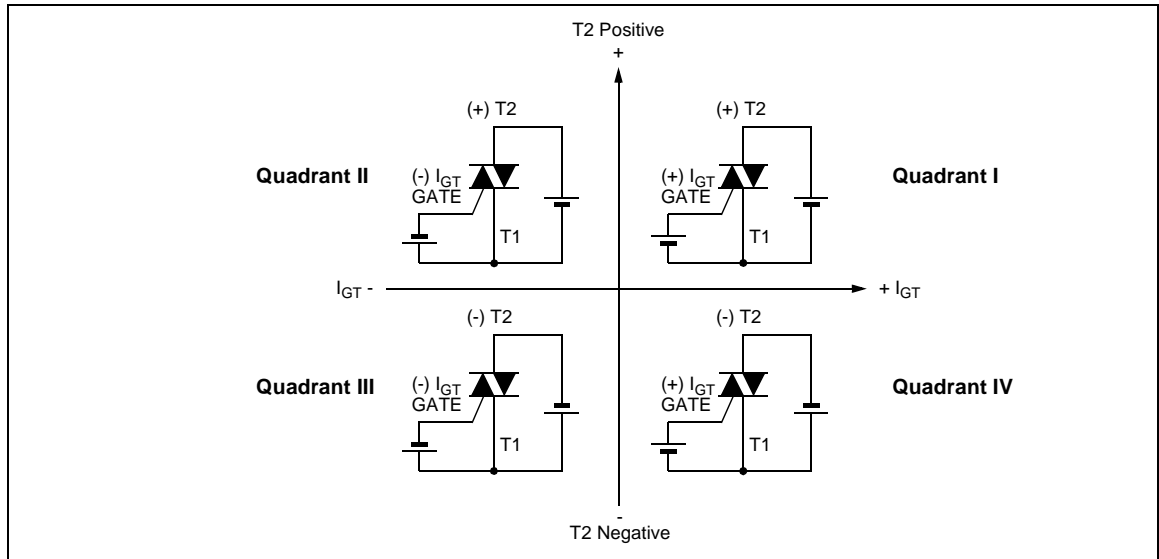
| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units | |
|-------------|---|--|-----------------|------|------|------------------|----|
| I_{DRM} | Repetitive Peak Off-State Current | V_{DRM} applied | - | - | 20 | μA | |
| V_{TM} | On-State Voltage | $T_C=25^\circ\text{C}$, $I_{TM}=3\text{A}$ Instantaneous measurement | - | - | 1.6 | V | |
| V_{GT} | Gate Trigger Voltage (Note 2) | $V_D=12\text{V}$, $R_L=20\Omega$ | T2(+), Gate (+) | - | - | 1.5 | V |
| | | | T2(+), Gate (-) | - | - | 1.5 | V |
| | | | T2(-), Gate (-) | - | - | 1.5 | V |
| I_{GT} | Gate Trigger Current (Note 2) | $V_D=12\text{V}$, $R_L=20\Omega$ | T2(+), Gate (+) | - | - | 5 | mA |
| | | | T2(+), Gate (-) | - | - | 5 | mA |
| | | | T2(-), Gate (-) | - | - | 5 | mA |
| V_{GD} | Gate Non-Trigger Voltage | $T_J=125^\circ\text{C}$, $V_D=1/2V_{DRM}$ | 0.2 | - | - | V | |
| I_H | Holding Current | $V_D=12\text{V}$, $I_{TM}=1\text{A}$ | - | - | 10 | mA | |
| I_L | Latching Current | $V_D=12\text{V}$, $I_G=1.2I_{GT}$ | I, III | - | - | 10 | mA |
| | | | II | - | - | 10 | mA |
| dv/dt | Critical Rate of Rise of Off-State Voltage | $V_{DRM} = \text{Rated}$, $T_J = 125^\circ\text{C}$, Exponential Rise | 500 | - | - | V/ μs | |
| $(dv/dt)_C$ | Critical-Rate of Rise of Off-State Commutating Voltage (Note 3) | | 5 | - | - | V/ μs | |

Notes:

- Gate Open
- Measurement using the gate trigger characteristics measurement circuit
- The critical-rate of rise of the off-state commutating voltage is shown in the table below
- Case temperature is measured at the T2 terminal 1.5mm away from the molded case.

| V_{DRM} (V) | Test Condition | Commutating voltage and current waveforms (inductive load) |
|---------------|--|--|
| FKN2L60 | 1. Junction Temperature $T_J=125^\circ\text{C}$ 2. Rate of decay of on-state commutating current $(di/dt)_C = -0.5\text{A/ms}$ 3. Peak off-state voltage $V_D = 400\text{V}$ | |

Quadrant Definitions for a Triac



Typical Curves

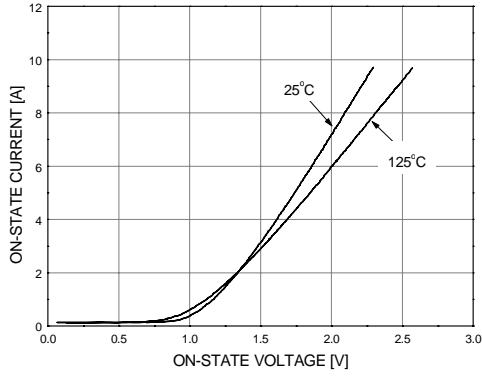


Figure 1. Maximum On-state Characteristics

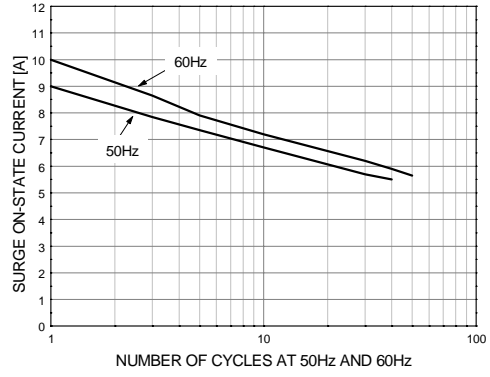


Figure 2. Rated Surge On-state Current

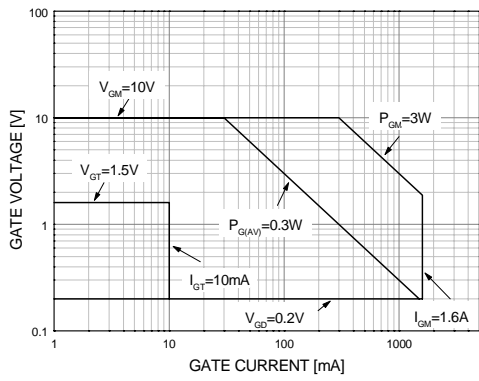


Figure 3. Gate Characteristics

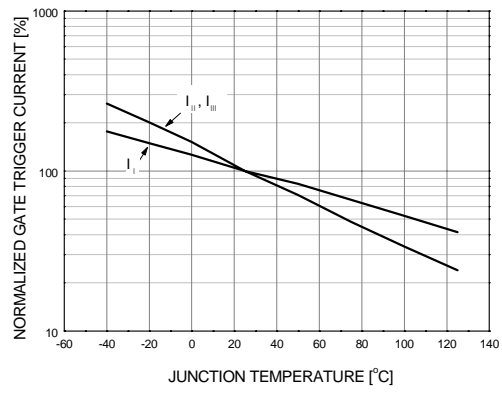


Figure 4. Gate Trigger Current vs Tj

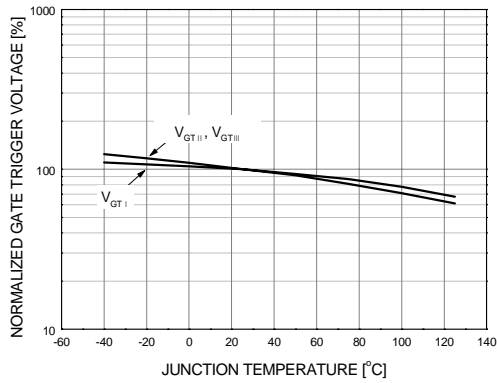


Figure 5. Gate Trigger Voltage vs Tj

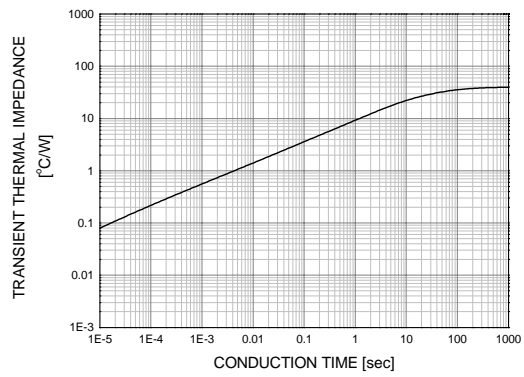


Figure 6. Transient Thermal Impedance

Typical Curves (Continues)

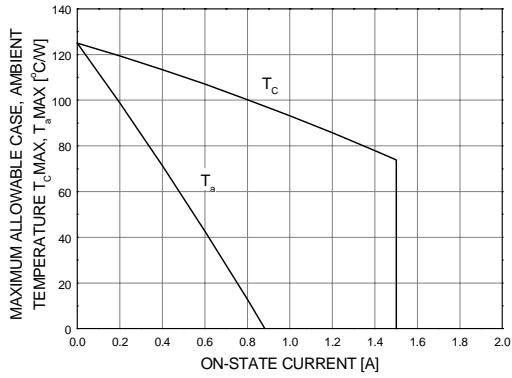


Figure 7. Allowable Case, Ambient Temperature vs Rms On-state Current

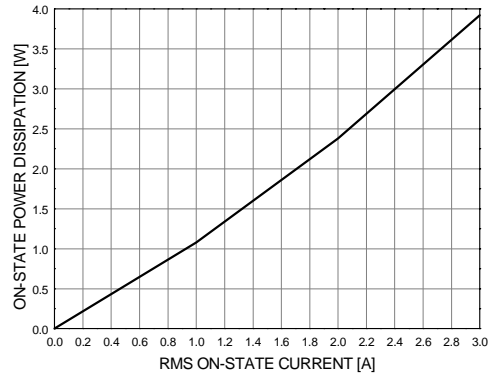


Figure 8. Maximum On-state Power Dissipation

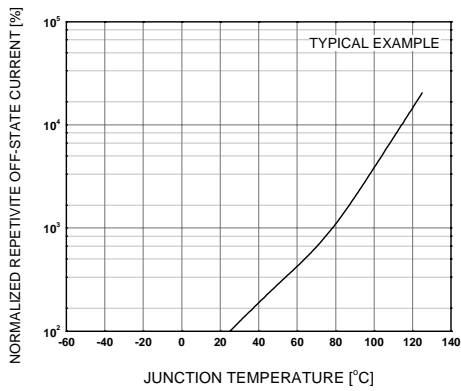


Figure 9. Repetitive Peak Off-state Current vs Junction Temperature

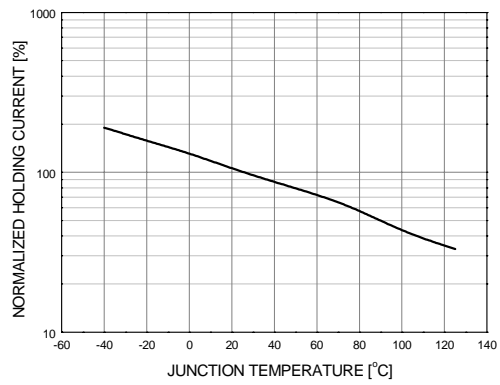


Figure 10. Holding Current vs Junction Temperature

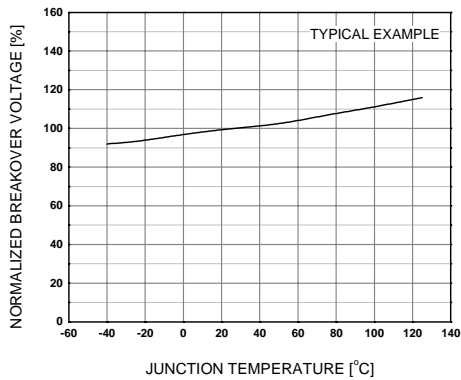


Figure 11. Breakover Voltage vs Junction Temperature

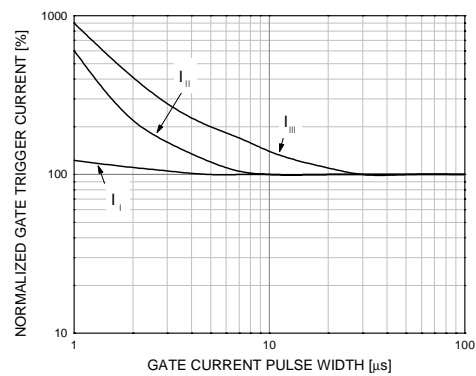


Figure 12. Gate Trigger Current vs Gate Current Pulse Width

Typical Curves (Continues)

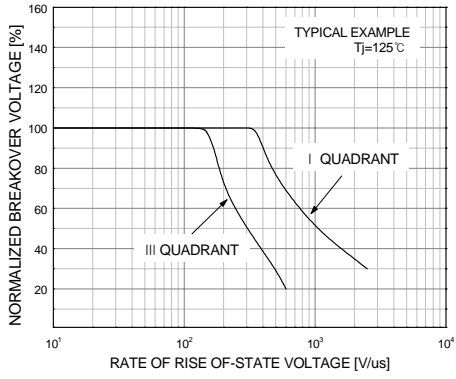
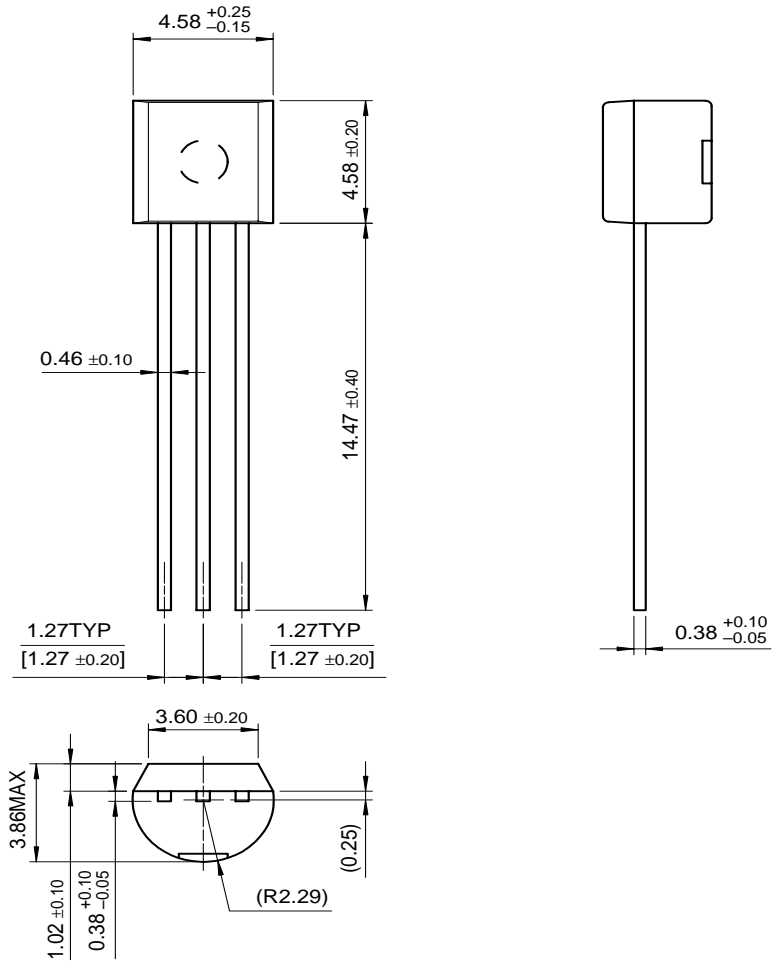


Figure 13. Breakover Voltage vs Rate of Rise of Off-state Voltage

Package Dimension

FKN2L60

TO-92



Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

| | | | | |
|--------------------------------------|---------------------|--------------------|---------------------|-----------------|
| ACEx™ | FACT Quiet Series™ | ImpliedDisconnect™ | PACMAN™ | SPM™ |
| ActiveArray™ | FAST® | ISOPLANAR™ | POP™ | Stealth™ |
| Bottomless™ | FASTr™ | LittleFET™ | Power247™ | SuperFET™ |
| CoolFET™ | FPST™ | MICROCOUPLER™ | PowerSaver™ | SuperSOT™-3 |
| CROSSVOLT™ | FRFET™ | MicroFET™ | PowerTrench® | SuperSOT™-6 |
| DOMET™ | GlobalOptoisolator™ | MicroPak™ | QFET® | SuperSOT™-8 |
| EcoSPARK™ | GTO™ | MICROWIRE™ | QS™ | SyncFET™ |
| E ² C MOS™ | HiSeC™ | MSX™ | QT Optoelectronics™ | TinyLogic® |
| EnSigna™ | µC™ | MSXPro™ | Quiet Series™ | TINYOPTO™ |
| FACT™ | i-Lo™ | OCX™ | RapidConfigure™ | TruTranslation™ |
| Across the board. Around the world.™ | OCXPro™ | RapidConnect™ | UHC™ | |
| The Power Franchise® | OPTOLOGIC® | SILENT SWITCHER® | UltraFET® | |
| Programmable Active Droop™ | OPTOPLANAR™ | SMART START™ | VCX™ | |

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |

Home >> Find products >>

FKN2L60

Bi-Directional Triode Thyristor Planar Silicon

Contents

- [Applications](#)
- [Product status/pricing/packaging](#)
- [Order Samples](#)
- [Qualification Support](#)

Applications

- Switching mode power supply, light dimmer, electric flasher unit, hair drier
- TV sets, stereo, refrigerator, washing machine
- Electric blanket, solenoid driver, small motor control
- Photo copier, electric tool

[back to top](#)

Product status/pricing/packaging

BUY

BUY

Datasheet

[Download this datasheet](#)



[e-mail this datasheet](#)



This page

[Print version](#)

Related Links

[Request samples](#)

[How to order products](#)

[Product Change Notices \(PCNs\)](#)

[Support](#)

[Sales support](#)

[Quality and reliability](#)

[Design center](#)

| Product | Product status | Pb-free Status | Pricing* | Package type | Leads | Packing method | Package Marking Convention** |
|------------|-----------------|-----------------|----------|-----------------------|-------|----------------|------------------------------|
| FKN2L60BU | Full Production | Full Production | \$0.168 | TO-92 | 3 | BULK | Line 1: N2L80 |
| FKN2L60FBU | Full Production | Full Production | \$0.168 | TO-92 | 3 | BULK | N/A |

* Fairchild 1,000 piece Budgetary Pricing

** A sample button will appear if the part is available through Fairchild's on-line samples program. If there is no sample button, please contact a [Fairchild distributor](#) to obtain samples



Indicates product with Pb-free second-level interconnect. For more information [click here](#).

Package marking information for product FKN2L60 is available. [Click here for more information](#).

[back to top](#)

Qualification Support

Click on a product for detailed qualification data

| Product |
|----------------------------|
| FKN2L60BU |
| FKN2L60FBU |

[back to top](#)

© 2007 Fairchild Semiconductor



[Products](#) | [Design Center](#) | [Support](#) | [Company News](#) | [Investors](#) | [My Fairchild](#) | [Contact Us](#) | [Site Index](#) | [Privacy Policy](#) | [Site Terms & Conditions](#) | [Standard Terms & Conditions](#) | [Contact Us](#)