



A Product Line of Diodes Incorporated



DMN4030LK3

**40V N-CHANNEL ENHANCEMENT MODE MOSFET** 

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C		
40V	30mΩ @ V <sub>GS</sub> = 10V	13.7A		
400	50mΩ @ V <sub>GS</sub> = 4.5V	10.6A		

## **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- DC-DC Converters
- Power management functions

#### **Features and Benefits**

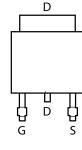
- Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)

### **Mechanical Data**

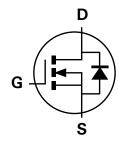
- Case: TO252-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Below
- Ordering Information: See Below
- Weight: 0.33 grams (approximate)



TOP VIEW



PIN OUT -TOP VIEW



Equivalent Circuit

#### Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel	
DMN4030LK3-13	N4030L	13	16	2,500	

Note: 1. Diodes, Inc. defines "Green" products as those which are Eu RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.

### **Marking Information**



>\\\ = Manufacturer's Marking N4030L = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 09 = 2009) WW = Week (01-52)





## Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Cha	acteristic		Symbol Value		Unit
Drain-Source voltage			V <sub>DSS</sub>	40	V
Gate-Source voltage			V <sub>GS</sub>	±20	V
		(Note 3)	ID	13.7	
Continuous Drain current	$V_{GS} = 10V$	T <sub>A</sub> =70°C (Note 3)		10.9	A
		(Note 2)		9.4	
Pulsed Drain current	V <sub>GS</sub> = 10V	(Note 4)	IDM	37.7	A
Continuous Source current (Body diode) (Note 3)		Is	10.7	А	
Pulsed Source current (Body diode) (Note 4)		I <sub>SM</sub>	37.7	А	

#### Thermal Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 2)		4.18 33.4		
Power dissipation Linear derating factor	(Note 3)	PD	8.9 71.4	W mW/°C	
	(Note 5)		2.14 17.1		
Thermal Resistance, Junction to Ambient	(Note 2) (Note 3) (Note 5)	R <sub>θJA</sub>	29.9 14.0 58.4	°C/W	
Thermal Resistance, Junction to Lead	(Note 6)	R <sub>θJL</sub>	2.46		
Operating and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	°C	

2. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is Notes: measured when operating in a steady-state condition.

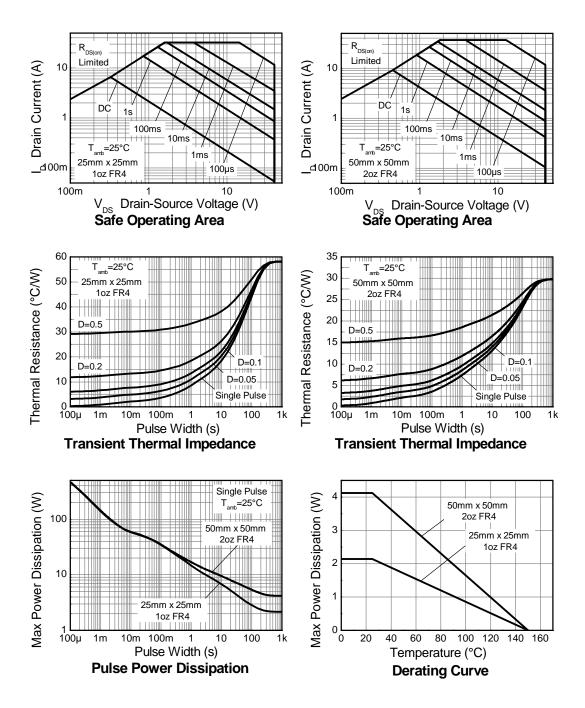
3. Same as note 2, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.

5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

6. Thermal resistance from junction to solder-point (at the end of the drain lead).



## **Thermal Characteristics**







# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

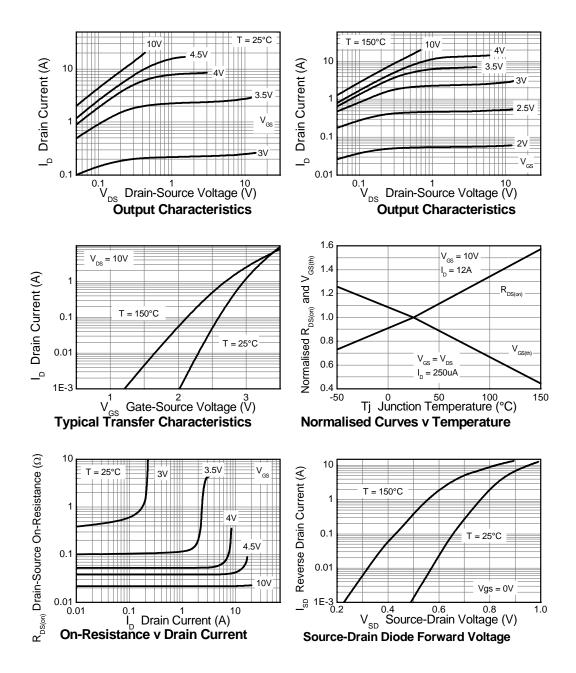
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS			•	•	•	•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	0.5	μA	$V_{DS}$ = 40V, $V_{GS}$	= 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS}$ = ±20V, $V_{D}$	<sub>S</sub> = 0V	
ON CHARACTERISTICS								
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0		3.0	V	$I_{D}$ = 250µA, V <sub>DS</sub>	= V <sub>GS</sub>	
Static Drain-Source On-Resistance (Note 7)	<b>D</b>			0.030	Ω	$V_{GS}$ = 10V, $I_{D}$ =	12A	
	R <sub>DS (ON)</sub>			0.050	52	$V_{GS}$ = 4.5V, $I_{D}$ =	6A	
Forward Transconductance (Notes 7 & 8)	<b>g</b> fs	_	22.8	—	S	$V_{DS}$ = 15V, $I_{D}$ =	12A	
Diode Forward Voltage (Note 7)	V <sub>SD</sub>	_	0.95	1.1	V	I <sub>S</sub> = 12A, V <sub>GS</sub> = 0V		
Reverse recovery time (Note 8)	t <sub>rr</sub>		135	—	ns	-I <sub>S</sub> = 12A, di/dt= 100A/μs		
Reverse recovery charge (Note 8)	Q <sub>rr</sub>	_	799	_	nC			
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C <sub>iss</sub>	_	604	_	pF			
Output Capacitance	C <sub>oss</sub>	_	106	_	pF	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V – f= 1MHz		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	59.6	_	pF			
Total Gate Charge	Qg	_	6.5		nC	V <sub>GS</sub> = 4.5V		
Total Gate Charge	Qg	_	12.9		nC		V <sub>DS</sub> = 20V	
Gate-Source Charge	Q <sub>gs</sub>	_	2.3		nC	V <sub>GS</sub> = 10V	I <sub>D</sub> = 12A	
Gate-Drain Charge	Q <sub>gd</sub>	_	3.6		nC			
Turn-On Delay Time (Note 9)	t <sub>D(on)</sub>		4.2	_	ns			
Turn-On Rise Time (Note 9)	tr		12.4	_	ns	V <sub>DD</sub> = 20V, V <sub>GS</sub> = 10V		
Turn-Off Delay Time (Note 9)	t <sub>D(off)</sub>		13.8	_	ns	I <sub>D</sub> = 12A, R <sub>G</sub> ≅ 6.0Ω		
Turn-Off Fall Time (Note 9)	t <sub>f</sub>		10.7	_	ns	1		

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.

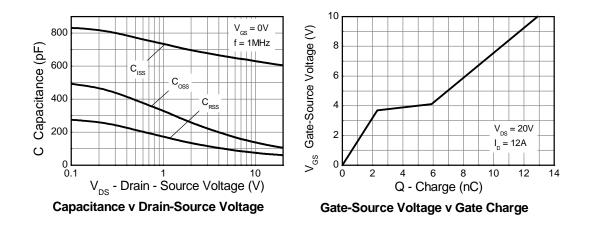


# **Typical Characteristics**

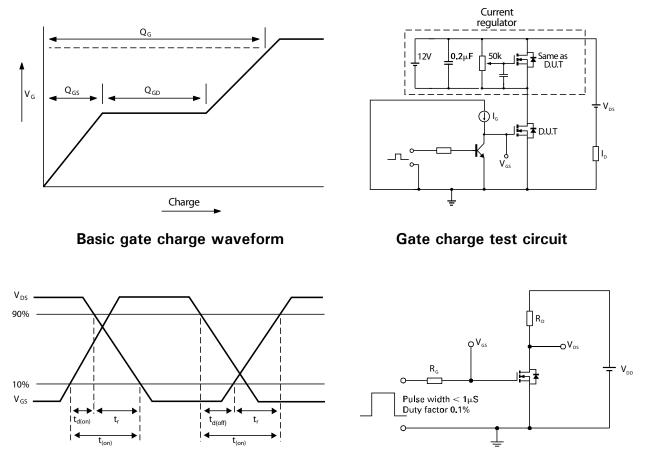




# **Typical Characteristics - continued**



**Test Circuits** 



Switching time waveforms

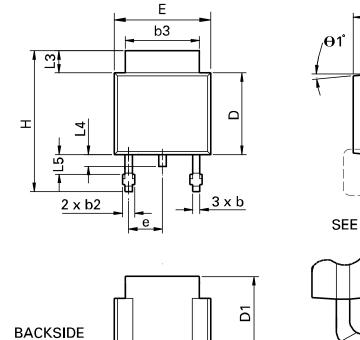
Switching time test circuit



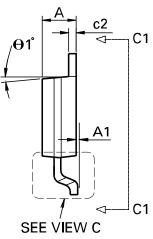
DMN4030LK3

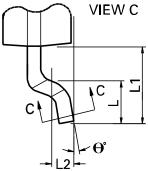
## **Package Outline Dimensions**

VIEW (C1--C1)



h



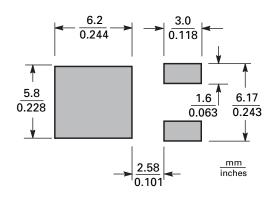


DIM	Inc	hes	Millin	neters	DIM	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
А	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
A1	-	0.005	-	0.127	н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
с	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	θ1°	0°	10°	0°	10°
E	0.250	0.265	6.35	6.73	θ°	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	_	-	-	-



DMN4030LK3

## Suggested Pad Layout



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