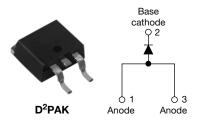


Vishay High Power Products

Input Rectifier Diode, 25 A



PRODUCT SUMMARY				
V _F at 10 A	< 1 V			
I _{FSM}	300 A			
V _{RRM}	800 V to 1200 V			

DESCRIPTION/FEATURES

The VS-25ETS..SPbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.



FREE

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

- Compliant to RoHS directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition
- Designed and qualified for industrial level

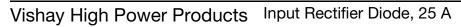
OUTPUT CURRENT IN TYPICAL APPLICATIONS					
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS					
Capacitive input filter $T_A = 55$ °C, $T_J = 125$ °C common heatsink of 1 °C/W 20 23 A					

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Sinusoidal waveform	25	A			
V _{RRM}		800 to 1200	V			
I _{FSM}		300	A			
V _F	10 A, T _J = 25 °C	1.0	V			
TJ		- 40 to 150	°C			

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
VS-25ETS08SPbF	800	900					
VS-25ETS10SPbF	1000	1100	1				
VS-25ETS12SPbF	1200	1300					

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	T _C = 106 °C, 180° conduction half sine wave	25			
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	250	A		
non-repetitive surge current		10 ms sine pulse, no voltage reapplied	300			
Maximum 12t fau fuain a	I ² t	10 ms sine pulse, rated V _{RRM} applied	316	A ² s		
Maximum I ² t for fusing	1-1	10 ms sine pulse, no voltage reapplied	442	A-S		
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	4420	A²√s		

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CON	IDITIONS	VALUES	UNITS		
Maximum forward voltage drop	V _{FM}	25 A, T _J = 25 °C		1.14	V		
Forward slope resistance	r _t	T 150 °C		9.62	mΩ		
Threshold voltage	V _{F(TO)}	T _J = 150 °C		0.87	V		
Maximum reverse leakage current	l	T _J = 25 °C	V _B = Rated V _{BBM}	0.1	mA		
Maximum reverse leakage current	I _{RM}	T _J = 150 °C	VR = hateu VRRM	1.0			

THERMAL - MECHAN	ICAL SPE	CIFICATI	ons			
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 150	°C	
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.9		
Maximum thermal resistance, junction to ambient		R _{thJA}		62	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.5		
Annyayimata wajaht				2	g	
Approximate weight				0.07	OZ.	
Manustina taurus	minimum			6 (5)	kgf ⋅ cm	
Mounting torque — maxim				12 (10)	(lbf · in)	
Marking device				25ETS08S		
			Case style D ² PAK (SMD-220)		S10S	
				25ET	S12S	

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Input Rectifier Diode, 25 A Vishay High Power Products

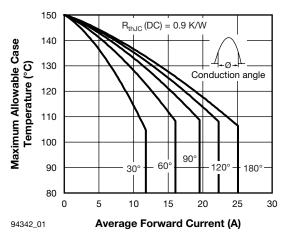


Fig. 1 - Current Rating Characteristics

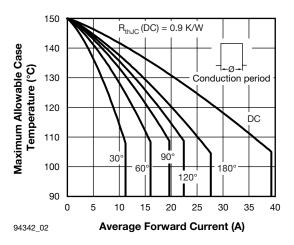


Fig. 2 - Current Rating Characteristics

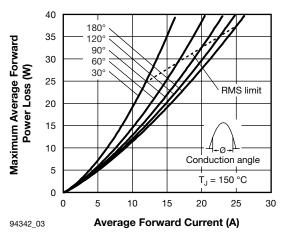


Fig. 3 - Forward Power Loss Characteristics

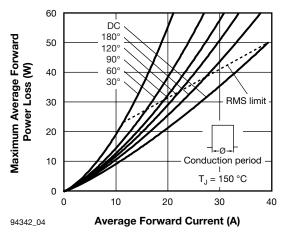


Fig. 4 - Forward Power Loss Characteristics

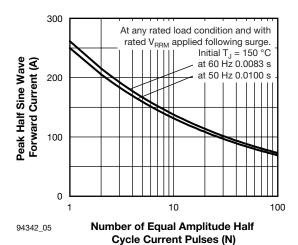


Fig. 5 - Maximum Non-Repetitive Surge Current

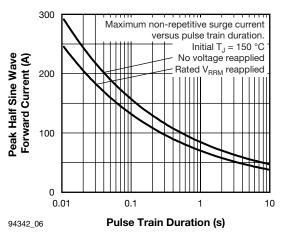


Fig. 6 - Maximum Non-Repetitive Surge Current

Vishay High Power Products Input Rectifier Diode, 25 A



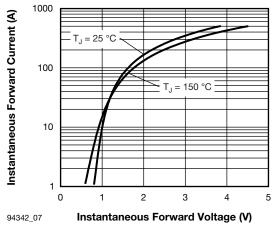


Fig. 7 - Forward Voltage Drop Characteristics

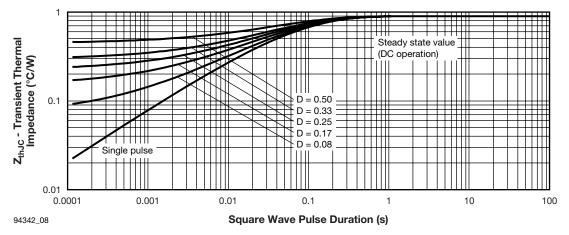


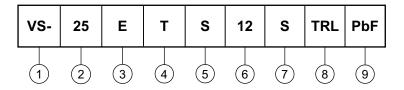
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Input Rectifier Diode, 25 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



HPP product suffix

Current rating (25 = 25 A)

Circuit configuration

E = Single diode

4 Package:

T = TO-220AC

5 Type of silicon:

S = Standard recovery rectifier

08 = 800 V

Voltage code x 100 = V_{RRM}

10 = 1000 V $S = TO-220 D^2PAK (SMD-220) version$

12 = 1200 V

• None = Tube

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

9 PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95046			
Part marking information	www.vishay.com/doc?95054			
Packaging information	www.vishay.com/doc?95032			

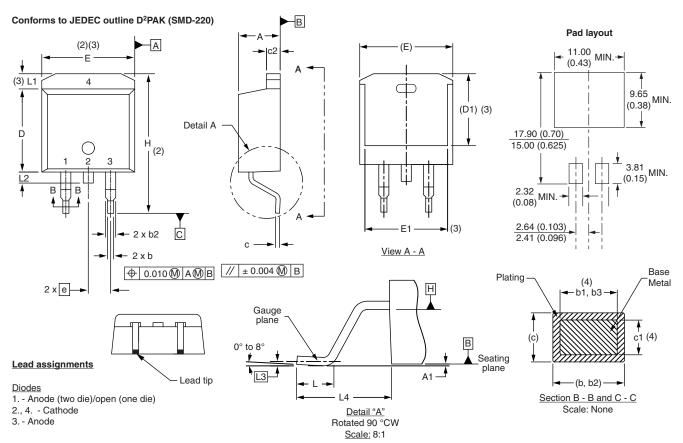
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Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	NOTES	
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54 BSC		0.100 BSC		
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	1	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25 BSC		0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- $^{(1)}$ Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC outline TO-263AB





Vishay

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