





EH47 20 ET T TS -5.000M

Series

RoHS Compliant (Pb-free) 2.5V 4 Pad 2.5mm x 3.2mm
Ceramic SMD LVCMOS Oscillator

Frequency Tolerance/Stability — ±20ppm Maximum

Operating Temperature Range – -40°C to +85°C Nominal Frequency 5.000MHz

Pin 1 Connection
Tri-State (High Impedance)

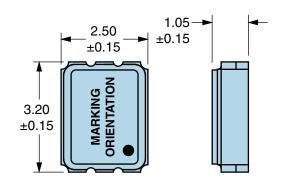
- Duty Cycle 50 ±5(%)

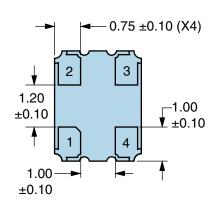
Operating Topologing at 25°C ±5ppm/Year ±5ppm/Year 40°C to +85 pply Voltage 2.5Vdc ±5% out Current 6mA Maxim thut Voltage Logic High (Voh) 90% of Vdd thut Voltage Logic Low (Vol) 10% of Vdd se/Fall Time 6nSec Maxim ty Cycle 50 ±5(%) (Maxim thut Logic Type CMOS 1 Connection 7ri-State (High Voltage Connection 1 Tri-State (High Voltage Connectio	
Operating To 260°C Reflower Renating Temperature Range   pply Voltage   Out Current   Out Voltage Logic High (Voh)   Out Voltage Logic Low (Vol)   Out Current   Out Voltage Logic Low (Vol)   Out Voltage Logic Low (Vol)   Out Current   Out C	emperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°, v, Shock, and Vibration)  Maximum  °C  Im (No Load)
pply Voltage 2.5Vdc ±5% but Current 6mA Maxim titput Voltage Logic High (Voh) 90% of Vdd titput Voltage Logic Low (Vol) 10% of Vdd se/Fall Time 6nSec Maxim ty Cycle 30 ±5(%) (M ad Drive Capability 15pF Maxim ttput Logic Type 11 Connection -State Input Voltage (Vih and Vil) 90% of Vdd	°C ım (No Load)
pply Voltage  2.5Vdc ±5%  6mA Maxim  titput Voltage Logic High (Voh)  90% of Vdd  titput Voltage Logic Low (Vol)  10% of Vdd  6nSec Maxim  ty Cycle  50 ±5(%) (M  ad Drive Capability  15pF Maxim  ttput Logic Type  CMOS  1 Connection  Tri-State (Hi  -State Input Voltage (Vih and Vil)	im (No Load)
tout Current  formal Maxim  tout Current  formal Maxim  10 90% of Vdd  10% of Vdd	
trut Voltage Logic High (Voh)  10% of Vdd  10% of Vdd  10% of Vdd  10% of Vdd  6nSec Maxin  15pF Maxim  15pt Maxim  15pt Logic Type  1 Connection  Tri-State (Hi  -State Input Voltage (Vih and Vil)	
triput Voltage Logic Low (Vol)  10% of Vdd  6nSec Maxin  ty Cycle  50 ±5(%) (M  ad Drive Capability  15pF Maxim  triput Logic Type  CMOS  1 Connection  Tri-State (Hi  -State Input Voltage (Vih and Vil)	Vlinimum (IOH = -8mA)
se/Fall Time 6nSec Maxim ty Cycle 50 ±5(%) (M ad Drive Capability 15pF Maxim tput Logic Type CMOS 1 1 Connection -State Input Voltage (Vih and Vil) 90% of Vdd	
ty Cycle 50 ±5(%) (Maximum to the content of the co	Maximum (IOL = +8mA)
ad Drive Capability  15pF Maxim  ttput Logic Type  CMOS  11 Connection  Tri-State (Hi  -State Input Voltage (Vih and Vil)  90% of Vdd	num (Measured at 20% to 80% of waveform)
tput Logic Type CMOS  1 Connection Tri-State (Hi -State Input Voltage (Vih and Vil) 90% of Vdd	easured at 50% of waveform)
n 1 Connection Tri-State (Hi -State Input Voltage (Vih and Vil) 90% of Vdd	um
-State Input Voltage (Vih and Vil) 90% of Vdd	
,	gh Impedance)
	Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High
andby Current 10µA Maxim	um (Pin 1 = Ground)
solute Clock Jitter ±100pSec N	
art Up Time 10mSec Ma	aximum
orage Temperature Range -55°C to +12	

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS		
ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500Vdc	
Fine Leak Test	MIL-STD-883, Method 1014, Condition A	
Flammability	UL94-V0	
Gross Leak Test	MIL-STD-883, Method 1014, Condition C	
Mechanical Shock	MIL-STD-883, Method 2002, Condition B	
Moisture Resistance	MIL-STD-883, Method 1004	
Moisture Sensitivity	J-STD-020, MSL 1	
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K	
Resistance to Solvents	MIL-STD-202, Method 215	
Solderability	MIL-STD-883, Method 2003	
Temperature Cycling	MIL-STD-883, Method 1010, Condition B	
Vibration	MIL-STD-883, Method 2007, Condition A	



### **MECHANICAL DIMENSIONS (all dimensions in millimeters)**



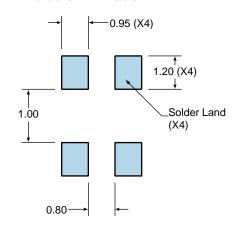


PIN	CONNECTION
1	Tri-State
2	Case Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	EPO
_	XXXXX XXXXX=Ecliptek Manufacturing Identifier

#### **Suggested Solder Pad Layout**

All Dimensions in Millimeters



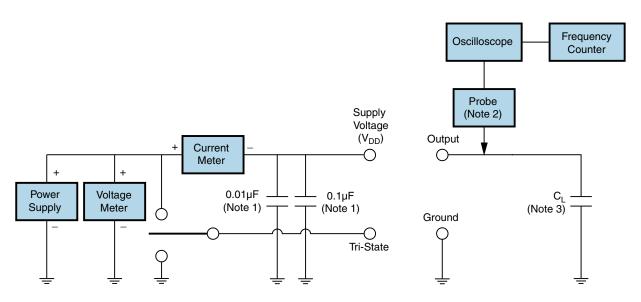
All Tolerances are ±0.1



#### **OUTPUT WAVEFORM & TIMING DIAGRAM**



#### **Test Circuit for CMOS Output**



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.
- Note 3: Capacitance value C<sub>1</sub> includes sum of all probe and fixture capacitance.



### **Recommended Solder Reflow Methods**



### **High Temperature Infrared/Convection**

T <sub>s</sub> MAX to T <sub>∟</sub> (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>S</sub> MAX)	200°C
- Time (t <sub>s</sub> MIN)	60 - 180 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T <sub>P</sub> Target)	250°C +0/-5°C
Time within 5°C of actual peak (tp)	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



### **Recommended Solder Reflow Methods**



### Low Temperature Infrared/Convection 240°C

T <sub>S</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	N/A
- Temperature Typical (T <sub>S</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	240°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (tp)	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

#### **Low Temperature Manual Soldering**

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

#### **High Temperature Manual Soldering**

260°C Maximum for 5 seconds Maximum, 2 times Maximum.