

APT5020BNR 500V 28.0A 0.20Ω

APT5022BNR 500V 27.0A 0.22Ω

POWER MOS IV®

AVALANCHE RATED

N-CHANNEL ENHANCEMENT MODE HIGH VOLTAGE POWER MOSFETS

MAXIMUM RATINGS

All Ratings: $T_C = 25^\circ\text{C}$ unless otherwise specified.

Symbol	Parameter	APT5020BNR	APT5022BNR	UNIT
V _{DSS}	Drain-Source Voltage	500	500	Volts
I _D	Continuous Drain Current @ T _C = 25°C	28	27	Amps
I _{DM}	Pulsed Drain Current ①	112	108	
V _{GS}	Gate-Source Voltage Continuous	±20		Volts
V _{GSM}	Gate-Source Voltage Transient	±30		
P _D	Total Power Dissipation @ T _C = 25°C	360		Watts
	Linear Derating Factor	2.9		W/°C
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150		°C
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300		
I _{AR}	Avalanche Current ① (Repetitive and Non-Repetitive)	28		Amps
E _{AR}	Repetitive Avalanche Energy ①	30		mJ
E _{AS}	Single Pulse Avalanche Energy ④	1300		

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV_{DS}	Drain-Source Breakdown Voltage ($V_{GS} = 0V, I_D = 250\mu A$)	500			Volts
$I_{D(ON)}$	On State Drain Current ② ($V_{DS} > I_{D(ON)} \times R_{DS(ON)}$ Max, $V_{GS} = 10V$)	APT5020BNR	28		Amps
		APT5022BNR	27		
$R_{DS(ON)}$	Drain-Source On-State Resistance ② ($V_{GS} = 10V, 0.5 I_D$ [Cont.])	APT5020BNR		0.20	Ohms
		APT5022BNR		0.22	
I_{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = V_{DSS}, V_{GS} = 0V$)			250	μA
	Zero Gate Voltage Drain Current ($V_{DS} = 0.8 V_{DSS}, V_{GS} = 0V, T_C = 125^\circ\text{C}$)			1000	
I_{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 20V, V_{DS} = 0V$)			± 100	nA
$V_{GS(TH)}$	Gate Threshold Voltage ($V_{DS} = V_{GS}, I_D = 1.0mA$)	2		4	Volts

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
$R_{\theta JC}$	Junction to Case			0.34	$^\circ\text{C/W}$
$R_{\theta JA}$	Junction to Ambient			40	

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

USA

405 S.W. Columbia Street

EUROPE

Avenue J.F. Kennedy Bât B4 Parc Cadéra Nord F-33700 Merignac - France

Bend, Oregon 97702-1035

Phone: (503) 382-8028

FAX: (503) 388-0364

Phone: (33) 56 34 34 71

FAX: (33) 56 47 97 61

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DYNAMIC CHARACTERISTICS

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Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C_{iss}	Input Capacitance	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 1 \text{ MHz}$		4150	5000	pF
C_{oss}	Output Capacitance			620	870	
C_{rss}	Reverse Transfer Capacitance			235	350	
Q_g	Total Gate Charge ③	$V_{GS} = 10V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$		171	240	nC
Q_{gs}	Gate-Source Charge			23	34	
Q_{gd}	Gate-Drain ("Miller") Charge			77	110	
$t_{d(on)}$	Turn-on Delay Time	$V_{GS} = 15V$ $V_{DD} = 0.5 V_{DSS}$ $I_D = I_D [\text{Cont.}] @ 25^\circ\text{C}$ $R_G = 1.8\Omega$		15	25	ns
t_r	Rise Time			40	60	
$t_{d(off)}$	Turn-off Delay Time			112	170	
t_f	Fall Time			70	105	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
I_S	Continuous Source Current (Body Diode)	APT5020BNR		28	Amps
		APT5022BNR		27	
I_{SM}	Pulsed Source Current ① (Body Diode)	APT5020BNR		112	
		APT5022BNR		108	
V_{SD}	Diode Forward Voltage ② ($V_{GS} = 0V$, $I_S = -I_D [\text{Cont.}]$)			1.3	Volts
t_{rr}	Reverse Recovery Time ($I_S = -I_D [\text{Cont.}]$, $dI_S/dt = 100A/\mu s$)		510	1020	ns
Q_{rr}	Reverse Recovery Charge ($I_S = -I_D [\text{Cont.}]$, $dI_S/dt = 100A/\mu s$)		10.2	15	μC

SAFE OPERATING AREA CHARACTERISTICS

Symbol	Characteristic	Test Conditions / Part Number	MIN	TYP	MAX	UNIT
SOA1	Safe Operating Area	$V_{DS} = 0.4 V_{DSS}$, $I_{DS} = P_D / 0.4 V_{DSS}$, $t = 1 \text{ Sec.}$	360			Watts
SOA2	Safe Operating Area	$I_{DS} = I_D [\text{Cont.}]$, $V_{DS} = P_D / I_D [\text{Cont.}]$, $t = 1 \text{ Sec.}$	360			
I_{LM}	Inductive Current Clamped	APT5020BNR	112			Amps
		APT5022BNR	108			

① Repetitive Rating: Pulse width limited by maximum junction temperature.

③ See MIL-STD-750 Method 3471

② Pulse Test: Pulse width < 380 μs , Duty Cycle < 2%

④ Starting $T_J = +25^\circ\text{C}$, $L = 3.3mH$, $R_G = 25\Omega$, Peak $I_L = 28A$

APT Reserves the right to change, without notice, the specifications and information contained herein.

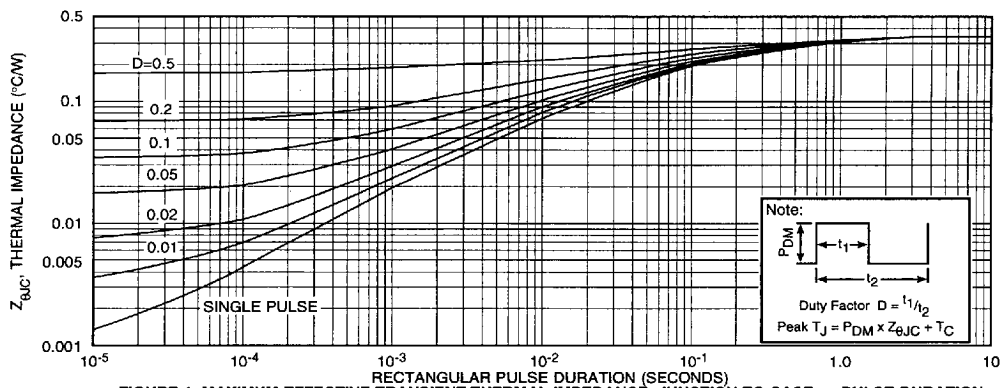


FIGURE 1. MAXIMUM EFFECTIVE TRANSIENT THERMAL IMPEDANCE, JUNCTION-TO-CASE vs PULSE DURATION
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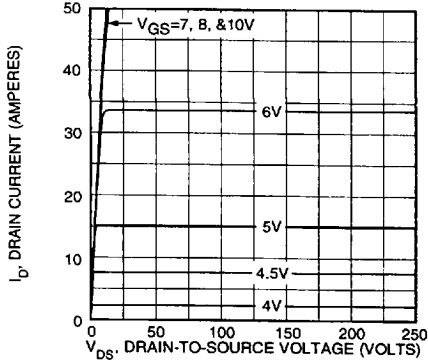


FIGURE 2, TYPICAL OUTPUT CHARACTERISTICS

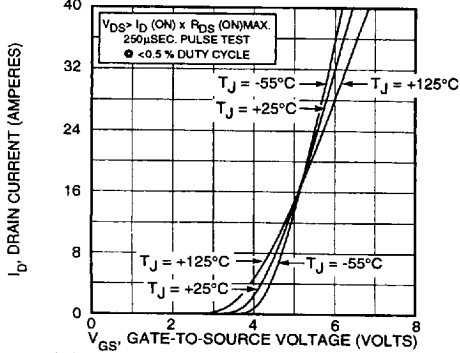


FIGURE 4, TYPICAL TRANSFER CHARACTERISTICS

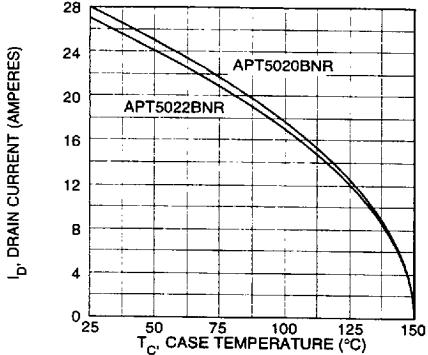


FIGURE 6, MAXIMUM DRAIN CURRENT vs CASE TEMPERATURE

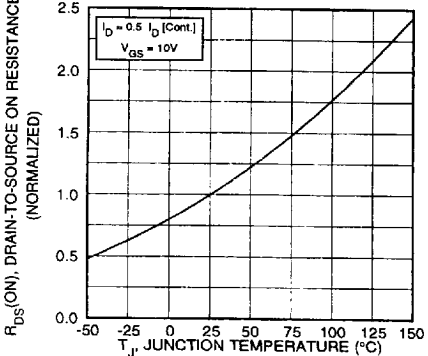


FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

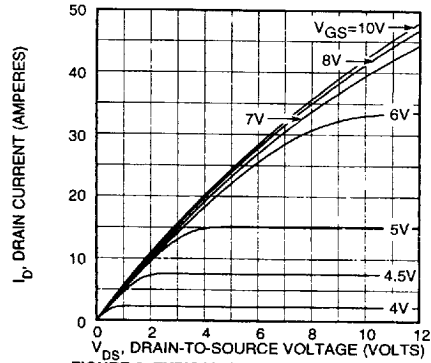


FIGURE 3, TYPICAL OUTPUT CHARACTERISTICS

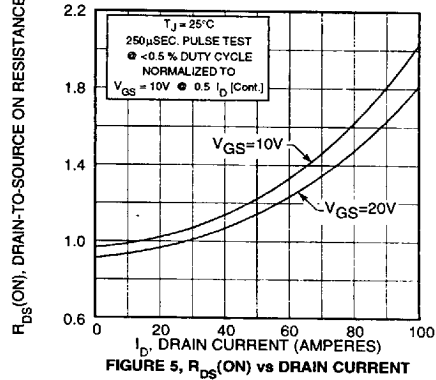


FIGURE 5, $R_{DS(ON)}$ vs DRAIN CURRENT

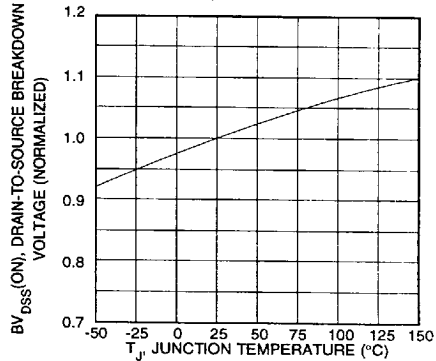


FIGURE 7, BREAKDOWN VOLTAGE vs TEMPERATURE

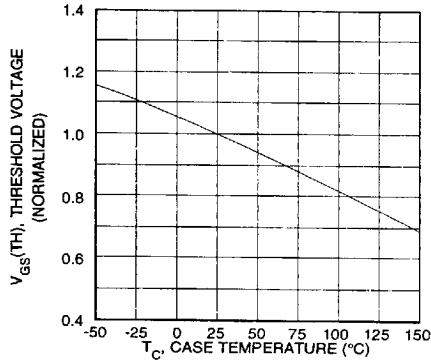


FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

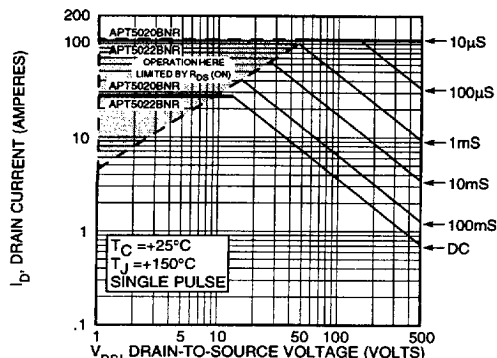


FIGURE 10. MAXIMUM SAFE OPERATING AREA

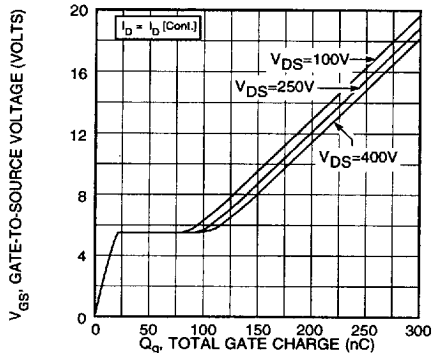


FIGURE 12. GATE CHARGES vs GATE-TO-SOURCE VOLTAGE

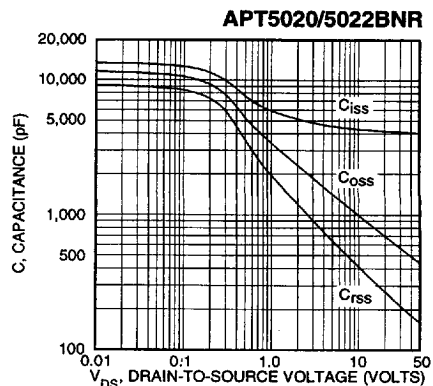


FIGURE 11. TYPICAL CAPACITANCE vs DRAIN-TO-SOURCE VOLTAGE

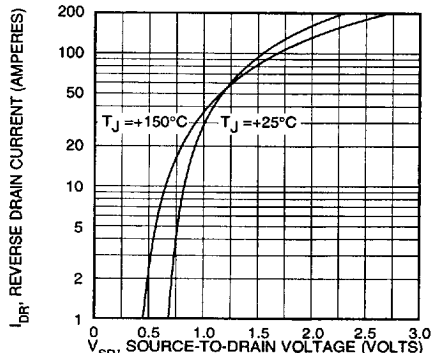
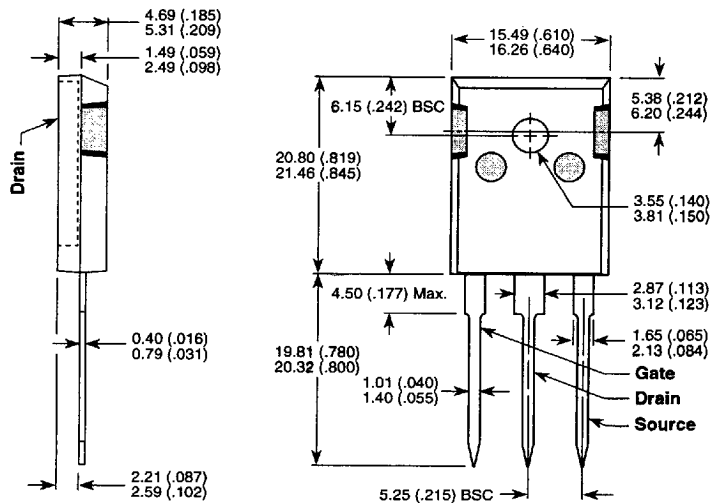


FIGURE 13. TYPICAL SOURCE-DRAIN DIODE FORWARD VOLTAGE

TO-247AD Package Outline



Dimensions in Millimeters and (Inches)
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