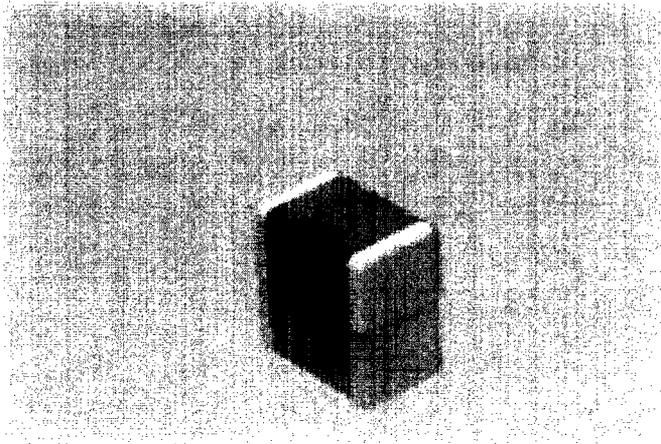


X7R Dielectric

General Specifications



X7R formulations are called "temperature-stable" ceramics and fall into EIA Class II materials. X7R is the most popular of these intermediate dielectric-constant materials. Its temperature variation of capacitance is within $\pm 15\%$ from -55°C to $+125^{\circ}\text{C}$. This capacitance change is non-linear.

Capacitance for X7R varies under the influence of electrical operating conditions such as voltage and frequency. It also varies with time, approximately $1\% \Delta C$ per decade of time, representing about 5% change in ten years.

X7R dielectric chip usage covers the broad spectrum of industrial applications where known changes in capacitance due to applied voltages are acceptable.

PART NUMBER (see page 7 for complete information and options)

0805	5	C	103	M	A	T	2	A
Size (L" x W")	Voltage 10V = Z 16V = Y 25V = 3 50V = 5 100V = 1	Dielectric X7R = C	Capacitance Code	Capacitance Tolerance Preferred M = $\pm 20\%$ K = $\pm 10\%$	Failure Rate A = Not Applicable	Terminations T = Plated Ni and Solder	Packaging 2 = 7" Reel Paper/Unmarked	Special Code A = Std. Product

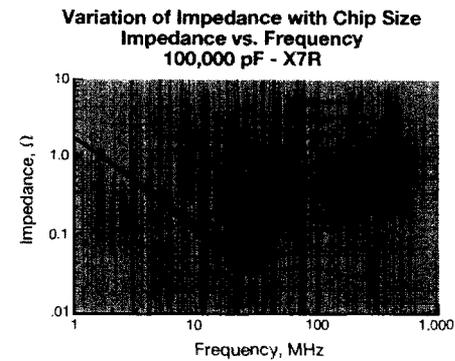
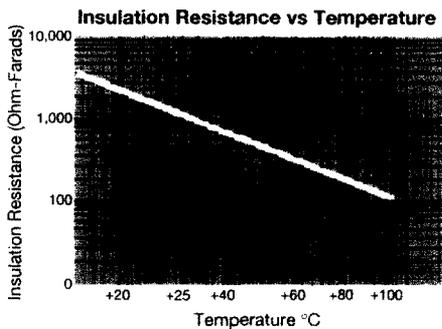
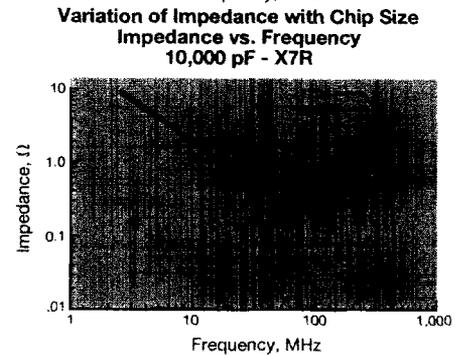
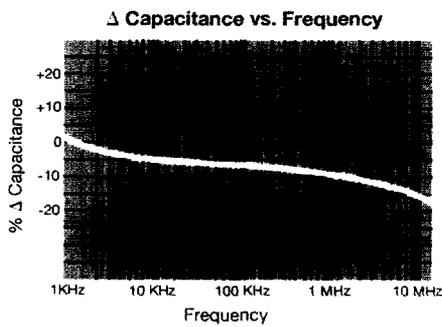
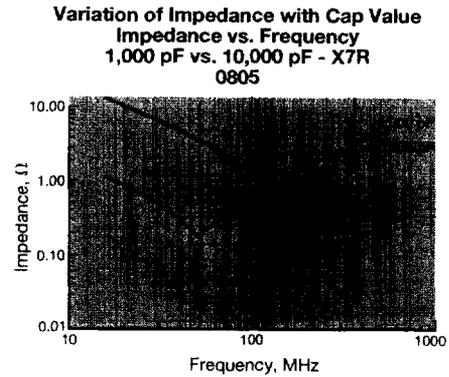
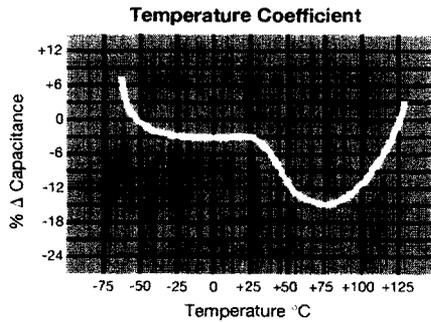
PERFORMANCE CHARACTERISTICS

Capacitance Range	100 pF to 2.2 μF (1.0 ± 0.2 Vrms, 1kHz)
Capacitance Tolerances	Preferred $\pm 10\%$, $\pm 20\%$ others available: $\pm 5\%$, +80 -20%
Operating Temperature Range	-55°C to $+125^{\circ}\text{C}$
Temperature Characteristic	$\pm 15\%$ (0 VDC)
Voltage Ratings	10, 16, 25, 50, 100 VDC ($+125^{\circ}\text{C}$)
Dissipation Factor	For 50 volts and 100 volts: 2.5% max. For 25 volts: 3.0% max. For 16 volts: 3.5% max. For 10 volts: 5% max.
Insulation Resistance ($+25^{\circ}\text{C}$, RVDC)	100,000 megohms min. or 1000 $\text{M}\Omega$ - μF min., whichever is less
Insulation Resistance ($+125^{\circ}\text{C}$, RVDC)	10,000 megohms min. or 100 $\text{M}\Omega$ - μF min., whichever is less
Aging Rate	$\approx 1\%$ per decade hour
Dielectric Strength	250% of rated voltage for 5 seconds at 50 mamp max. current
Test Voltage	1.0 ± 0.2 Vrms
Test Frequency	1 KHz

X7R Dielectric



Typical Characteristic Curves**



SUMMARY OF CAPACITANCE RANGES VS. CHIP SIZE

Style	10V	16V	25V	50V	100V
0402*	—	100pF - 47nF	100pF - 6.8nF	100pF - 3.9nF	—
0504	—	—	—	100pF - .01μF	100pF - 3.3nF
0603*	100pF - 0.22μF	100pF - 0.1μF	100pF - 47nF	100pF - 15nF	100pF - 4.7nF
0805*	100pF - 1μF	100pF - 0.47μF	100pF - 0.22μF	100pF - 0.1μF	100pF - 22nF
1206*	1.5μF - 2.2μF	1nF - 1μF	1nF - 0.47μF	1nF - 0.22μF	1nF - 0.1μF
1210*	→	1nF - 1.8μF	1nF - 1μF	1nF - 0.22μF	1nF - 0.1μF
1505	→	→	→	1nF - 0.1μF	1nF - 27nF
1808	→	→	10nF - 0.33μF	10nF - 0.33μF	10nF - 0.1μF
1812*	→	→	→	10nF - 1μF	10nF - 0.47μF
1825*	→	→	→	10nF - 1μF	10nF - 0.47μF
2220	→	→	→	10nF - 1.5μF	10nF - 1.2μF
2225	→	→	→	10nF - 2.2μF	10nF - 1.5μF

* Standard Sizes

**For additional information on performance changes with operating conditions consult AVX's software SpiCap.



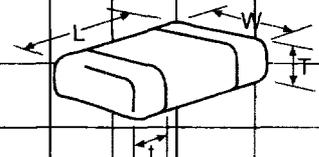
X7R Dielectric

Capacitance Range



PREFERRED SIZES ARE SHADED

SIZE		1210			1808*			1812*		1825*		2220			2225*			
(L) Length	MM (in.)	3.20 ± .20 (.126 ± .008)			4.57 ± .25 (.180 ± .010)			4.50 ± .30 (.177 ± .012)		4.50 ± .30 (.177 ± .012)		5.7 ± 0.4 (.225 ± .016)			5.72 ± .25 (.225 ± .010)			
(W) Width	MM (in.)	2.50 ± .20 (.098 ± .008)			2.03 ± .25 (.080 ± .010)			3.20 ± .20 (.126 ± .008)		6.40 ± .40 (.252 ± .016)		5.0 ± 0.4 (.197 ± .016)			6.35 ± .25 (.250 ± .010)			
(T) Max. Thickness	MM (in.)	1.70 (.067)			1.52 (.060)			1.70 (.067)		1.70 (.067)		2.30 (.090)			1.70 (.067)			
(t) Terminal	MM (in.)	.50 ± .25 (.020 ± .010)			.64 ± .39 (.025 ± .015)			.61 ± .36 (.024 ± .014)		.61 ± .36 (.024 ± .014)		.64 ± .39 (.025 ± .015)			.64 ± .39 (.025 ± .015)			
WVDC		16	25	50	100	25	50	100	50	100	50	100	50	100	200	50	100	
Cap. (pF)		1000			1200			1500		1800		2200			2700			
Cap. (µF)		.010			.012			.015		.018		.022			.027			
		.033			.039			.047		.056		.068			.082			
		.10			.12			.15		.18		.22			.27			
		.33			.39			.47		.56		.68			.82			
		1.0			1.2			1.5		1.8		2.2						



*IR and vapor phase soldering only recommended.

NOTES:

For higher voltage chips, see pages 24 and 25.