

NOTE  
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CON  
2.0 A  
TOL

ELECTRICAL SPECIFICATIONS:

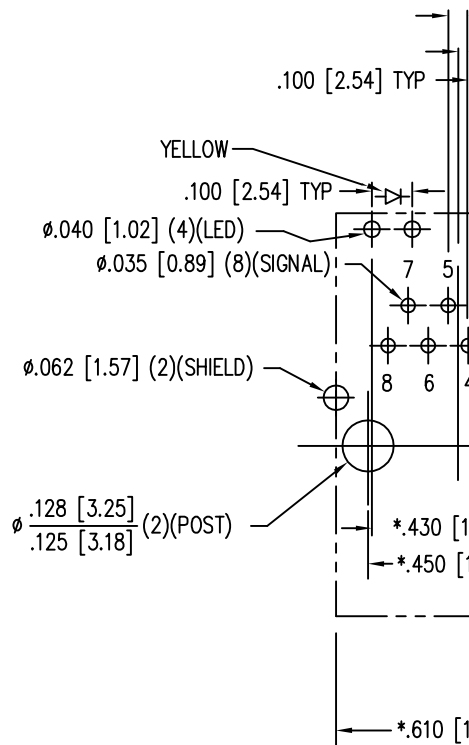
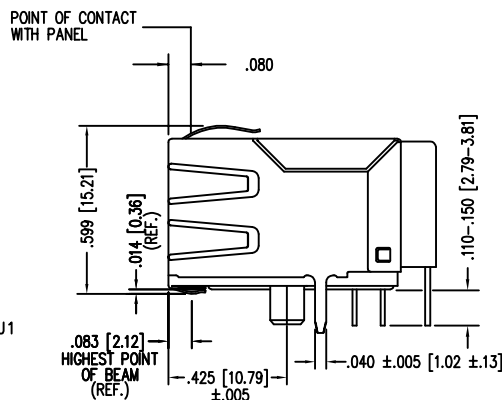
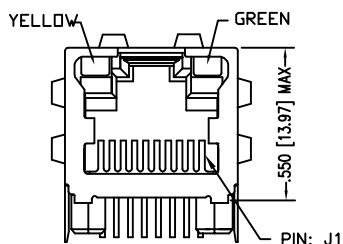
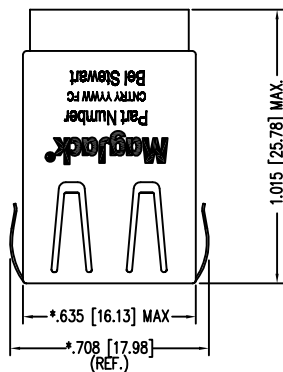
- |      |   |  |
|------|---|--|
| 1.0  | Turns Ratio: (P8-P6-P7) : (J3-J6)<br>(P4-P5-P3) : (J1-J2)   | : 1CT : 1CT ±3%<br>: 1CT : 1CT ±3%   |
| 2.0  | Inductance: (P7-P8)<br>(P4-P3)  | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias<br>: 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
| 3.0  | Leakage Inductance: P8-P7 (WITH J6 AND J3 SHORT)<br>P4-P3 (WITH J2 AND J1 SHORT)                            | : 0.3uH MAX. @ 1MHz<br>: 0.3uH MAX. @ 1MHz   |
| 4.0  | Interwinding Capacitance: (P8,P6,P7) TO (J6,J3)<br>(P4,P5,P3) TO (J2,J1)                                    | : 25pf TYP @ 1MHz<br>: 25pf TYP @ 1MHz   |
| 5.0  | DC Resistance: (J6-J3)=(J2-J1)  | : 1.2 ohms Max.  |
| 6.0  | Return Loss: (P7-P8)=100 OHMS AND (P1-P2)=100 OHM REF.<br>1MHz TO 30MHz<br>30MHz TO 60MHz<br>60MHz TO 80MHz | : -18dB MIN.<br>: -(19-20 LOG (f/30MHz))<br>: -12dB MIN.                             |
|      | NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).   |  |
| 7.0  | Dielectric Withstand: (J1, J2) TO (P7, P8)<br>(J3, J6) TO (P1,P2)   | : 1500 VAC<br>: 1500 VAC   |
| 8.0  | Insertion Loss: RS=RL=100 ohms<br>1-65MHz   | : -1 dB MAX  |
| 9.0  | Rise Time: RS=100 OHMS AND RL = 100 OHMS<br>OUTPUT VOLTAGE = 1 V peak<br>PULSE WIDTH= 112nS                 | : 3.0 nS MAX<br>: 3.0 nS MAX   |
| 10.0 | Cross Talk: 1-65MHz   | : -35 dB MIN   |
| 11.0 | Common to Common Mode Attenuation: 30MHz TO 100MHz<br>100MHz TO 130MHz                                      | : -30dB MAX<br>: -20dB MAX   |

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STANDARD LED	WAVELENGTH
GREEN	565 nm
YELLOW	590 nm

\* WITH A FORWARD CUR



NOTES:

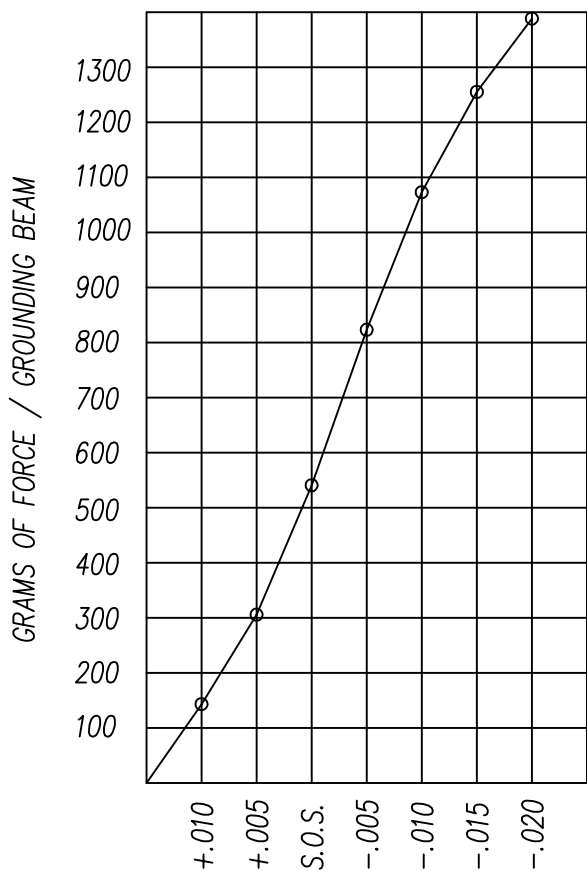
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "\*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 50 MICRO-INCH SELECTIVE GOLD PLATING

CT750006

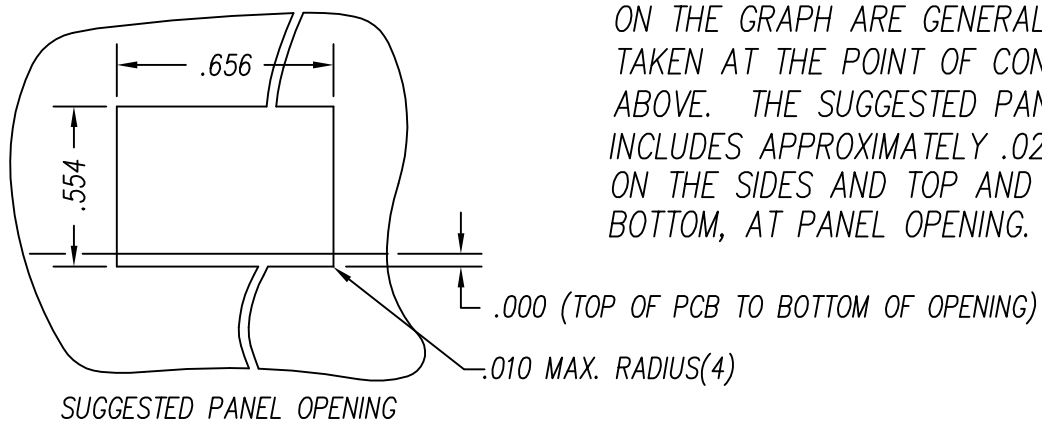
P.C.B. RECOMMEND  
SEEN FROM COM  
TOLERANCE ±.003 [0.08] UN

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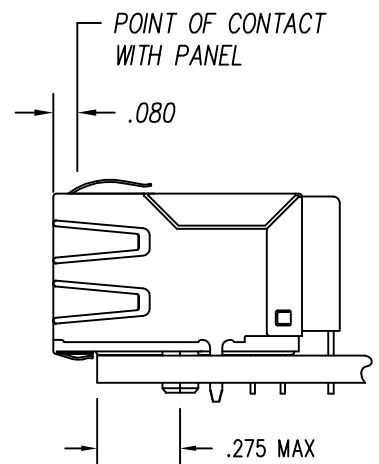
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PANEL GROUNDING BEAM DEFLECTION  
S.O.S. = SUGGESTED OPENING SIZE



SUGGESTED PANEL OPENING



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.

CT720034X1/24-001302

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SHEET  
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