

Single P-channel MOSFET

ELM33415CA-S

■ General description

ELM33415CA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -20V$
- $I_d = -3.5A$
- $R_{ds(on)} < 51m\Omega$ ($V_{gs} = -4.5V$)
- $R_{ds(on)} < 61m\Omega$ ($V_{gs} = -2.5V$)
- $R_{ds(on)} < 71m\Omega$ ($V_{gs} = -1.8V$)

■ Maximum absolute ratings

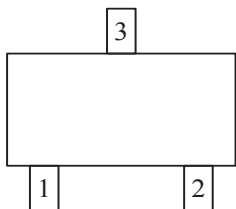
Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	-20	V	
Gate-source voltage	V_{gs}	± 8	V	
Continuous drain current	I_d	$T_a = 25^\circ C$	-3.5	A
		$T_a = 70^\circ C$	-2.8	
Pulsed drain current	I_{dm}	-21	A	3
Power dissipation	P_d	$T_a = 25^\circ C$	1.0	W
		$T_a = 70^\circ C$	0.6	
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$	

■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$		120	$^\circ C/W$	

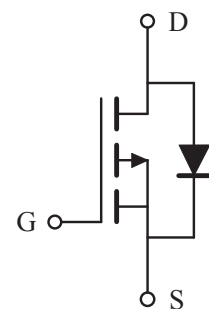
■ Pin configuration

SOT-23(TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■ Circuit



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■Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
STATIC PARAMETERS								
Drain-source breakdown voltage	BV _{dss}	V _{gs} =0V, I _d =-250μA	-20			V		
Zero gate voltage drain current	I _{dss}	V _{ds} =-16V, V _{gs} =0V			-1	μA		
		V _{ds} =-10V, V _{gs} =0V, T _j =70°C			-10			
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±8V			±100	nA		
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250μA	-0.45	-0.60	-0.90	V		
On state drain current	I _{d(on)}	V _{gs} =-4.5V, V _{ds} =-5V	-21			A	1	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-4.5V, I _d =-3.5A		40	51	mΩ	1	
		V _{gs} =-2.5V, I _d =-3.5A		48	61	mΩ		
		V _{gs} =-1.8V, I _d =-2.0A		60	71	mΩ		
Forward transconductance	G _{fs}	V _{ds} =-5V, I _d =-3.5A		17		S	1	
Diode forward voltage	V _{sd}	I _f =-3.5A, V _{gs} =0V			-1.3	V	1	
Max. body-diode continuous current	I _s				-3.5	A		
DYNAMIC PARAMETERS								
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =-10V, f=1MHz		1180		pF		
Output capacitance	C _{oss}				185		pF	
Reverse transfer capacitance	C _{rss}				117		pF	
SWITCHING PARAMETERS								
Total gate charge	Q _g	V _{gs} =-4.5V, V _{ds} =-10V I _d =-3.5A		16.7		nC	2	
Gate-source charge	Q _{gs}				1.8		nC	2
Gate-drain charge	Q _{gd}				4.6		nC	2
Turn-on delay time	t _{d(on)}	V _{gs} =-4.5V, V _{ds} =-10V I _d ≈-3.5A, R _{gen} =3.3Ω		20		ns	2	
Turn-on rise time	t _r				36		ns	2
Turn-off delay time	t _{d(off)}				45		ns	2
Turn-off fall time	t _f				62		ns	2
Body diode reverse recovery time	t _{rr}	I _f =-3.5A, dI/dt=100A/μs		30		ns		
Body diode reverse recovery charge	Q _{rr}				14		nC	

NOTE :

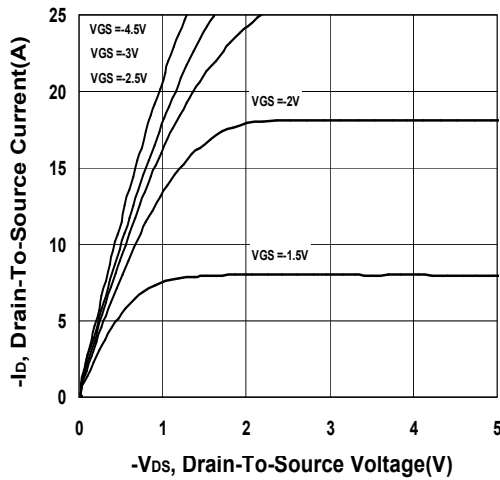
1. Pulsed width≤300μsec and Duty cycle≤2%.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature.
4. Duty cycle ≤ 1%.

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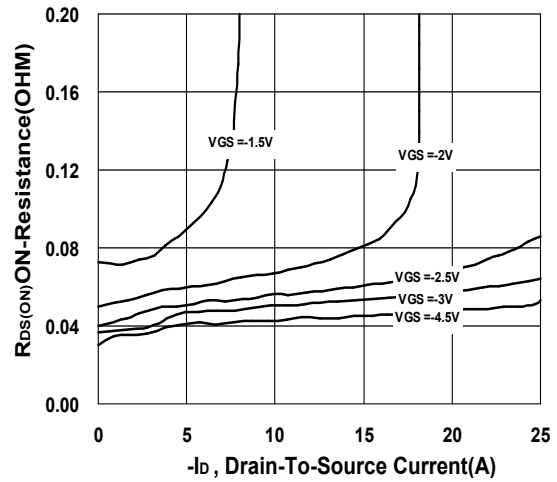
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■ Typical electrical and thermal characteristics

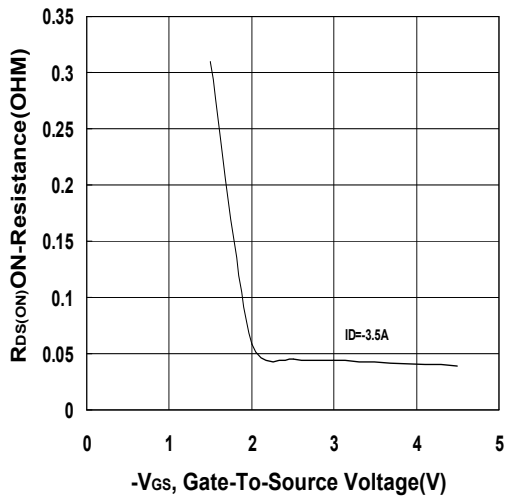
Output Characteristics



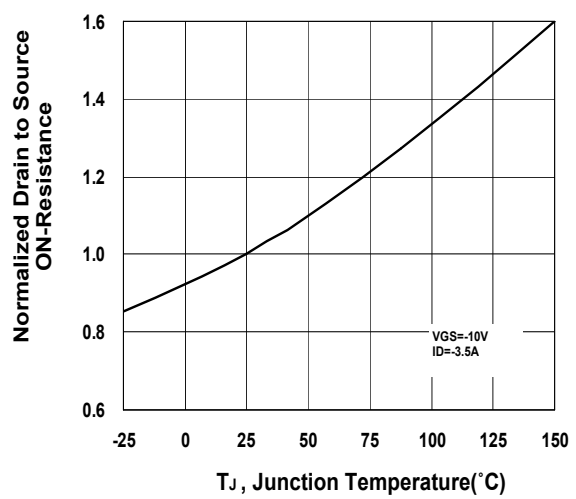
On-Resistance VS Drain Current



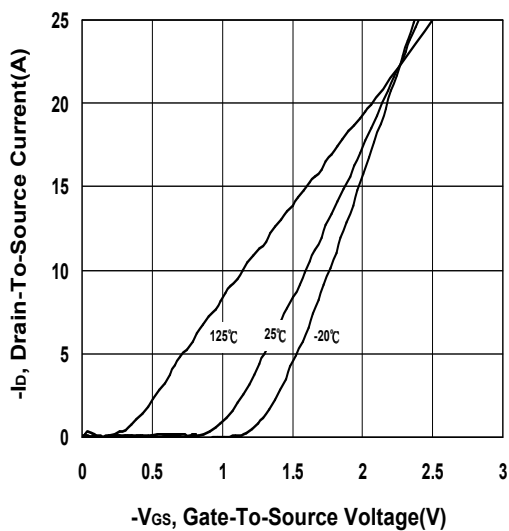
On-Resistance VS Gate-To-Source



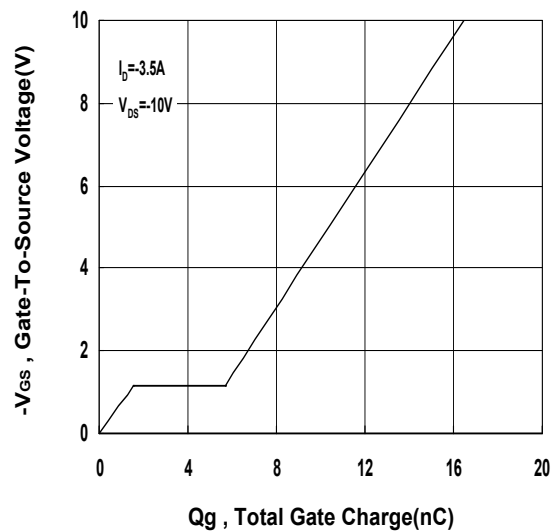
On-Resistance VS Temperature



Transfer Characteristics



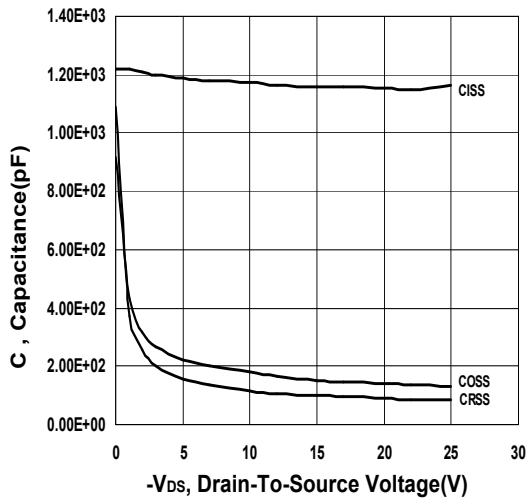
Gate charge Characteristics



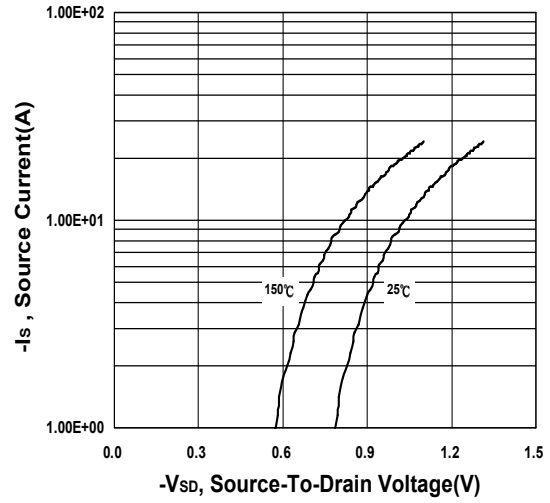
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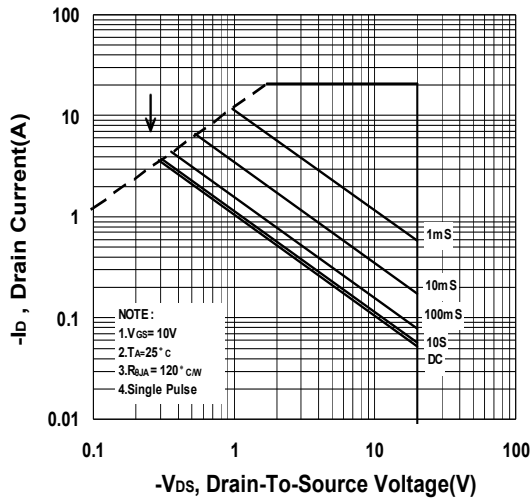
Capacitance Characteristic



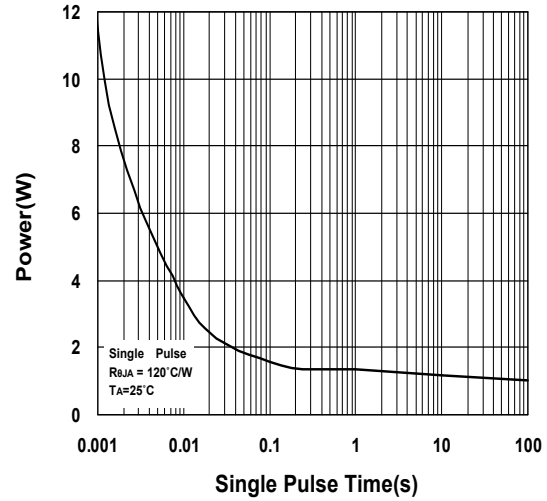
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

