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**1.5GHZ 10 Prescaler** Data Sheet

Features DS3690 May 2002

- · High speed operation 1.5GHz
- Silicon techology for low phase noise (typically better than -140dBc/Hz at 10kHz)
- Very low power dissipation: 150mW (Typ.)
- Single 5V supply operation
- High input sensitivity
- · Very wide operating frequency range
- Available as DESC SMD 5962 9157201MPA
- Very wide operating frequency rang

## **Description**

The SP8830 is one of a range of very high speed low power prescalers for professional and military applications. The device features a complementary output stage with on chip current sources for the emitter follower outputs.

## **Ordering Information**

SP8830 A DG SP8830 B DG DES9157201/AC/DGAZ (SMD)

Temperature Range: -55°C to +125°C (A Grade)
 -40 °C to +85°C (B Grade)

## **Absolute Maximum Ratings**

Supply voltage,  $V_{CC}$  6.5V Clock input voltage 2.5V p-p Storage temperature range Junction temperature + 175°C + 175°C

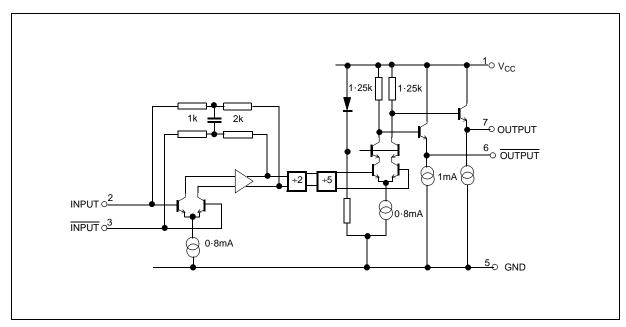


Figure 1 - SP8830 Block Diagram

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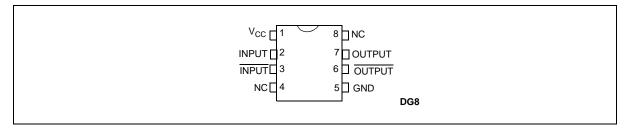


Figure 2 - Pin Connections

**Electrical Characteristics -** Unless otherwise stated, the Electrical Characteristics are guaranteed over specified supply,

frequency and temperature range.

Supply voltage,  $V_{CC}$  -4.75V to +5.25V. Temperature,  $T_{AMB}$  = -55°C to +125°C (A Grade), -40°C to +85°C (B Grade)

Characteristic	Pin	Value			Units	Conditions
Characteristic		Min.	Тур.	Max.	Units	Conditions
Supply current, I <sub>CC</sub>	1		•		mA	
			40	50		
Input sensitivity, 100MHz to 500MHz	2, 3			100	mV	RMS sinewave, measured in $50\Omega$ system. See Figs. 3 and 4.
Input impedance (series equivalent)	2, 3		50 2		$\Omega$ pF	See Fig. 5
Output voltage with $f_{IN} = 100MHz$ Output voltage with $f_{IN} = 1500MHz$	6, 7 6, 7	0.7	1 0·4		V p-p V p-p	

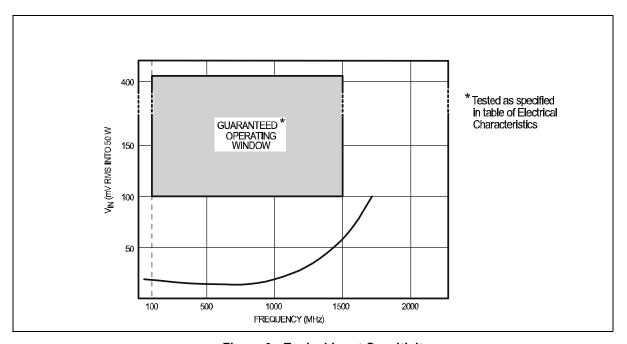


Figure 3 - Typical Input Sensitivity

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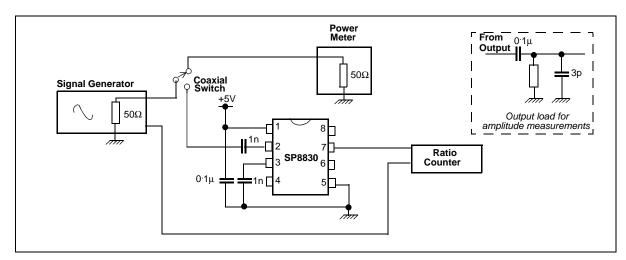


Figure 4 - Test Circuit

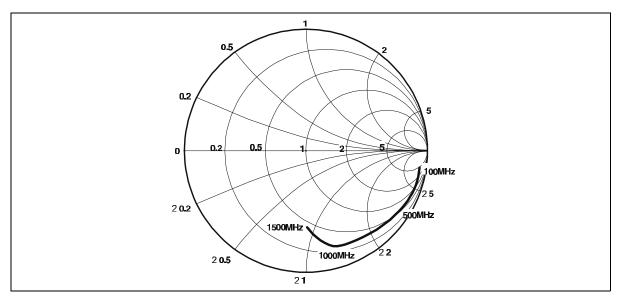
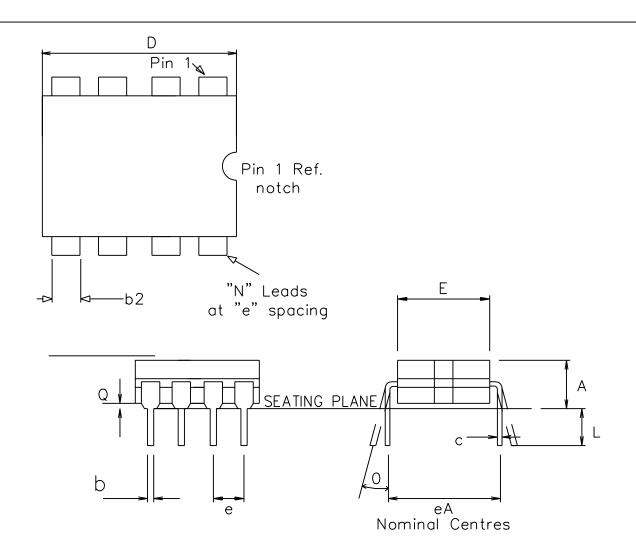


Figure 5 - Typical Input Impedance, Normalised to 50W

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	Alterr	n. Dimer	isions		Control Dimensions			
Symbol	in	millimet	res		in inches			
- ,	MIN Nominal MAX				MIN Nominal MAX			
L	3.18		4.06		0.125		0.160	
Α			5.08				0.200	
Q	0.51				0.020			
E	5.59		7.87		0.220		0.310	
eА		7.62				0.300		
С	0.20		0.36		0.008		0.014	
D			10.29				0.405	
е	2.54 BSC.				0.100 BSC.			
b2	1.14		1.65		0.045		0.065	
b	0.36		0.58		0.014		0.023	
0			15				15	
	Pin features							
N	8							
ND	4							
NE	0							
NOTE	RECTANGULAR							

This drawing supersedes 418/ED/39501/001 (Swindon)

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ACN	201728	212450		ZARLINK SEMICONDUCTOR		(Glass Seal Ceramic)
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