



MPS - D04

MPS - D54

COMPLEMENTARY DARLINGTON SILICON PLANAR TRANSISTORS

MICRO ELECTRONICS

MPS-D04 (NPN) and MPS-D54 (PNP) are complementary darlington silicon planar transistors designed for use in high gain driver applications.

CASE T0-92A



EBC

ABSOLUTE MAXIMUM RATINGS For p-n-p devices, voltage and current values are negative.

Collector-Emitter Voltage	V _{CES}	25V
Emitter-Base Voltage	V _{EBO}	10V
Collector Current	I _C	300mA
Total Power Dissipation @ T _A =25°C	P _{tot}	625mW
@ T _C =25°C		1.5W
Operating Junction & Storage Temperature	T _j , T _{stg}	-55 to +150°C

ELECTRICAL CHARACTERISTICS (T_A=25°C)

PARAMETER	SYMBOL	MIN	MAX	UNIT	TEST CONDITIONS
Collector-Emitter Breakdown Voltage	BV _{CES}	25		V	I _C =100μA V _{BE} =0
Emitter-Base Breakdown Voltage	BV _{EBO}	10		V	I _E =10μA I _C =0
Collector Cutoff Current	I _{CES}		1	μA	V _{CE} =20V V _{BE} =0
Collector Cutoff Current	I _{CB0}		1	μA	V _{CB} =20V I _E =0
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		1	V	I _C =100mA I _B =0.1mA
D.C. Current Gain	HFE *	1000	2000		I _C =10mA V _{CE} =5V
		1000			I _C =100mA V _{CE} =5V
					I _C =300mA V _{CE} =5V
Current Gain-Bandwidth Product	f _T	100		MHz	I _C =10mA V _{CE} =5V
					f=100MHz

* Pulse Test : Pulse Width=0.3ms, Duty Cycle=1%

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