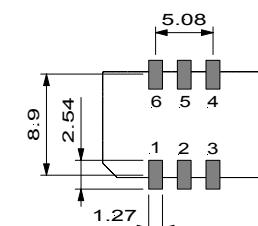
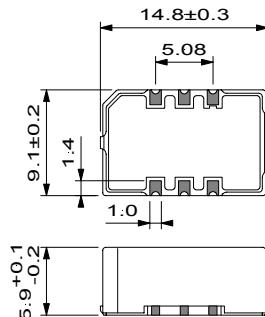


HIGH FREQUENCY MINIATURE SURFACE MOUNT PXO

DFN S1-MLECPI (3.3 V)

KEY FEATURES	
600 to 800 MHz	
$\pm 20 \text{ ppm}$ /15 years stability available	
Parametric frequency multiplication	
0.4 ps RMS jitter over 50 kHz to 80 MHz B.W.	
APPLICATIONS	
OC-192/Sonet/SDH	

Function	DFN S1
N/C	1
E / D	2
GND	3
Output 1	4
Output 2	5
Vcc	6



PC board footprint

TYPE	DFN S1-MLECPI
Frequency Range	600 to 800 MHz
Standard Frequencies	622.0800; 644.5313; 666.5143; 669.3266; 693.4828; 777.6000 MHz

ELECTRICAL SPECIFICATIONS	
supply voltage	3.3 V $\pm 5 \%$
supply current (no load)	$\leq 60 \text{ mA}$
output load	LVPECL 100 K (50 Ω to 1.3 V)
duty cycle @ 50% level	45/55...55/45 %
rise/fall times (20 to 80%)	$\leq 0.5 \text{ ns}$
high/low levels	$\geq 2.22 \text{ V} / \leq 1.7 \text{ V}$
jitter RMS (12 kHz to 5 MHz)	0.10 ps typ; $\leq 0.15 \text{ ps}$
jitter RMS (12 kHz to 20 MHz)	0.15 ps typ; $\leq 0.20 \text{ ps}$
jitter RMS (50 kHz to 80 MHz)	0.40 ps typ; $\leq 0.50 \text{ ps}$
enable / disable on pin 2	low or open = enable, high = disable
complementary output on pin 5	180° phase shifted
start up	$\leq 10 \text{ ms} @ 3.15 \text{ V}$

FREQUENCY STABILITY		stability [ppm] and temperature code							
types	temperature range	stability	code	stability	code	stability	code	stability	code
all types	0 to 70°C	± 20	XB20	± 25	XB25	± 50	XB50	± 100	XB100
	-10 to 70°C	± 20	XI20	± 25	XI25	± 50	XI50	± 100	XI100
	-40 to 85°C	± 25	XE25	± 50	XE50	± 75	XE75	± 100	XE100
remark	includes calibration at 25°C, temperature, ageing, Vcc and load changes 1 st year								

OPTIONS	
stability over long life time	A = 5 years B = 10 years C = 15 years
ORDERING CODE	
Example	type + option code + frequency + stability / temperature code DFN S1-MLECPI 622.08 MHz XB20C