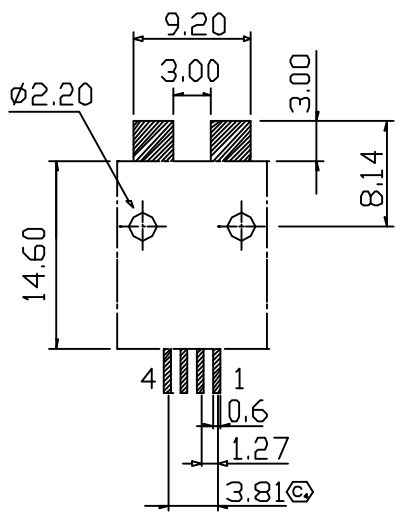
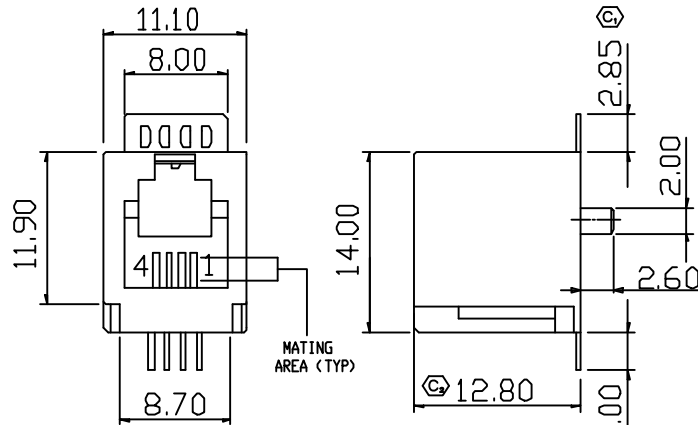
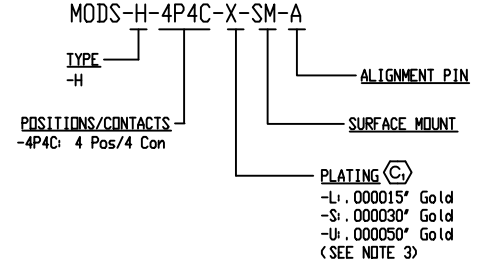
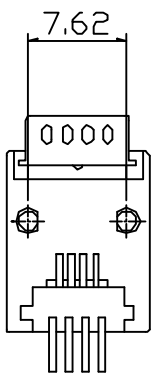


REVISION B



PCB Layout



NOTES:

1. C_2 REPRESENTS CRITICAL DIMENSION.
2. CRITICAL DIMENSIONS ON PCB LAYOUT APPLY TO THE ASSEMBLY
3. PLATING APPLIES TO MATING AREA, GOLD FLASH ON TAILS, ALL PLATING OVER Ni UNDERCOAT
4. PIN NUMBERS REFLECT PIN CALLOUT ON THE CONNECTOR, NOT USER WIRING ASSIGNMENT.

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN MM. TOLERANCES ARE:
 DECIMALS: .XX: $\pm .25$
 .XXX: $\pm .13$
 .XXXX: $\pm .05$

PROPRIETARY NOTE
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520 PARK EAST BLVD. NEW ALBANY, IN 47150
 (812) 944-6733 P.O. BOX 1147 TWX: 810-540-4085

CODE: 553222

DO NOT SCALE DRAWING
 INSULATOR: Nylon 46
 CONTACT: .35mm Phos Bronze

SHEET SCALE: 1:2

MAX FLASH ALLOWED: .061
 MAX GATE VESTIGE: .051
 F:\DWG\EXSPROD\ASSEMBLY\NO\MODJACKS\MODS\

DESCRIPTION:
 RJ11 Top Entry SMT PCB Jack

DWG. NO.
 MODS-H-4P4C-X-SM-A

BY: J. Riley 04/7/04 SHEET 1 OF 1