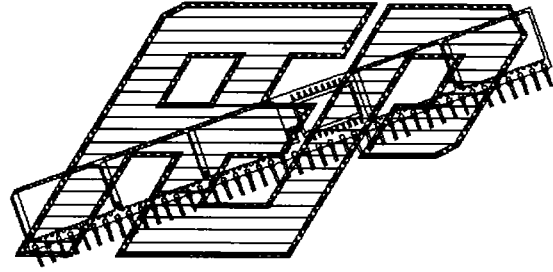>> *1,048,576 x 9 Organization*

>> *On board BUFFERS for all address and control lines*

>> *Low profile design, 0.5 inch stand-off height.*

>> *Double sided to maximize bit density*

>> *Completely Static operation*

>> *TTL compatible*

>> *Uses single +5V power supply*

1 MEGAWORD BY 9 BIT HIGH
SPEED STATIC RAM MODULE
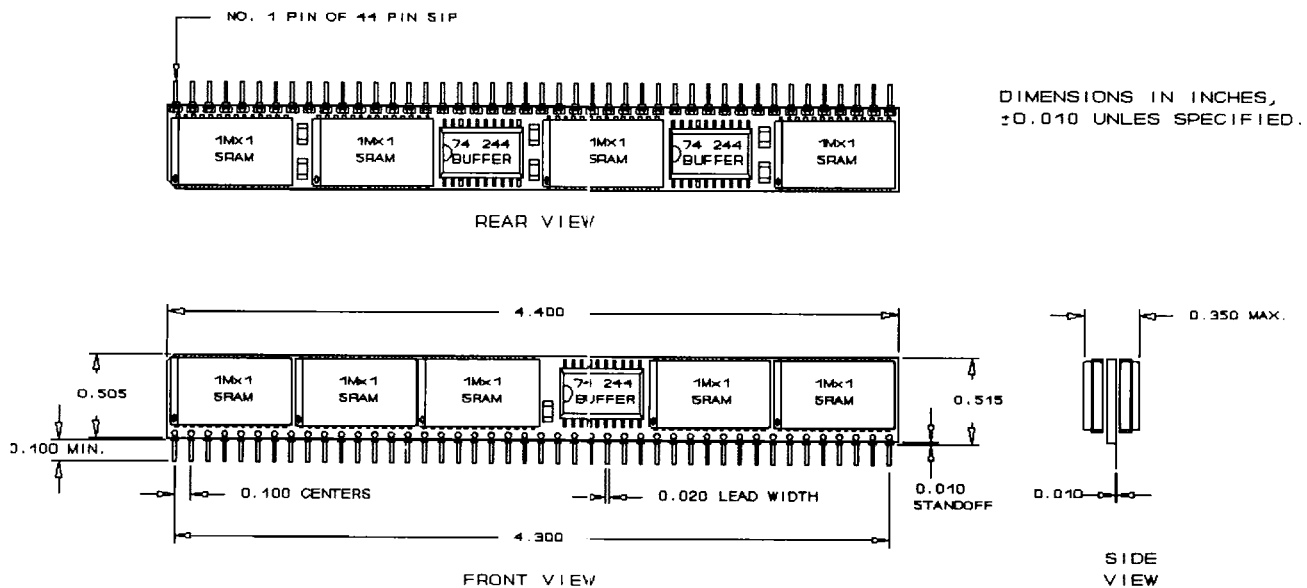## WITH ON-BOARD BUFFERS

DESCRIPTION:

The AEPSS1M9 is a high speed, high density 1 megaword by 9 bit static random access memory module with on board buffering of all address and control lines. Physically it consists of an FR4 PC material substrate surface mounted with nine 1M x 1 high speed static RAM ICs, three 244 type buffer ICs, five 0.18 microfarad decoupling capacitors, and 44 press-in I/O pins in a single-inline-package format.

The module can use any of the 1M x 1 SRAMs with SOJ lead packages and standard pin-out made by any of a variety of manufacturers. A wide range of access speeds are available.

Performance specifications and electrical characteristics are determined by the IC devices used. These items can vary according to the type and manufacturer of the components. The necessary information is obtained from the IC vendors' data sheets, like those attached, or from their data books.

Mechanical dimensions are 0.50 in. high by 4.40 in. long by 0.32 in. wide. The I/O pins are on 0.1 inch center spacing. Also available in 256K x 9 and 64K x 9 versions. These versions can be made in a by 8 bit organization on request.
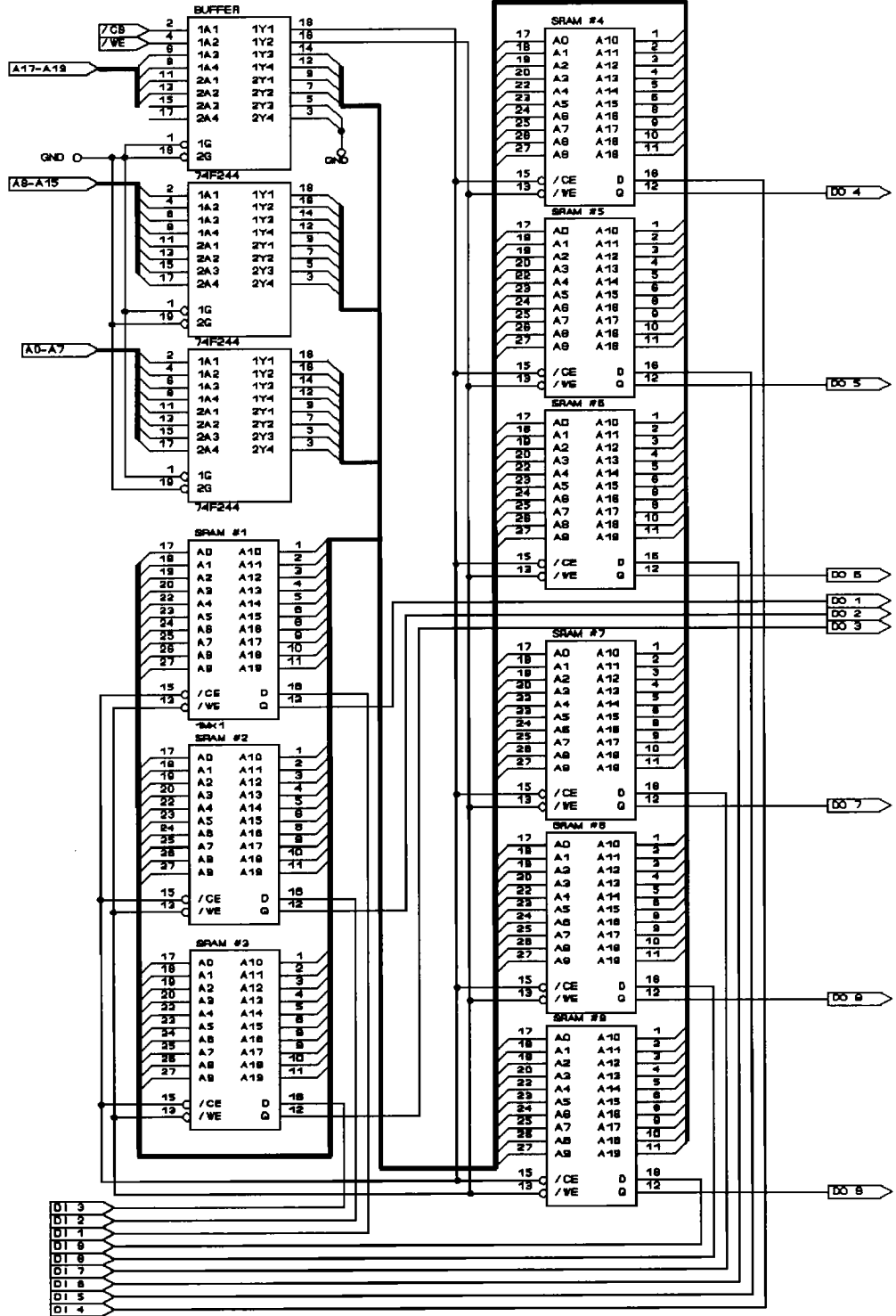
SPECIFICATION DRAWING
1M x 9 SRAM

REAR VIEW

DIMENSIONS IN INCHES,
±0.010 UNLES SPECIFIED.

FRONT VIEW

SIDE VIEW

BUFFERED HIGH SPEED 1M x 9 SRAM MODULE

SIP PIN-OUT CONFIGURATION

Pin	Signal
1	GND
2	VCC
3	Di_1
4	Do_1
5	Di_2
6	Do_2
7	Di_3
8	Do_3
9	Di_4
10	Do_4
11	Di_5
12	Do_5
13	A_0
14	A_1
15	A_2
16	A_3
17	A_4
18	A_5
19	A_6
20	A_7
21	A_8
22	A_9
23	A_{10}
24	A_{11}
25	A_{12}
26	A_{13}
27	A_{14}
28	A_{15}
29	A_{16}
30	A_{17}
31	A_{18}
32	A_{19}
33	CS*
34	WE*
35	Di_6
36	Do_6
37	Di_7
38	Do_7
39	Di_8
40	Do_8
41	Di_9
42	Do_9
43	VCC
44	GND

*ACTIVE WHEN LOW

notes:
pins 31 and 32 are no connects on 256K x 9 verion.
pins 29, 30, 31, 32 are no connects on and 64K x 9 versions.

FUNCTIONAL DIAGRAM