
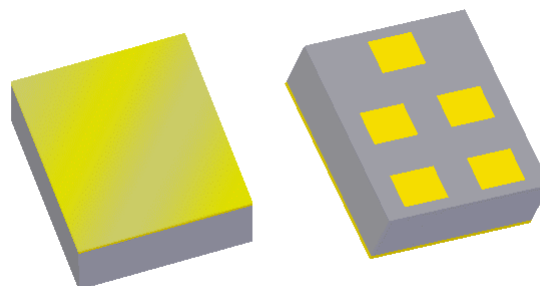


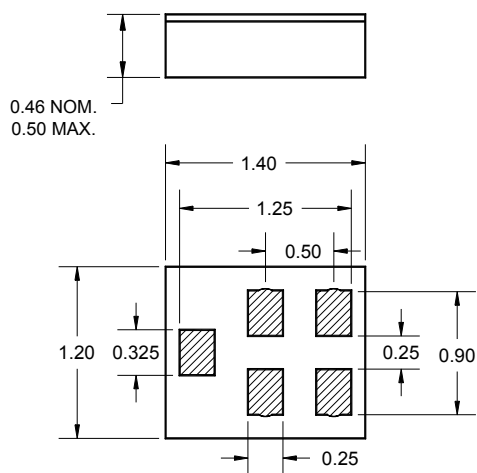
Features

- For CDMA applications
- Usable bandwidth 25 MHz
- Single-ended operation
- Ceramic Chip Scale Package (CSP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



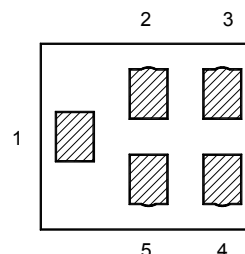
Package

Surface Mount 1.40 x 1.20 x 0.46 mm



Pin Configuration

Bottom View



Pin No.	Description
1	Input
4	Output
3	Ground
2,5	Case ground

Dimensions shown are nominal in millimeters
All tolerances are ± 0.10 mm

Body: Al_2O_3 ceramic
Lid: Kovar or Alloy 42, Au over Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Electrical Specifications ⁽¹⁾

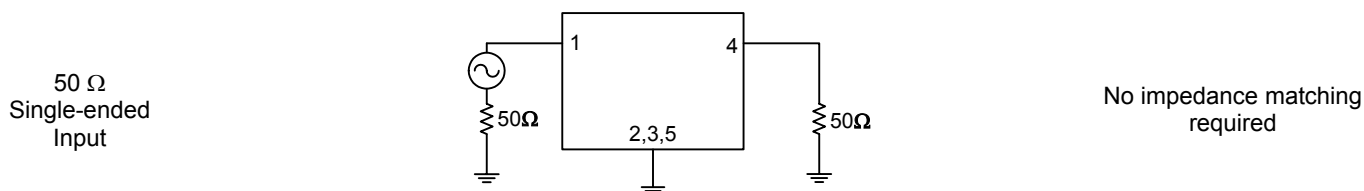
Operating Temperature Range: ⁽²⁾ -30 to +85 °C

Parameter ⁽³⁾	Minimum	Typical	Maximum	Unit
Center Frequency	-	836.5	-	MHz
Maximum Insertion Loss 824 - 849 MHz	-	2.3	3.0	dB
Absolute Attenuation				
10 - 800 MHz	30	45	-	dB
869 - 894 MHz	40	50	-	dB
1574.42 - 1576.42 MHz	28	39	-	dB
1638 - 1708 MHz	27	38	-	dB
1930 - 1990 MHz	25	35	-	dB
2110 - 2170 MHz	24	34	-	dB
2462 - 2557 MHz	20	32	-	dB
3286 - 3406 MHz	18	29	-	dB
Amplitude Variation 824 - 849 MHz	-	1.0	2.0	dB
Input/Output Return Loss 824 - 849 MHz	8.5	10.6	-	-
Optimal Source Impedance ⁽⁴⁾	-	50	-	Ω
Optimal Load Impedance ⁽⁴⁾	-	50	-	Ω

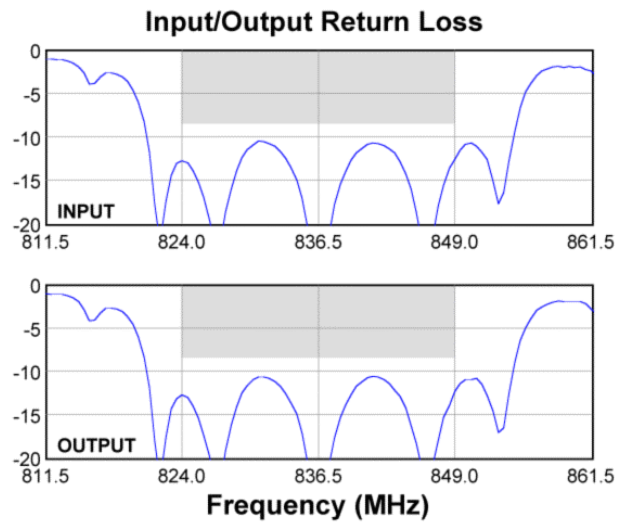
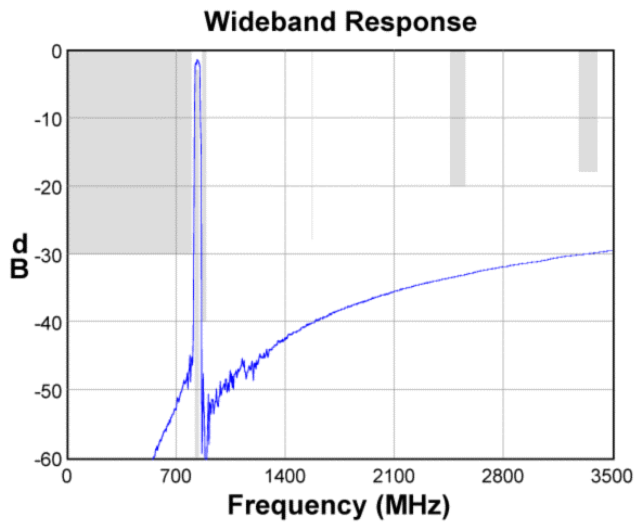
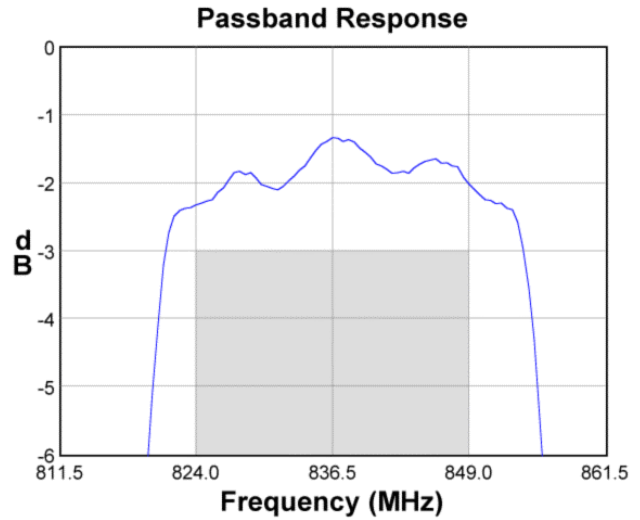
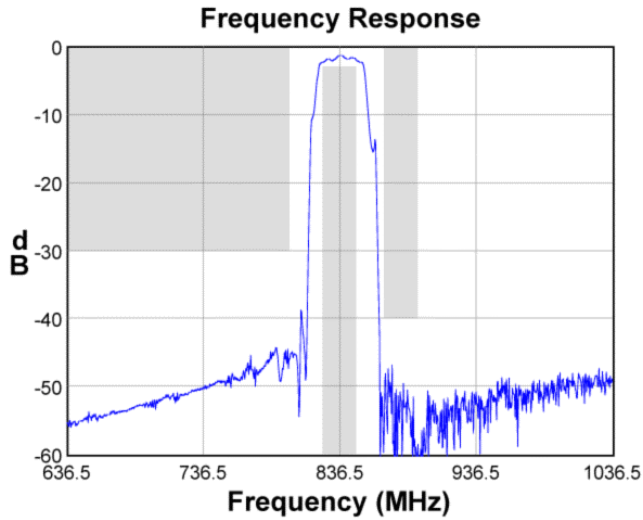
Notes:

1. All specifications are based on the test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

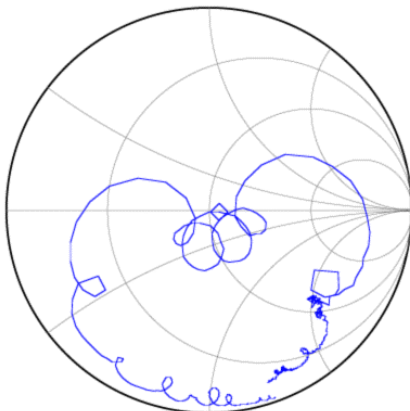
Test Circuit:



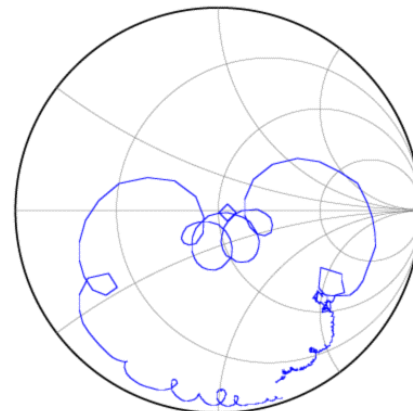
Typical Performance (at +25°C)



Input Smith Chart

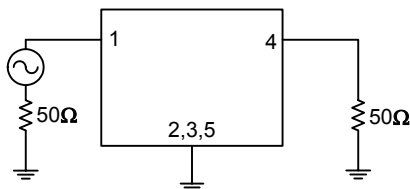


Output Smith Chart



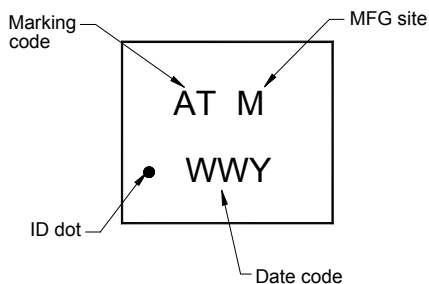
Matching Schematics

50 Ω
Single-ended
Input



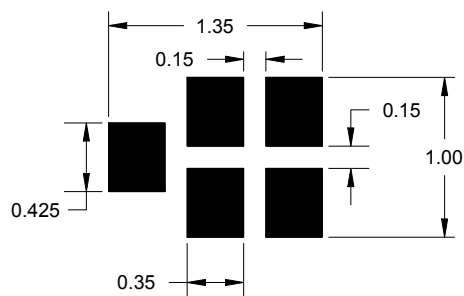
No impedance matching
required

Marking



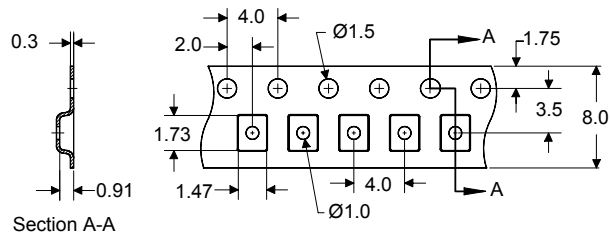
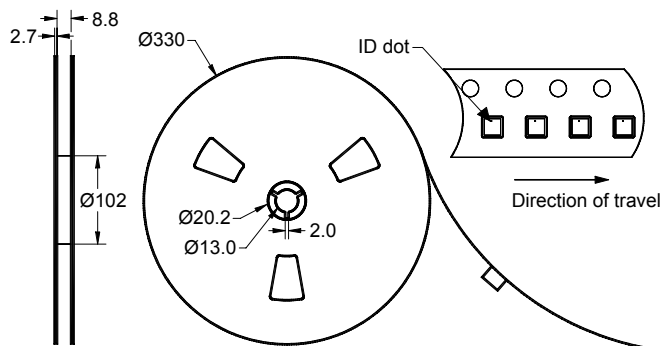
The date code consists of: WW = 2 digit week,
Y = last digit of year, M = manufacturing site code

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel




Dimensions shown are nominal in millimeters
Packaging quantity: 10000 units/reel

Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-30	+85	°C
Storage Temperature Range	T _{stg}	-40	+85	°C

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JEDEC J-STD-020C **Pb-free** process, **260°C** peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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