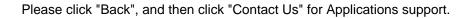
## Engineering Development Model

## **Voltage Controlled Oscillator**

## JCOS-EDR4785

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.





**CASE STYLE: BG291** 

ELECTRICAL SPECIFICATIONS 50Ω					
Parameter	Min.	Тур.	Max.	Units	
Frequency	61		83	MHz	
Tuning Voltage	0.5		9	V	
Power Output		+9		dBm	
Phase Noise					
at 1 kHz offset		-100		dBc/Hz	
at 10 KHz offset		-120		dBc/Hz	
at 100 KHz offset		-139		dBc/Hz	
at 1000 kHz offset		-156		dBc/Hz	
Pulling at 12 dBr PK-PK all phases		0.1		MHz	
Pushing at Vcc=5V±0.25V		0.1		MHz/V	
Tuning Sensitivity		2.5 - 5.8		MHz/V	
Harmonic Suppression		-16	-8	dBc	
3 dB Modulation Bandwidth		8000		kHz	
Supply Voltage		9		V	
Supply Current			20	mA	

MAXIMUM RATINGS		
Operating Temperature	-55°C to 85°C	
Storage Temperature	-55°C to 100°C	
Absolute Supply Voltage (Vcc)	+11V	
Absolute Tuning Voltage (Vtune)	+11V	

PIN CONNECTIONS		
RF OUT	10	
VCC	14	
V-TUNE	1	
GROUND	2,3,4,5,6,9,10,11,12,13,14	