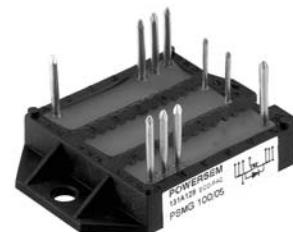
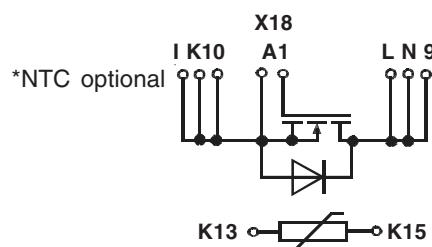


Power MOSFET

in ECO-PAC 2

Single MOSFET Die

Preliminary Data Sheet



MOSFET

Symbol	Conditions	Maximum Ratings		
V_{DSS}	$T_J = 25^\circ\text{C}$ to 150°C	500	V	
V_{DGR}	$T_J = 25^\circ\text{C}$ to 150°C ; $R_{GS} = 1 \text{ M}\Omega$	500	V	
V_{GS}	Continuous	± 20	V	
V_{GSM}	Transient	± 30	V	
I_{D25}	$T_c = 25^\circ\text{C}$	82	A	
I_{D80}	$T_c = 80^\circ\text{C}$	62	A	
E_{AR}	$T_c = 25^\circ\text{C}$	60	mJ	
E_{AS}	$T_c = 25^\circ\text{C}$	3	J	
dv/dt	$I_s \leq I_{DM}$, $di/dt \leq 100 \text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DSS}$ $T_J \leq 150^\circ\text{C}$, $R_G = 2 \Omega$	5	V/ns	
P_D	$T_c = 25^\circ\text{C}$	400	W	

Symbol	Conditions	Characteristic Values		
		($T_J = 25^\circ\text{C}$, unless otherwise specified)		
		min.	typ.	max.
V_{DSS}	$V_{GS} = 0 \text{ V}$, $I_D = 5 \text{ mA}$	500		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 8 \text{ mA}$	2		V
I_{GSS}	$V_{GS} = \pm 20 \text{ V}$, $V_{DS} = 0$			$\pm 100 \text{ nA}$
I_{DSS}	$V_{DS} = V_{DSS}$, $T_J = 25^\circ\text{C}$ $V_{GS} = 0 \text{ V}$, $T_J = 125^\circ\text{C}$			$100 \mu\text{A}$ 2 mA
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$, $I_D = I_T$, ¹⁾			50 m Ω
g_{fs}	$V_{DS} = 10 \text{ V}$, $I_D = I_T$, ¹⁾	45		S
C_{iss} C_{oss} C_{rss}	$V_{GS} = 0 \text{ V}$, $V_{DS} = 25 \text{ V}$, $f = 1 \text{ MHz}$	9400 1280 460		pF
$t_{d(on)}$ t_r $t_{d(off)}$ t_f	$V_{GS} = 10 \text{ V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = I_T$ $R_G = 1 \Omega$ (external)	45 60 120 45		ns ns ns ns
$Q_{g(on)}$ Q_{gs} Q_{gd}	$V_{GS} = 10 \text{ V}$, $V_{DS} = 0.5 \cdot V_{DSS}$, $I_D = I_T$	330 55 155		nC nC nC
R_{thJC} R_{thCK}	with heatsink compound (0.42 K/m.K; 50 μm)	0.15	0.30 K/W K/W	

$$\begin{aligned}I_{D25} &= 82 \text{ A} \\V_{DSS} &= 500 \text{ V} \\R_{DS(on)} &= 50 \text{ m}\Omega\end{aligned}$$

Features

- Silicon chip on Direct-Copper-Bond substrate
 - High power dissipation
 - Isolated mounting surface
 - 3000 V electrical isolation
- Low drain to tab capacitance(< 25 pF)
- Low $R_{DS(on)}$ HDMOS™ process
- Rugged polysilicon gate cell structure
- Unclamped Inductive Switching (UIS) rated
- Fast intrinsic Rectifier
- UL certified, E 148688

Applications

- DC-DC converters
- Battery chargers
- Switched-mode and resonant-mode power supplies
- DC choppers
- AC motor control

Advantages

- Easy assembly
- Space savings
- High power density

Caution:

These Devices are sensitive to electrostatic discharge. Users should observe proper ESD handling precautions.

Source-Drain Diode

Characteristic Values

($T_1 = 25^\circ\text{C}$, unless otherwise specified)

Symbol		Conditions	min.	typ.	max.
I_s		$V_{GS} = 0 \text{ V}$		85	A
I_{SM}		Repetitive; pulse width limited by T_{JM}		340	A
V_{SD}		$I_F = I_s, V_{GS} = 0 \text{ V}$		1.5	V
t_{rr} Q_{RM} I_{RM}	$\left. \right\} I_F = 50 \text{ A}, -di/dt = 100 \text{ A}/\mu\text{s}, V_R = 100 \text{ V}$		250	ns	
			1.4 13	μC A	

Note: 1) Pulse test, $t \leq 300 \mu\text{s}$, duty cycle $d \leq 2\%$

²⁾ I_T test current: I_T = 25 A

Module

Symbol	Conditions	Maximum Ratings		
T_{VJ}		-40...	+150	°C
T_{stg}		-40...	+125	°C
V_{ISOL}	$I_{ISOL} \leq 1 \text{ mA}; 50/60 \text{ Hz}; t = 1 \text{ s}$	3600	V~	
M_d	Mounting torque (M4)	1.5 - 2.0	Nm	
a	Max. allowable acceleration	50	m/s ²	
Symbol	Conditions	Characteristic Values		
		min.	typ.	max.
d_s	Creepage distance on surface (pin to heatsink)	11.2		mm
d_A	Strike distance in air (pin to heatsink)	11.2		mm
Weight		24		g

Dimensions in mm (1 mm = 0.0394")

