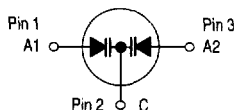


Silicon Tuning Diode

This device is designed for FM tuning, general frequency control and tuning, or any top-of-the-line application requiring back-to-back diode configurations for minimum signal distortion and detuning.

- High Figure of Merit — $Q = 140$ (Typ) @ $V_R = 3.0$ Vdc, $f = 100$ MHz
- Guaranteed Capacitance Range
 $37\text{--}42$ pF @ $V_R = 3.0$ Vdc (MV104)
- Dual Diodes — Save Space and Reduce Cost
- Monolithic Chip Provides Near Perfect Matching — Guaranteed $\pm 1.0\%$ (Max) Over Specified Tuning Range



MV104

**DUAL
 VOLTAGE VARIABLE
 CAPACITANCE DIODE**



**CASE 29-04, STYLE 15
 TO-92 (TO-226AA)**

MAXIMUM RATINGS (EACH DIODE)

Rating	Symbol	Value	Unit
Reverse Voltage	V_R	32	Vdc
Forward Current	I_F	200	mAdc
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	280 2.8	mW mW/ $^\circ\text{C}$
Junction Temperature	T_J	+125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage ($I_R = 10$ μAdc)	$V_{(BR)R}$	32	—	—	Vdc
Reverse Voltage Leakage Current $T_A = 25^\circ\text{C}$ ($V_R = 30$ Vdc) $T_A = 60^\circ\text{C}$	I_R	—	—	50 500	nAdc
Diode Capacitance Temperature Coefficient ($V_R = 4.0$ Vdc, $f = 1.0$ MHz)	TC_C	—	280	—	ppm/ $^\circ\text{C}$

Device	C_T , Diode Capacitance $V_R = 3.0$ Vdc, $f = 1.0$ MHz pF		Q, Figure of Merit $V_R = 3.0$ Vdc $f = 100$ MHz		C_R , Capacitance Ratio C_3/C_{30} $f = 1.0$ MHz	
	Min	Max	Min	Typ	Min	Max
MV104	37	42	100	140	2.5	2.8

TYPICAL CHARACTERISTICS (Each Diode)

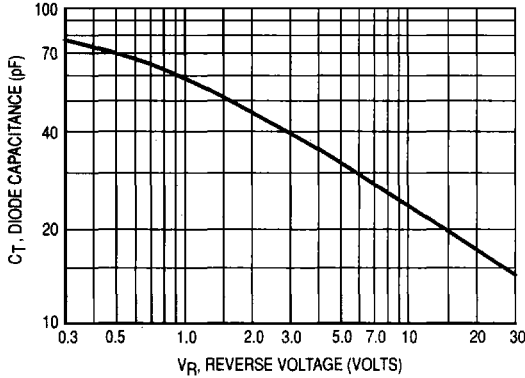


Figure 1. Diode Capacitance (Each Diode)

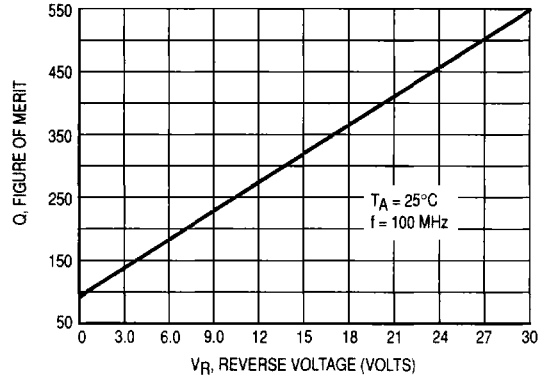


Figure 2. Figure of Merit versus Voltage

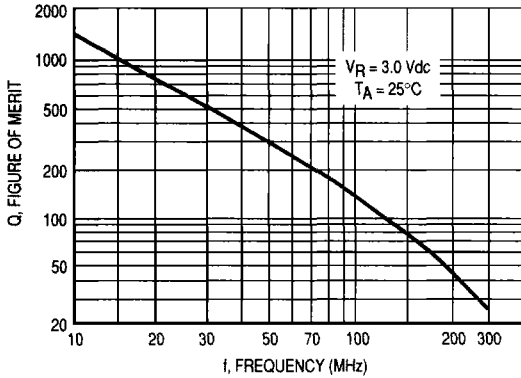


Figure 3. Figure of Merit versus Frequency

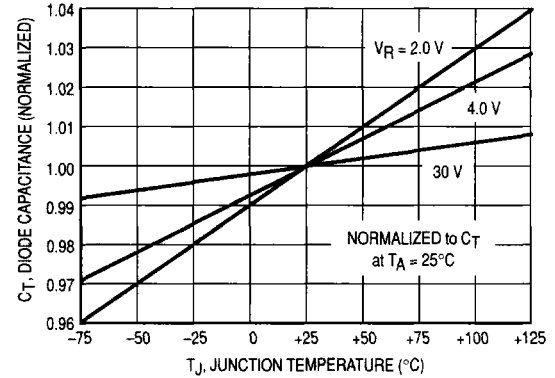


Figure 4. Diode Capacitance versus Temperature

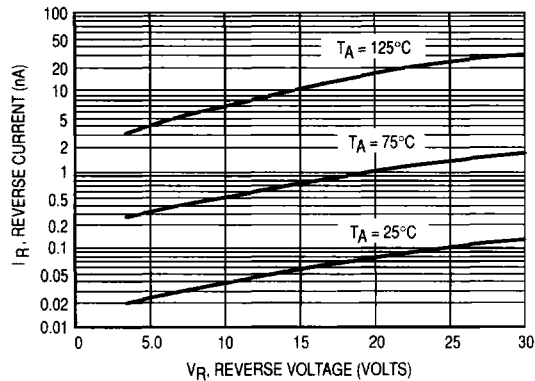


Figure 5. Reverse Current versus Reverse Voltage