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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HZM6.2Z4MWA

Silicon Planar Zener Diode for Surge Absorb

REJ03G0368-0100 Rev.1.00 Oct 01, 2004

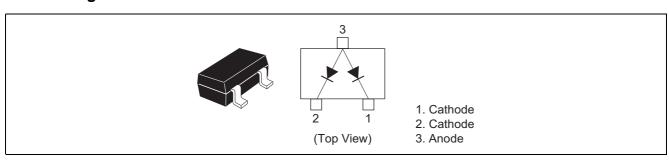
Features

- HZM6.2Z4MWA has two devices in a monolithic, and can absorb surge.
- Low capacitance (C = 4.0 pF Typ / 4.5 pF Max) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HZM6.2Z4MWA	N1	MPAK

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	−55 to +150	°C

Note: Two device total, See Fig.2.

Electrical Characteristics *1

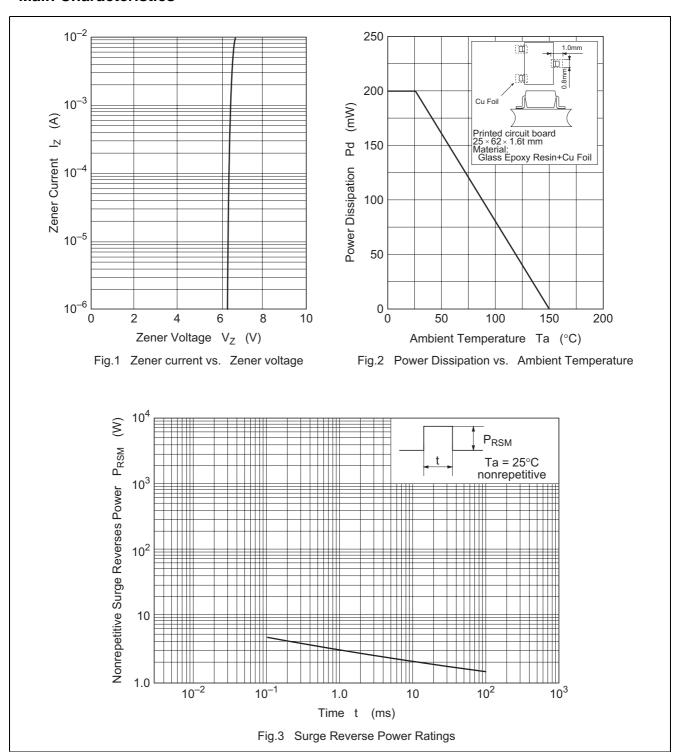
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Zener voltage	V.z.	5.90	_	6.50	V	$I_Z = 5$ mA, 40 ms pulse
Reverse current	I _{.R} .	_	_	3	μΑ	V _R = 5.5 V
Capacitance	С	_	4.0	4.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d .	_	_	60	Ω	$I_Z = 5 \text{ mA}$
ESD-Capability *2	_	8	_	_	kV	C = 150 pF, R = 330 Ω , Both forward
						and reverse direction 10 pulse

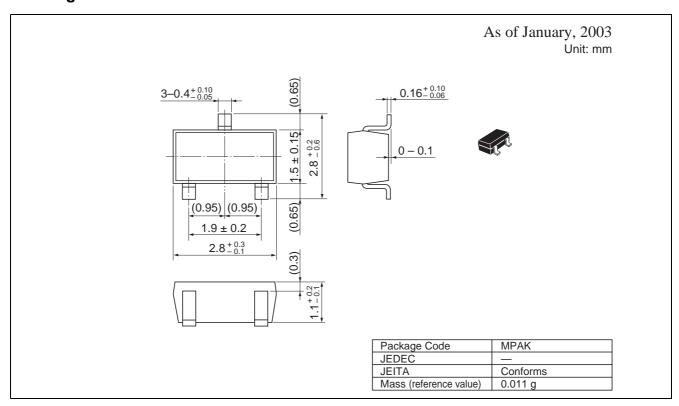
Notes: 1. Per one device.

2. Failure criterion ; $I_{R_{\text{c}}}$ > 3 μA at V_{R} = 5.5 V.

Main Characteristics



Package Dimensions



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Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

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Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001