

# HALF SIZE DIP LOW VOLTAGE 3.3VHCMOS/TTL COMPATIBLE CRYSTAL CLOCK OSCILLATOR



13.2 x 13.2 x 5.5mm

ACHL Series



**RoHS**  
Compliant

## FEATURES:

- Tri-state Enable/Disable option.
- Low supply voltage.
- HCMOS and TTL compatible.
- Tight symmetry option.

## APPLICATIONS:

- Clock signal sources for digital chips and microprocessors.
- Low power applications.

## STANDARD SPECIFICATIONS:

### PARAMETERS

ABRACON P/N	ACHL Series
Frequency:	400kHz to 160MHz
Operating temperature:	0°C to +70°C (see options)
Storage temperature:	- 55°C to + 125°C
Overall frequency stability*:	± 100ppm max. (see options)
Supply voltage (Vdd):	3.3Vdc ± 10%
Input current:	See Table 1
Symmetry:	40/60% max. (see option)
Rise and Fall Times (Tr/Tf):	See Table 1
Output load:	10 TTL or 15 pF (see options)
Output Voltage:	VOH = 0.9 * Vdd min. VOL = 0.1* Vdd max.
Tri State Function:	"1" (VIH >= 4.5 Vdc) or open: Oscillation "0" (VIL < 0.5V): Hi Z
Output Disable/ Enable time:	100 ns max. (for Option "-A" ONLY)
Start-up-time:	10 ms max

Table 1

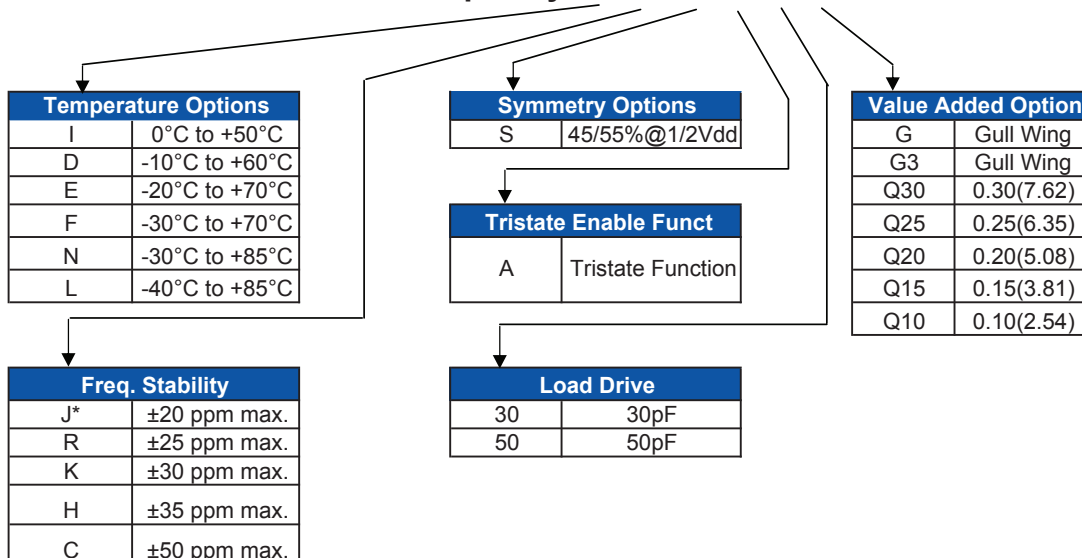
Frequency MHz	Input current mA	Rise/Fall time ns
400.000kHz ≤ F < 24.999MHz	20	10
25.000Hz ≤ F < 100.000MHz	40	10
100.000kHz ≤ F < 160.000MHz	80	5

\* Overall frequency stability includes initial tolerance, temperature stability, and aging.

## OPTIONS & PART IDENTIFICATION:

(Left blank if standard)

ACHL - Frequency - □ - □ - □ - □ - □ - □



\*Temp option I, D, and E only

ABRACON IS  
ISO 9001 / QS 9000  
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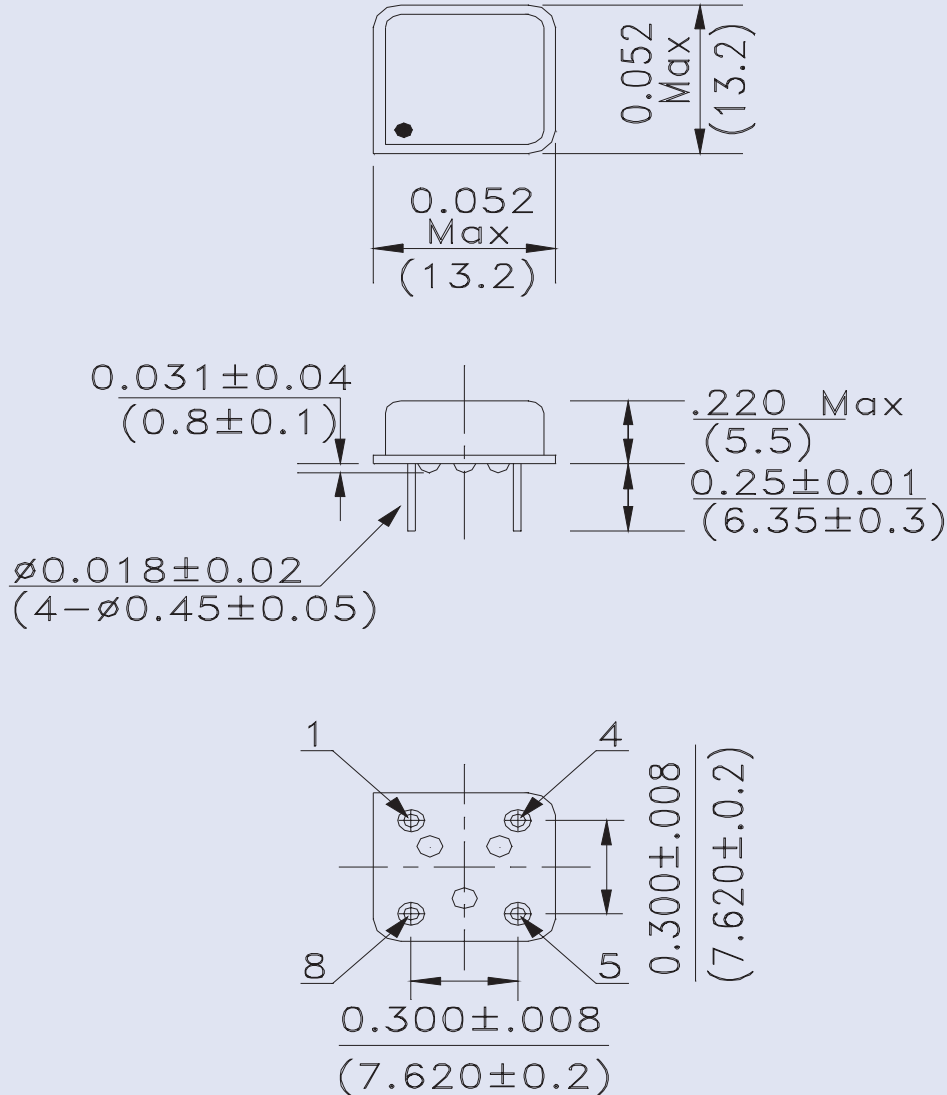


RoHS  
Compliant



13.2 x 13.2 x 5.5mm

## OUTLINE DIMENSIONS:



PIN	FUNCTION
1	NC or Tristate Enable/ Disable
4	GND
5	Output
8	VDD

Note: Recommended using an approximately 0.01uF bypass capacitor between PIN 4 and 8.

Dimensions: inches (mm)