



Features

- · 300W standard full-brick
- Industry-standard footprint
- · Open-frame packaging
- 100°C baseplate operation
- Planar magnetics
- Superior transient response
- 1500V isolation
- VWB booster modules available

Description

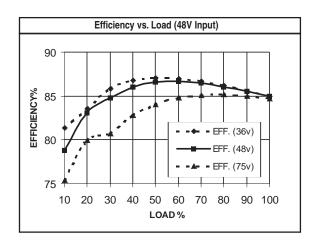
VWS DC/DC converters are industry-standard full-bricks. Featuring a fixed frequency design, the VWS offers excellent transient response and predictable EMI performance. The VWS can be combined with VWB booster modules to provide increased power. VWS converters use 100% surface-mount construction, along with planar magnetics, and are fully compatible with production board washing processes.

Technical Specifications

Input	
Voltage Range	
24 VDC Nominal	18 - 36 VDC
48 VDC Nominal	36 - 72 VDC
Input Reflected Ripple Current	50 mA Pk-Pk
Undervoltage Lockout - Turn on / Turn off	17-15 VDC /35-32 VDC

Output	
Setpoint Accuracy	±1%
Line Regulation V _{in} min V _{in} max., I _{out} rated	0.2% Vout
Load Regulation I _{OUt} min I _{OUt} max., V _{In} nom.	0.2% Vout
Remote Sense Headroom	0.5 VDC
Minimum Output Current	10 % Tout Rated
Dynamic Regulation, Loadstep	25% l _{out}
Pk Deviation	4% Vout
Settling Time	500 ms
Voltage Trim Range	±10%
Current Limit Threshold Range, % of I _{OUT} Rated	110 - 140%
OVP Trip Range	120 - 140% V _{out} Nom.
OVP Type	Self Recovering
Short Circuit/Overcurrent Protection	Shutdown/Hiccup

General	
Turn-On Time	10 ms
Remote Shutdown	Positive Logic
Remote Shutdown Reference	V _{in} Negative
Switching Frequency	500 kHz
Isolation	
Input - Output	1500 VDC
Input - Case	1050 VDC
Output - Case	500 VDC
Temperature Coefficient	0.03 %/°C
Case Temperature	
Operating Range	-40 To +100°C
Storage Range	-40 To +125°C
Thermal Shutdown Range	105 - 115°C
Vibration, 3 Axes, 5 Min Each	5 g, 10 - 55 Hz
MTBF† (Bellcore TR-NWT-000332)	Consult Factory
Safety	UL, cUL, TUV
Weight (Approx.)	6.0 oz



Notes

Specifications typically at 25°C, normal line, and full load, unless otherwise stated.

Soldering Conditions: I/O pins, 260°C, ten seconds; fully compatible with commercial wave-soldering equipment.

Safety: Agency approvals may vary from model to model. Please consult factory for specific model information.

[†] MTBF predictions may vary slightly from model to model.



Model Selection

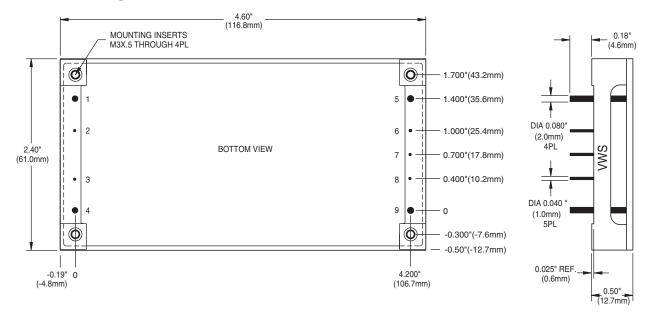
MODEL	INPUT VOLTAGE (VOLTS)	INPUT VOLTAGE Range (Volts)	MAXIMUM INPUT CURRENT (AMPS)*	OUTPUT Voltage (volts)	RATED OUTPUT CURRENT (AMPS)	RIPPLE & NOISE pk-pk (mV)	TYPICAL EFFICIENCY**
VWS250YH-A	24	18-36	21.0	12	20.8	120	81%
VWS250YJ-A	24	18-36	21.0	15	16.7	150	81%
VWS250YK-A	♦ 24	18-36	21.0	24	10.4	200	82%
VWS250Y28-A	♦ 24	18-36	21.0	28	8.9	200	82%
VWS250YL-A	24	18-36	21.0	48	5.2	200	84%
VWS300ZH-A	48	36-72	10.5	12	25.0	120	85%
VWS300ZJ-A	48	36-72	10.5	15	20.0	150	84%
VWS300ZK-A	48	36-72	10.5	24	12.5	200	85%
VWS300Z28-A	48	36-72	10.5	28	10.7	200	86%

NOTES: • Advanced product release - consult factory.

- * Maximum input current at minimum input voltage, maximum rated output power.
- ** At nominal V_{in} , rated output.

Model numbers highlighted in yellow or shaded are not recommended for new designs.

Mechanical Drawing



Thermal Impedance		
Natural Convection 100 LFM 200 LFM 300 LFM 400 LFM	5.4 °C/W 3.8 °C/W 2.5 °C/W 1.7 °C/W 1.6 °C/W	
Note: Thermal impedance data is dependent on many environmental factors. The exact thermal performance should be validated for specific application.		

Pin	Function	
1	^{-V} in	
2	Gate out	
3	Gate in	
4	^{+V} in	
5	⁻ [∨] out	
6	-Sense	
7	Trim	
8	+Sense	
9	^{+V} out	

Tolerances		
Inches: .XX ± 0.040 .XXX ± 0.010	(Millimeters) .X ± 0.5 .XX ± 1.0	
Pin: ± 0.002	± 0.05	
(Dimensions as listed unless otherwise specified.)		



This page is offered as a reference. Consult factory for actual availability of options. When ordering equipment options, use the following suffix information. Select preferred option(s) and add the suffix to the model number. Ordering option examples are located below the options table.

OPTION	SUFFIX	APPLICABLE SERIES	REMARKS
Negative Logic	N	HAS, HBD, HBS, HES, HLS, HLD, LES, QBS, QES, QLS, TES, TQD	TTL "Low" Turns Module ON TTL "High" Turns Module OFF
Lucent-Compatible Trim	Т	HAS, HBD, HBS, HES, HLS, QBS, QES, QLS	
Trim	1	IAS, LES	
Enable	2	IAD, IAS, LES, SMS	
Trim and Enable	3	IAS, LES	
Pin Length and Heatsink Options			Standard Pin Length is 0.180" (4.6mm)
0.110" (2.8mm) Pin Length	8	All Leaded Models	
0.150" (3.8mm) Pin Length	9	All Leaded Models	
0.24" (6.1mm) Horizontal Heatsink	1H	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad
0.24" (6.1mm) Vertical Heatsink	1V	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad
0.45" (11.4mm) Horizontal Heatsink	2H	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad
0.45" (11.4mm) Vertical Heatsink	2V	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad
0.95" (24.1mm) Horizontal Heatsink	3H	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad
0.95" (24.1mm) Vertical Heatsink	3V	All 1/4-Bricks, 1/2-Bricks, 3/4-Bricks, Full-Bricks (Except HLS, HLD, QLS, TLD, and TKD Packages)	Includes Thermal Pad

Example Options:

HBS050ZG-ANT3V = HBS050ZG-A with negative logic, Lucent-compatible trim, and 0.95" vertical heatsink. LES015YJ-3N = LES015YJ with optional trim and enable, negative logic.

QBS066ZG-AT8 = QBS066ZG-A with Lucent-compatible trim and 0.110" pin length.

NUCLEAR AND MEDICAL APPLICATIONS - Power-One products are not authorized for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems without the express written consent of the respective divisional president of Power-One, Inc.

TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.