



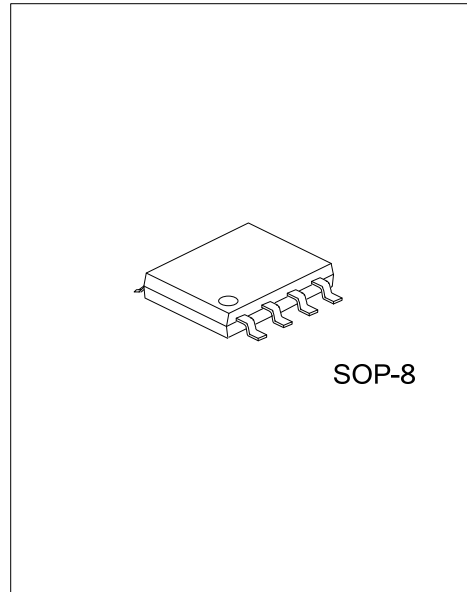
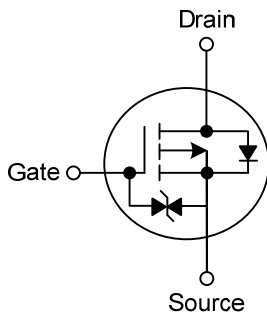
## P-CHANNEL ENHANCEMENT MODE

### DESCRIPTION

The UTC **UT9435HZ** is a P-channel enhancement power MOSFET. It has low gate charge, fast switching speed and perfect  $R_{DS(ON)}$ .

This device is generally applied in power management applications.

### SYMBOL



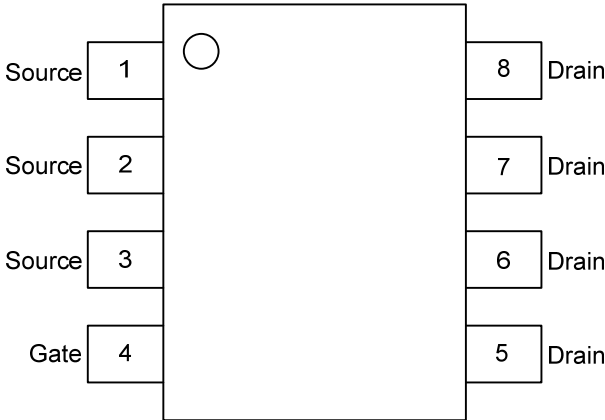
### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT9435HZL-S08-R	UT9435HZG-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel
UT9435HZL-S08-T	UT9435HZG-S08-T	SOP-8	S	S	S	G	D	D	D	D	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT9435HZL-S08-R</p> <ul style="list-style-type: none"> <li>(1) Packing Type</li> <li>(2) Package Type</li> <li>(3) Lead Free</li> </ul>	<ul style="list-style-type: none"> <li>(1) R: Tape Reel, T: Tube</li> <li>(2) S08: SOP-8</li> <li>(3) G: Halogen Free, L: Lead Free</li> </ul>
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■ PIN CONFIGURATION



■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain to Source Voltage	V <sub>DSS</sub>	-30	V
Gate to Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 3)	I <sub>D</sub>	±5.3	A
Pulsed Drain Current (Note 1, 2)	I <sub>DM</sub>	±20	A
Power Dissipation	P <sub>D</sub>	2.5	W
Junction Temperature	T <sub>J</sub>	+150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ <sub>JA</sub>	50	°C/W

■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V			-1	μA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±5	μA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1		-3	V
Drain-Source On-State Resistance (Note 2)	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5.3A		44	50	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4.2A		74	90	mΩ
On State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> =-5V, V <sub>GS</sub> =-10V	-20			V
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1.0MHz		1040		pF
Output Capacitance	C <sub>OSS</sub>			420		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			150		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge (Note 2)	Q <sub>G</sub>	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.6A		22.5	29	nC
Gate-Source Charge	Q <sub>GS</sub>			2		nC
Gate-Drain Charge	Q <sub>GD</sub>			6		nC
Turn-ON Delay Time (Note 2)	t <sub>D(ON)</sub>	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1A, V <sub>GEN</sub> =-10V, R <sub>G</sub> =6Ω,		19	26	ns
Turn-ON Rise Time	t <sub>R</sub>			9	13	ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			74	105	ns
Turn-OFF Fall Time	t <sub>F</sub>			36	50	ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage (Note 2)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-5.3A		-0.84	-1.3	V

Notes: 1. Pulse width limited by T<sub>J(MAX)</sub>.

2. Pulse width ≤300μs, duty cycle ≤2%.

3. Surface Mounted on 1in<sup>2</sup> copper pad of FR4 board

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