

# NE5550979A

## Silicon Power LDMOS FET

R09DS0031EJ0100 Rev.1.00 Nov 25, 2011

### **FEATURES**

• High Output Power :  $P_{out} = 39.5 \text{ dBm TYP.}$  ( $V_{DS} = 7.5 \text{ V}$ ,  $I_{Dset} = 200 \text{ mA}$ , f = 460 MHz,  $P_{in} = 25 \text{ dBm}$ )
• High power added efficiency :  $\eta_{add} = 66\% \text{ TYP.}$  ( $V_{DS} = 7.5 \text{ V}$ ,  $I_{Dset} = 200 \text{ mA}$ , f = 460 MHz,  $P_{in} = 25 \text{ dBm}$ )
• High Linear gain :  $G_L = 22 \text{ dB TYP.}$  ( $G_L = 22 \text{ dB TYP.}$ ) :  $G_L = 22 \text{ dB TYP.}$  ( $G_L = 22 \text{ dB TYP.}$ ) in G

• High ESD tolerance : ESD tolerance > 8 kV (IEC61000-4-2, Contact discharge)

• Suitable for VHF to UHF-BAND Class-AB power amplifier.

### **APPLICATIONS**

150 MHz Band Radio System

• 460 MHz Band Radio System

• 900 MHz Band Radio System

#### ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
NE5550979A	NE5550979A-AZ	79A	W6	12 mm wide embossed taping
		(Pb Free)		Gate pin faces the perforation side of the tape
NE5550979A-T1	NE5550979A-T1-AZ			12 mm wide embossed taping
				Gate pin faces the perforation side of the tape
				Qty 1 kpcs/reel
NE5550979A-T1A	NE5550979A-T1A-AZ			12 mm wide embossed taping
				Gate pin faces the perforation side of the tape
				Qty 5 kpcs/reel

Remark To order evaluation samples, please contact your nearby sales office.

Part number for sample order: NE5550979A

## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ , unless otherwise specified)

Operation in excess of any one of these parameters may result in permanent damage.

Parameter	Symbol	Ratings	Unit
Drain to Source Voltage	$V_{DS}$	30	V
Gate to Source Voltage	$V_{GS}$	6.0	V
Drain Current	I <sub>DS</sub>	3.0	Α
Total Power Dissipation Note	P <sub>tot</sub>	25	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	−55 to +150	°C

Note: Value at  $T_C = 25^{\circ}C$ 

#### **CAUTION**

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

# RECOMMENDED OPERATING RANGE ( $T_A = 25$ °C)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Drain to Source Voltage	$V_{DS}$		_	7.5	9.0	V
Gate to Source Voltage	$V_{GS}$		1.65	2.20	2.85	V
Drain Current	I <sub>DS</sub>		_	1.7	_	Α
Input Power	Pin	f = 460 MHz, V <sub>DS</sub> = 7.5 V	_	25	30	dBm

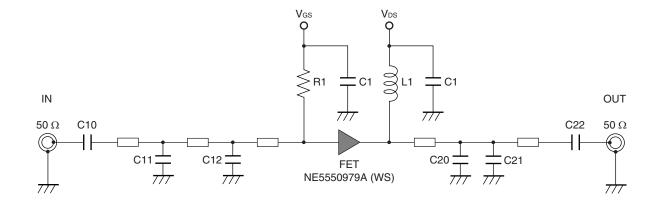
# **ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, unless otherwise specified)**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Gate to Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 6.0 V	_	_	100	nA
Drain to Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 25 V	_	_	10	μΑ
(Zero Gate Voltage Drain Current)						
Gate Threshold Voltage	$V_{th}$	$V_{DS} = 7.5 \text{ V}, I_{DS} = 1.0 \text{ mA}$	1.15	1.65	2.25	V
Drain to Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>DS</sub> = 10 μA	25	37	_	V
Transconductance	G <sub>m</sub>	$V_{DS} = 7.5 \text{ V}, I_{DS} = 700\pm100 \text{ mA}$	1.8	2.2	2.9	S
Thermal Resistance	R <sub>th</sub>	Channel to Case	_	5.0	_	°C/W
RF Characteristics						
Output Power	P <sub>out</sub>	$f = 460 \text{ MHz}, V_{DS} = 7.5 \text{ V},$	38.5	39.5	_	dBm
Drain Current	I <sub>DS</sub>	P <sub>in</sub> = 25 dBm,	_	1.70	_	Α
Power Drain Efficiency	$\eta_{\sf d}$	I <sub>Dset</sub> = 200 mA (RF OFF)	_	68	_	%
Power Added Efficiency	$\eta_{add}$		_	66	_	%
Linear Gain	G <sub>L</sub> Note 1		_	22.0	_	dB
Output Power	P <sub>out</sub>	f = 157 MHz, V <sub>DS</sub> = 7.5 V,	_	39.6	_	dBm
Drain Current	I <sub>DS</sub>	P <sub>in</sub> = 23 dBm,	_	1.60	_	Α
Power Drain Efficiency	$\eta_{\sf d}$	I <sub>Dset</sub> = 200 mA (RF OFF)	_	75	_	%
Power Added Efficiency	$\eta_{add}$		_	73	_	%
Linear Gain	G <sub>L</sub> Note 2		_	25.0	_	dB
Output Power	P <sub>out</sub>	$f = 900 \text{ MHz}, V_{DS} = 7.5 \text{ V},$	_	38.6	_	dBm
Drain Current	I <sub>DS</sub>	P <sub>in</sub> = 27 dBm,	_	1.76	_	Α
Power Drain Efficiency	$\eta_{d}$	I <sub>Dset</sub> = 200 mA (RF OFF)	_	55	_	%
Power Added Efficiency	$\eta_{add}$		_	52	_	%
Linear Gain	G <sub>L</sub> Note 1		_	16.0	_	dB

Note 1:  $P_{in}$  = 10 dBm Note 2:  $P_{in}$  = 5 dBm

**Remark** DC performance is 100% testing. RF performance is testing several samples per wafer. Wafer rejection criteria for standard devices is 1 reject for several samples.

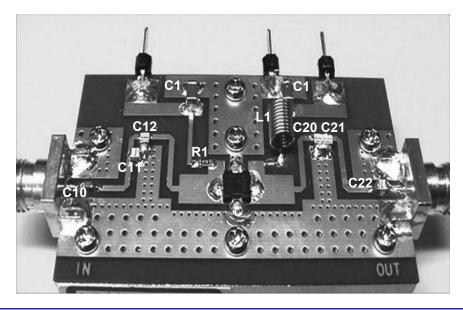
## TEST CIRCUIT SCHEMATIC FOR 460 MHz



### COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

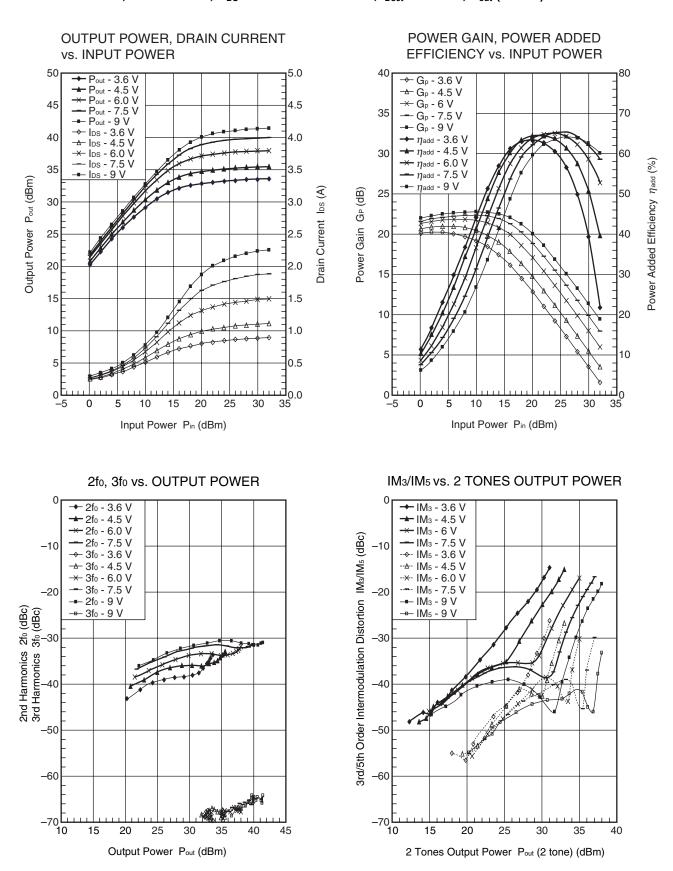
Symbol	Value	Туре	Maker
C1	1 <i>μ</i> F	GRM31CR72A105KA01B	Murata
C10	100 pF	GRM1882C1H101JA01	Murata
C11	24 pF	ATC100A240JW	American Technical
			Ceramics
C12	2.4 pF	ATC100A2R4BW	American Technical
			Ceramics
C20	27 pF	ATC100A270JW	American Technical
			Ceramics
C21	1.8 pF	ATC100A1R8BW	American Technical
			Ceramics
C22	100 pF	ATC100A101JW	American Technical
			Ceramics
R1	4.7 kΩ	1/10 W Chip Resistor	SSM
		SSM_RG1608PB472	
L1	123 nH	$\phi$ 0.5 mm, $\phi$ D = 3 mm, 10 Turns	Ohesangyou
PCB	_	R4775, t = 0.4 mm, $\varepsilon$ r = 4.5, size = 30 × 48 mm	Panasonic
SMA Connecter	_	WAKA 01K0790-20	WAKA

## COMPONENT LAYOUT OF TEST CIRCUIT FOR 460 MHz



## TYPICAL CHARACTERISTICS 1 ( $T_A = 25^{\circ}C$ )

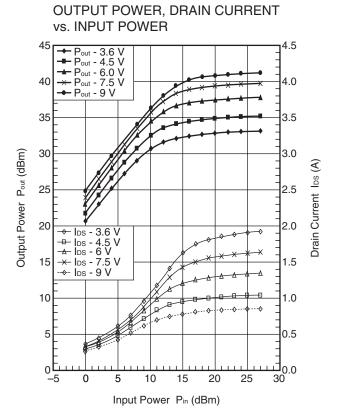
R: f = 460 MHz,  $V_{DS} = 3.6/4.5/6/7.5/8.4/9 V$ ,  $I_{Dset} = 200 mA$ ,  $P_{in} = 0$  to 32 dBm IM: f1 = 460 MHz, f2 = 461 MHz,  $V_{DS} = 3.6/4.5/6/7.5/8.4/9 V$ ,  $I_{Dset} = 200 mA$ ,  $P_{out}$  (2 tone) = 12 to 38 dBm

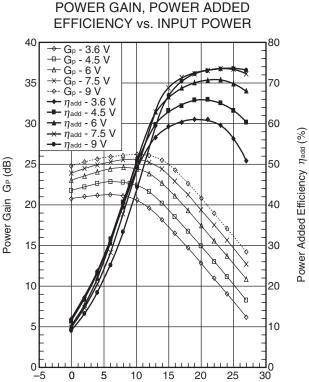


**Remark** The graphs indicate nominal characteristics.

## TYPICAL CHARACTERISTICS 2 ( $T_A = 25^{\circ}C$ )

### R: f = 157 MHz, $V_{DS} = 3.6/4.5/6/7.5/9 \text{ V}$ , $I_{Dset} = 200 \text{ mA}$ , $P_{in} = 0 \text{ to } 27 \text{ dBm}$



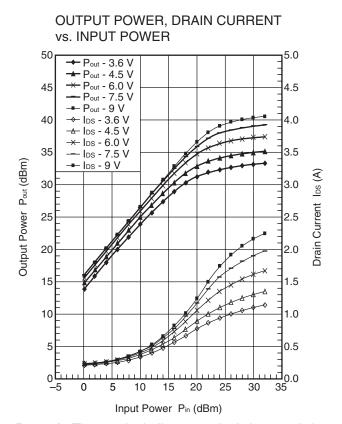


Input Power Pin (dBm)

POWER GAIN, POWER ADDED

## TYPICAL CHARACTERISTICS 3 (T<sub>A</sub> = 25°C)

RF:  $f = 900 \text{ MHz V}_{DS} = 3.6/4.5/6/7.5/9 \text{ V}$ ,  $I_{Dset} = 200 \text{ mA}$ ,  $P_{in} = 0 \text{ to } 32 \text{ dBm}$ 



EFFICIENCY vs. INPUT POWER 80 → G<sub>p</sub> - 3.6 V → G<sub>p</sub> - 6 V 35 70 --- G<sub>p</sub> - 7.5 V --- G<sub>p</sub> - 9 V - η<sub>add</sub> - 3.6 V → η

add - 4.5 V 60 30  $\rightarrow \eta$ add - 6.0 V - η<sub>add</sub> - 7.5 V -- η<sub>add</sub> - 9 V (dB) 25 50 Efficiency යු Power Gain 20 Added Power 10 20 10 0 0 5 10 15 20 25 30 35 Input Power Pin (dBm)

**Remark** The graphs indicate nominal characteristics.

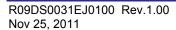
## **S-PARAMETERS**

S-parameters and noise parameters are provided on our Web site in a format (S2P) that enables the direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

Click here to download S-parameters.

[RF and Microwave]  $\rightarrow$  [Device Parameters]

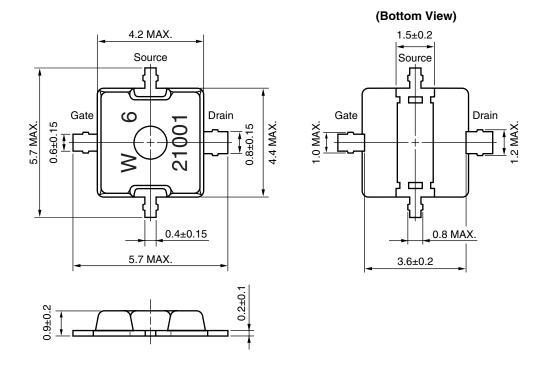
URL http://www2.renesas.com/microwave/en/download.html



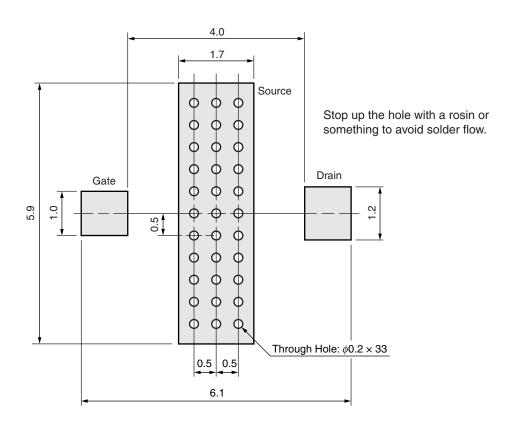


## **PACKAGE DIMENSIONS**

## 79A (UNIT: mm)



## 79A PACKAGE RECOMMENDED P.C.B. LAYOUT (UNIT: mm)



## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature)	: 260°C or below	IR260
	Time at peak temperature	: 10 seconds or less	
	Time at temperature of 220°C or higher	: 60 seconds or less	
	Preheating time at 120 to 180°C	: 120±30 seconds	
	Maximum number of reflow processes	: 3 times	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	
Wave Soldering	Peak temperature (molten solder temperature)	: 260°C or below	WS260
	Time at peak temperature	: 10 seconds or less	
	Preheating temperature (package surface temperature)		
		: 120°C or below	
	Maximum number of flow processes	: 1 time	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	
Partial Heating	Peak temperature (terminal temperature)	: 350°C or below	HS350
	Soldering time (per side of device)	: 3 seconds or less	
	Maximum chlorine content of rosin flux (% mass)	: 0.2% (Wt.) or below	

## **CAUTION**

Do not use different soldering methods together (except for partial heating).

**Revision History** 

# NE5550979A Data Sheet

		Description		
Rev.	Date	Page	Summary	
1.00	Nov 25, 2011	-	First edition issued	

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