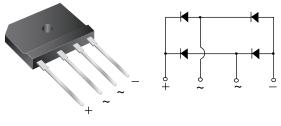


Vishay General Semiconductor

# Low V<sub>F</sub> Single-Phase Single In-Line Bridge Rectifiers



Case Style GSIB-5S

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	25 A			
V <sub>RRM</sub>	600 V			
I <sub>FSM</sub>	550 A			
I <sub>R</sub>	10 µA			
V <sub>F</sub> at I <sub>F</sub> = 12.5 A, T <sub>A</sub> = 125 °C	0.76 V			
T <sub>J</sub> max.	150 °C			

## FEATURES

- UL recognition file number E54214, Vol. 1
- Thin single in-line package
- Oxide planar chip junction
- Low forward voltage drop
- High surge current capability
- High case dielectric strength of 2500 V<sub>BMS</sub>
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

## **TYPICAL APPLICATIONS**

General purpose use in AC/DC bridge full wave rectification for switching power supply, home appliances and white-goods applications specially for Telecom power supply, high efficiency desktop PC and server SMPS:

## **MECHANICAL DATA**

Case: GSIB-5S

Epoxy meets UL 94 V-0 flammability rating

Base  $\ensuremath{\mathsf{P/N}}\xspace{-M3}$  - halogen-free,  $\ensuremath{\mathsf{RoHS}}\xspace$  compliant, and commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked on body

**Mounting Torque:** 10 cm-kg (8.8 in-lbs) maximum **Recommended Torque:** 5.7 cm-kg (5 in-lbs)

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	LVB2560	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	600	V	
Maximum average forward rectified output current at	T <sub>C</sub> = 105 °C	I <sub>O</sub> <sup>(1)</sup>	25	A	
	T <sub>A</sub> = 25 °C	I <sub>O</sub> <sup>(2)</sup>	3.6		
Non-repetiitive peak forward surge current 8.3 ms single sine-wave, $T_J = 25 \ ^{\circ}C$		I <sub>FSM</sub>	550	А	
Rating for fusing (t < 8.3 ms)	T <sub>J</sub> = 25 °C	l <sup>2</sup> t	1255	A <sup>2</sup> s	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150	°C	

#### Notes

<sup>(1)</sup> Unit case mounted on aluminum plate heatsink

<sup>(2)</sup> Units mounted on PCB without heatsink



ROHS COMPLIANT

HALOGEN

FREE

# LVB2560



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	1050	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.89	0.92	v
	I <sub>F</sub> = 12.5 A	T <sub>A</sub> = 125 °C		0.76	-	
Reverse current per diode	V <sub>R</sub> = 600 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	0.2	10	μA
		T <sub>A</sub> = 125 °C		140	-	
Typical reverse recovery time	$I_{\rm F} = 0.5  \text{A},  I_{\rm R} = 7$	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		1.8	-	μs
Typical junction capacitance	4.0 V, 1 MHz		CJ	330	-	pF

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	LVB2560	UNIT	
Maximum thermal resistance	R <sub>0JA</sub> <sup>(2)</sup>	25	°C/W	
	R <sub>0JC</sub> <sup>(1)</sup>	1.0	0/10	

#### Notes

(1) With heatsink

(2) Without heatsink, free air

EMC SURGE IMMUNITY TEST STANDARD ( $T_A = 25 \text{ °C}$ , unless otherwise noted)						
STANDARD	STANDARD TEST TYPE TEST CONDITIONS		SYMBOL	CLASS	VALUE	
IEC 61000-4-5	Power supply coupling mode, line to line	1.2/50 $\mu s$ waveform, R = 2 $\Omega,$ T_A = 25 °C $^{(1)}$	V <sub>PEAK</sub>	-	6 kV maximum	

#### Note

<sup>(1)</sup> Immunity to IEC 61000-4-5 peak pulse voltage test, 1.2/50  $\mu$ s, 2  $\Omega$ , 5 times each of positive and negative polarity test

ORDERING INFORMATION (Example)							
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE BASE QUANTIT				DELIVERY MODE			
LVB2560-M3/45	7.1	45	20	Tube			

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

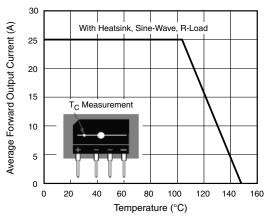
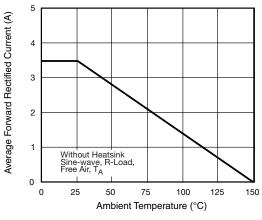


Fig. 1 - Derating Curve Output Rectified Current







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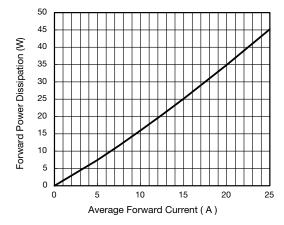


Fig. 3 - Forward Power Dissipation

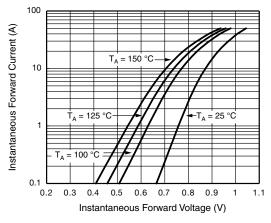


Fig. 4 - Typical Forward Characteristics Per Diode

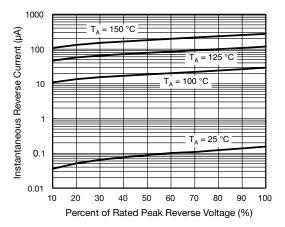


Fig. 5 - Typical Reverse Characteristics Per Diode

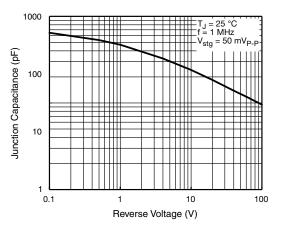
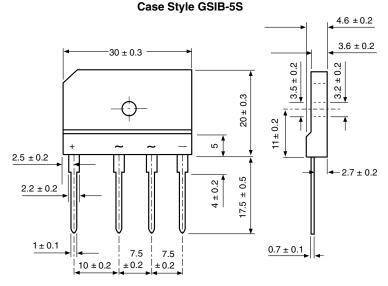


Fig. 6 - Typical Junction Capacitance Per Diode



PACKAGE OUTLINE DIMENSIONS in millimeters



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