

Single-Stage Power Factor Corrected Off-Line Switching Regulators

Features and Benefits

- Integrated on-width control circuit (it realizes high power factor by average current control)
- Integrated startup circuit (no external startup circuit necessary)
- Integrated soft-start circuit (reduces power stress during start-up on the incorporated power MOSFET and rectifier)
- Integrated bias assist circuit (improves the startup performance, suppresses V_{CC} voltage droop during operation, allows reduction of V_{CC} capacitor value as well as use of a ceramic capacitor)
- Integrated Leading Edge Blanking (LEB) circuit
- Integrated maximum on-time limit circuit
- Dual-chip structure, with an avalanche-guaranteed power MOSFET (allows simplified surge suppressing circuits)
- Protection features:
 - Overcurrent protection (OCP): pulse-by-pulse
 - Overvoltage protection (OVP): latched shutdown
 - Overload protection (OLP): latched shutdown
 - Thermal shutdown (TSD): latched shutdown

Packages: 7-pin TO-220F



Description

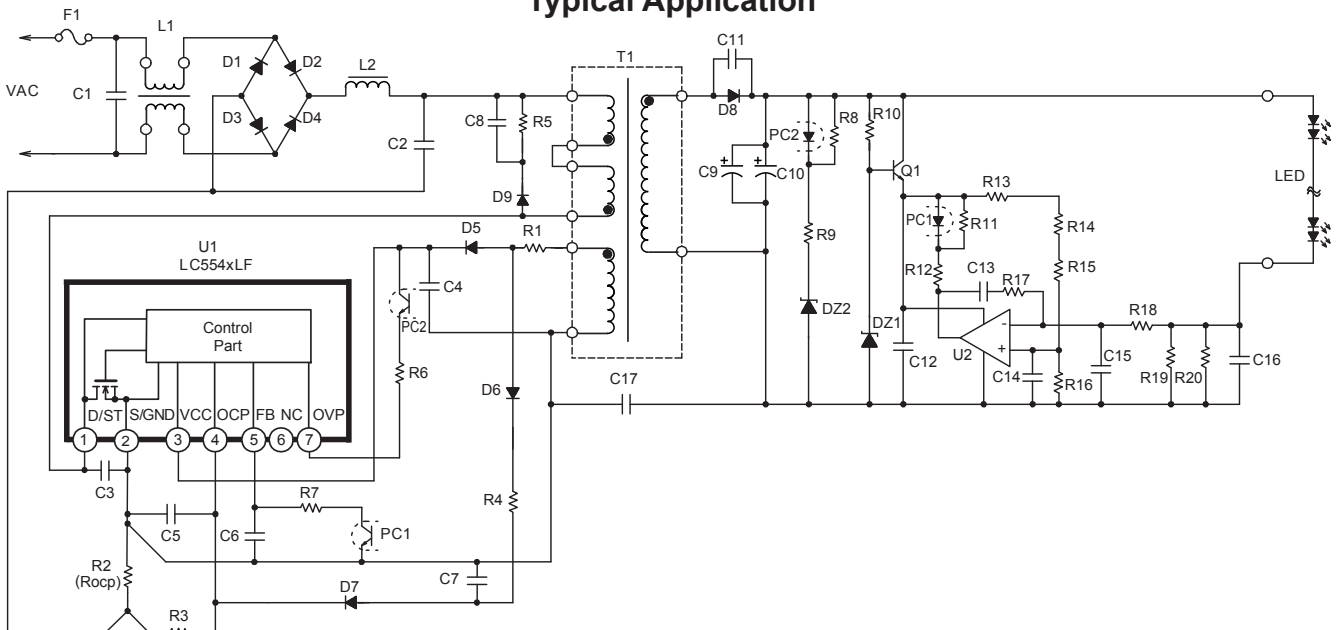
LC5540LF series is a quasi-resonant topology switching power supply IC, designed for input capacitorless applications, and making it possible for systems to comply with the harmonics standard (IEC61000-3-2 class C). It incorporates separate controller and power MOSFET chips. The controller adapts the average current control method for realizing high power factors, and the quasi-resonant topology contributes to high efficiency and low EMI noise. The rich set of protection features helps to realize low component counts, and high performance-to-cost power supply.

The LC5540LF devices are intended for isolated designs. The incorporated MOSFET has a $V_{DSS(min)}$ rating from 650 V (LC5546LF and LC5547LF) to 800 V (LC5549LF). The $R_{DS(on)(max)}$ is 1.1 Ω (LC5547LF) to 1.9 Ω (LC5546LF). It is capable of a maximum output power of 80 W on 230 VAC supply to 55 W on universal supply (LC5547LF) based on the thermal rating. Note that the maximum output power can be up to 120% to 140% of this value. However, it may be limited in applications with low output voltage or short duty cycle.

Applications

- LED lighting fixtures
- LED light bulbs

Typical Application



LC5540LF Series

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Selection Guide

Part Number	MOSFET $V_{DSS(min)}$ (V)	$R_{DS(on)}$ (max) (Ω)	PWM Operation Frequency, $f_{osc}(typ)$ (kHz)	On-Time $t_{ON(MAX)}(typ)$ (μ s)	P_{OUT}^* (W)	
					230 VAC	Universal
LC5546LF	650	1.9	40	17.5	60	40
LC5547LF		1.1			80	55
LC5549LF	800	1.7			60	40

*Based on the thermal rating; the allowable maximum output power can be up to 120% to 140% of this value. However, maximum output power may be limited in such an application with low output voltage or short duty cycle.

The polarity value for current specifies a sink as "+," and a source as "–," referencing the IC.

Absolute Maximum Ratings Unless specifically noted, T_A is 25°C

Characteristic	Symbol	Notes	Pins	Rating	Unit
Drain Current ¹	I_{DPeak}	LC5546LF	1 – 2	9.2	A
		LC5547LF		13.0	A
		LC5549LF		10.5	A
Single Pulse Avalanche Energy ²	E_{AS}	LC5546LF	1 – 2	99	mJ
		LC5547LF		233	mJ
		LC5549LF		92	mJ
Input Voltage for Control Part (MIC)	V_{CC}		3 – 2	35	V
OCP Pin Voltage	V_{OCP}		4 – 2	–2.0 to 5.0	V
FB Pin Voltage	V_{FB}		5 – 2	–0.3 to 7.0	V
OVP Pin Voltage	V_{OVP}		7 – 2	–0.3 to 5.0	V
Allowable Power Dissipation of MOSFET ³	P_{D1}	LC5546LF	1 – 2	20.2	W
		LC5547LF		23.6	W
		LC5549LF			
		Without heatsink		1.8	W
	T_F		—	–20 to 115	°C
Operating Ambient Temperature	T_{OP}		—	–55 to 115	°C
Storage Temperature	T_{stg}		—	–55 to 125	°C
Channel Temperature	T_{ch}		—	150	°C

¹Refer to MOSFET Safe Operating Area Curve.

²Refer to MOSFET Avalanche Energy Derating Coefficient Curve.

³Refer to MOSFET Temperature versus Power Dissipation Curve.

LC5540LF Series

Single-Stage Power Factor Corrected Off-Line Switching Regulators

Electrical Characteristics of Control Part (MIC) Unless specifically noted, T_A is 25°C, $V_{CC} = 20$ V

Characteristic	Symbol	Test Conditions	Pins	Min.	Typ.	Max.	Unit
Power Supply Startup Operation							
Operation Start Voltage	$V_{CC(ON)}$		3 – 2	13.8	15.1	17.3	V
Operation Stop Voltage*	$V_{CC(OFF)}$		3 – 2	8.4	9.4	10.7	V
Circuit Current in Operation	$I_{CC(ON)}$		3 – 2	–	–	4.7	mA
Startup Circuit Operation Voltage	$V_{STARTUP}$		1 – 2	18	21	24	V
Startup Current	$I_{CC(STARTUP)}$	$V_{CC} = 13$ V	3 – 2	–8.5	–4.0	–1.5	mA
Startup Current Threshold Biasing Voltage*	$V_{CC(BIAS)}$		3 – 2	9.5	11.0	12.5	V
Normal Operation							
PWM Operation Frequency	f_{OSC}		1 – 2	33	40	47	kHz
Maximum On-Time	$t_{ON(MAX)}$		1 – 2	14	17.5	21	μs
FB Pin Control Minimum Voltage	$V_{FB(MIN)}$		5 – 2	0.50	0.85	1.20	V
Maximum Feedback Current	$I_{FB(MAX)}$		5 – 2	–40	–25	–10	μA
Leading Edge Blanking Time	$t_{ON(LEB)}$		4 – 2	–	600	–	ns
Quasi-Resonant Operation Threshold Voltage-1	$V_{BD(TH1)}$		4 – 2	0.14	0.24	0.34	V
Quasi-Resonant Operation Threshold Voltage-2	$V_{BD(TH2)}$		4 – 2	0.11	0.16	0.21	V
Protected Operation							
OCP Pin Overcurrent Protection (OCP) Threshold Voltage	V_{OCP}		4 – 2	–0.66	–0.60	–0.54	V
OCP Pin Source Current	I_{OCP}		4 – 2	–120	–40	–10	μA
OCP Pin Overvoltage Protection (OVP) Operation Voltage	$V_{BD(OVP)}$		4 – 2	2.2	2.6	3.0	V
Overload Protection (OLP) Threshold Voltage	$V_{FB(OLP)}$		5 – 2	4.1	4.5	4.9	V
OVP Pin OVP Threshold Voltage	$V_{OVP(OVP)}$		7 – 2	1.6	2.0	2.4	V
VCC Pin OVP Threshold Voltage	$V_{CC(OVP)}$		3 – 2	28.5	31.5	34.0	V
Thermal Shutdown Activating Temperature	$T_{J(TSD)}$		–	135	–	–	°C

* $V_{CC(BIAS)} > V_{CC(OFF)}$ always.

LC5540LF Series

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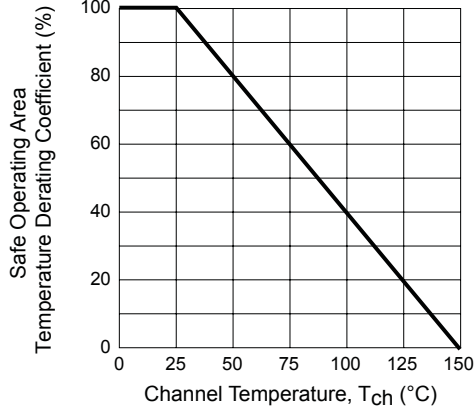
Electrical Characteristics of MOSFET Unless specifically noted, T_A is 25°C

Characteristic	Symbol	Test Conditions	Pins	Min.	Typ.	Max.	Unit
Drain-to-Source Breakdown Voltage	V_{DSS}	LC5546LF LC5547LF	1 – 2	650	—	—	V
		LC5549LF		800	—	—	V
Drain Leakage Current	I_{DSS}			—	—	300	μ A
On-Resistance	$R_{DS(ON)}$	LC5546LF	1 – 2	—	—	1.9	Ω
		LC5547LF		—	—	1.1	Ω
		LC5549LF		—	—	1.7	Ω
Switching Time	t_f	LC5546LF LC5547LF	1 – 2	—	—	400	ns
		LC5549LF		—	—	300	ns
Thermal Resistance*	$R_{\theta ch-F}$	LC5546LF	—	—	—	3.1	°C/W
		LC5547LF		—	—	2.2	°C/W
		LC5549LF					

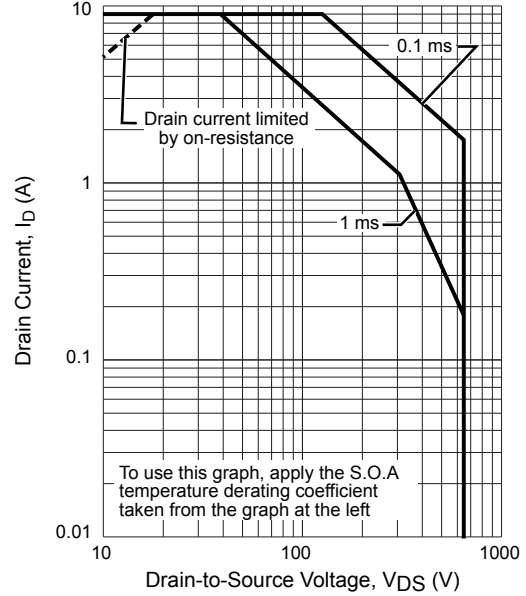
*The thermal resistance between the channels of the MOSFET and the internal frame.

Characteristic Performance LC5546LF

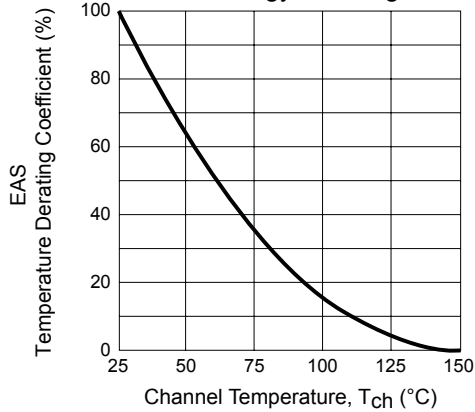
S. O. A. Temperature Derating Coefficient Curve



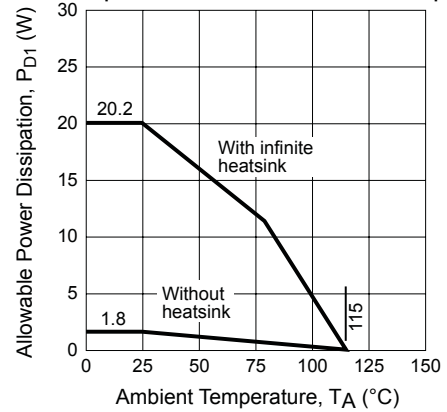
MOSFET Safe Operating Area Curve



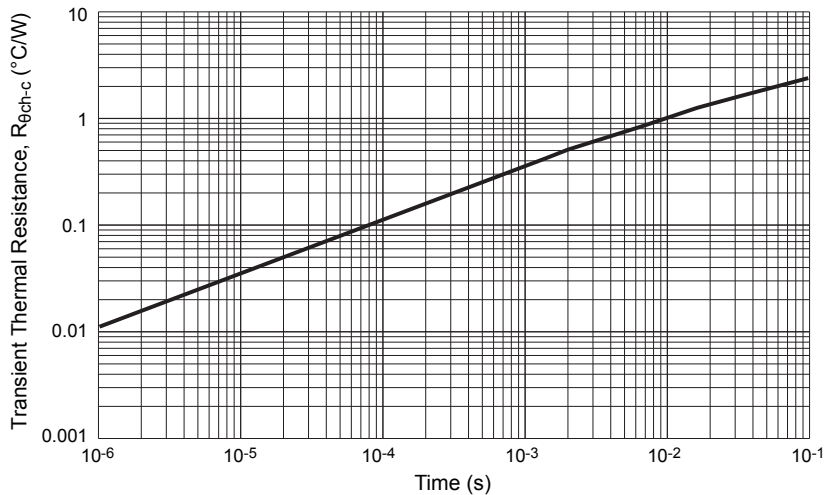
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve

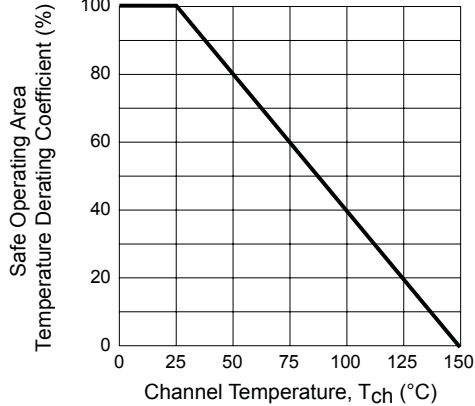


Transient Thermal Resistance Curve

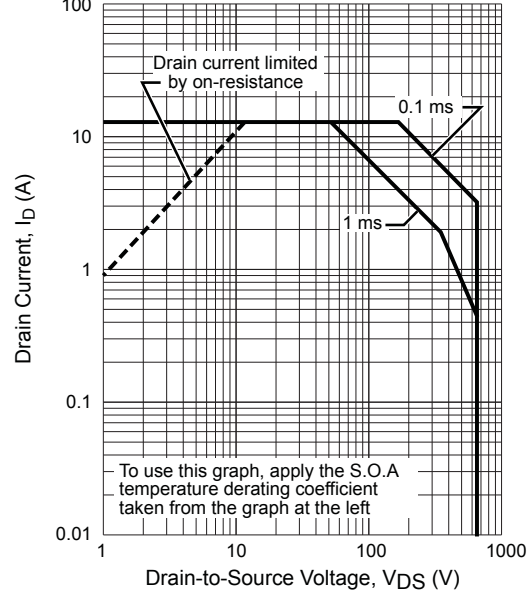


Characteristic Performance LC5547LF

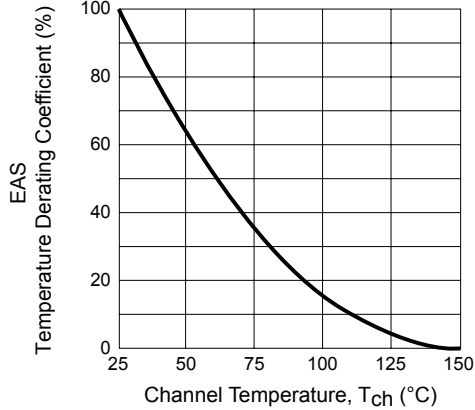
S. O. A. Temperature Derating Coefficient Curve



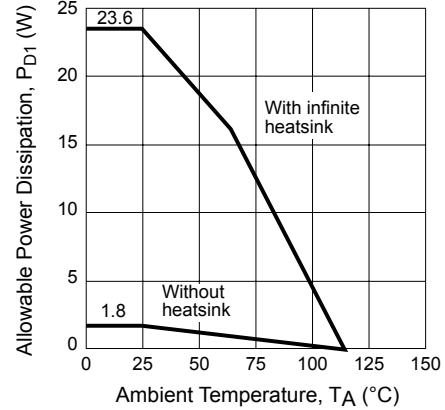
MOSFET Safe Operating Area Curve



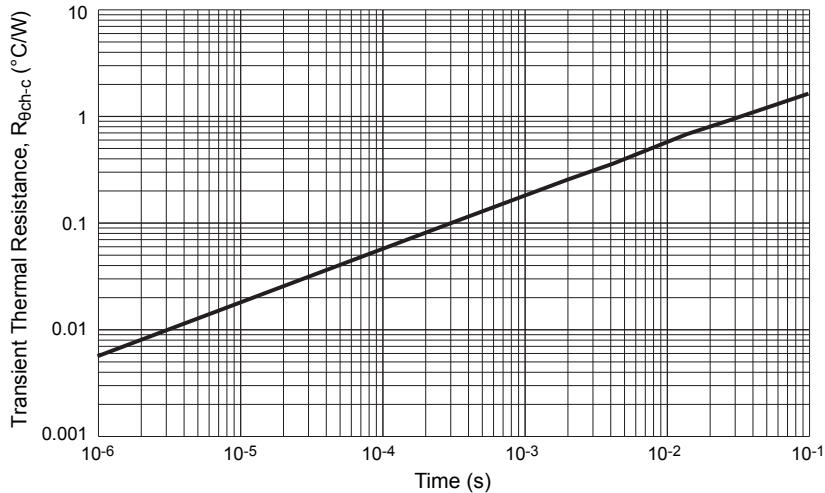
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve

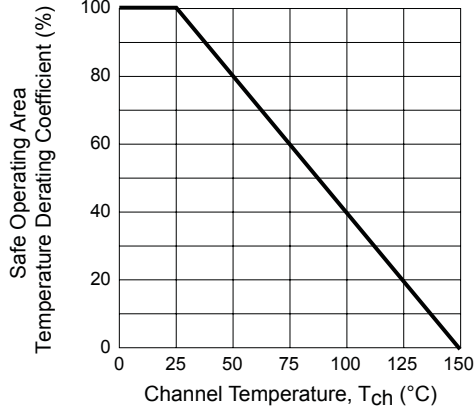


Transient Thermal Resistance Curve

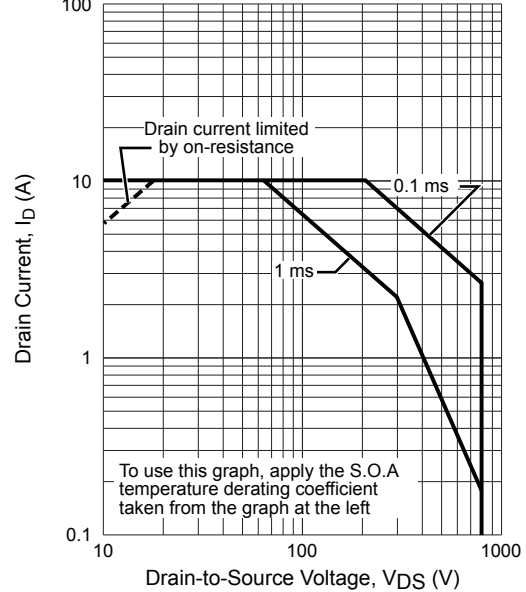


Characteristic Performance LC5549LF

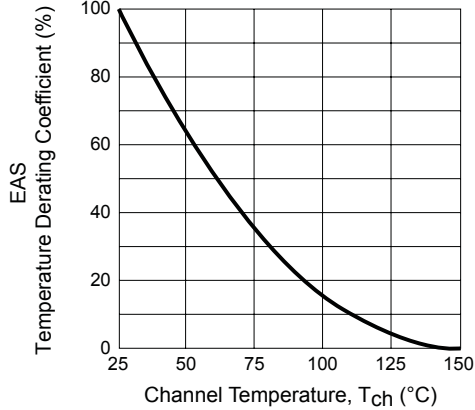
S. O. A. Temperature Derating Coefficient Curve



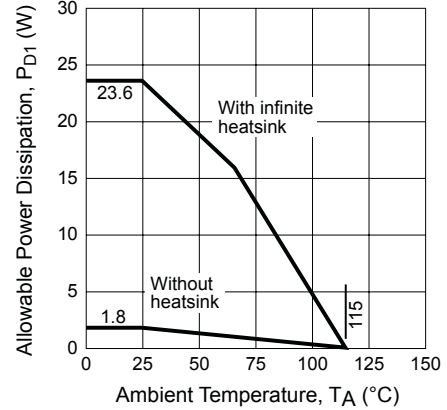
MOSFET Safe Operating Area Curve



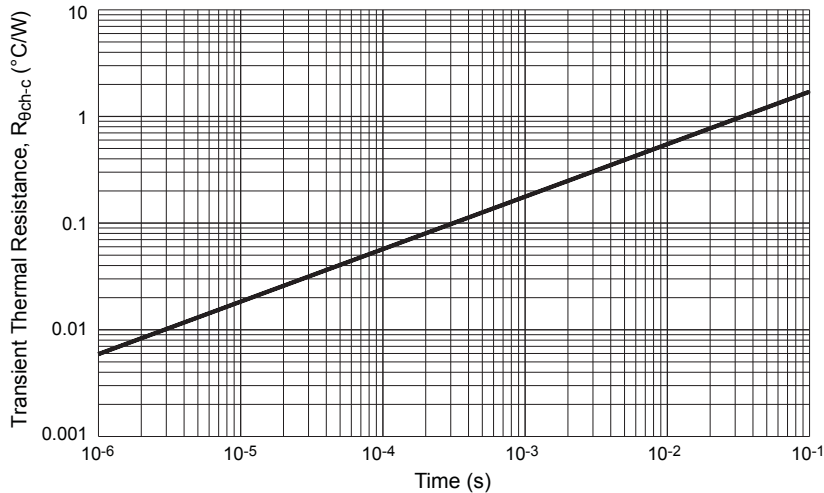
MOSFET Avalanche Energy Derating Coefficient Curve



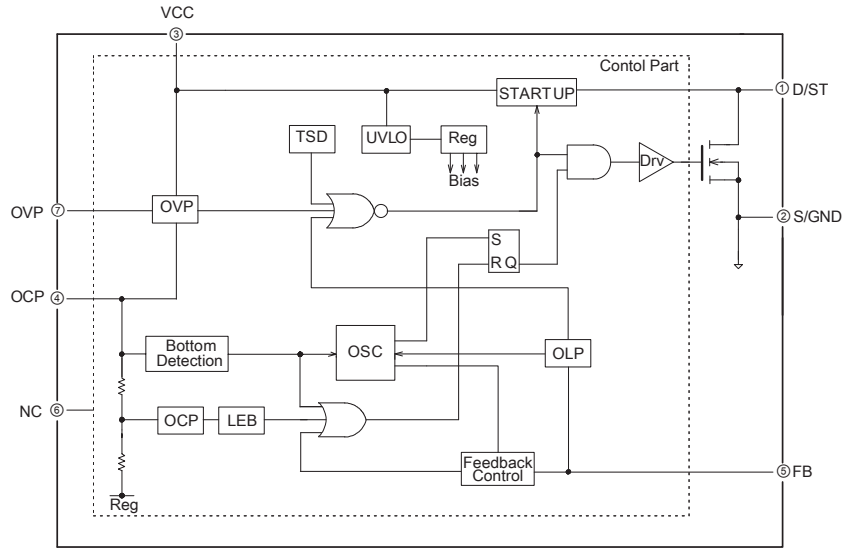
MOSFET Temperature versus Power Dissipation Curve



Transient Thermal Resistance Curve



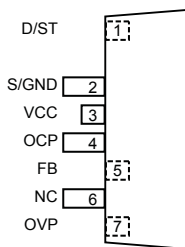
Functional Block Diagram



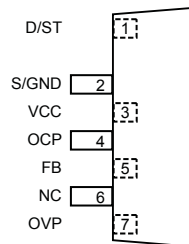
LC554xF Series Terminal List Table

Number	Name	Function
1	D/ST	MOSFET drain terminal and input of the startup current
2	S/GND	MOSFET source and GND terminal for the Control Part
3	VCC	Supply voltage input and Overvoltage protection (OVP) signal input
4	OCP	Overcurrent Protection, quasi-resonant signal input terminal, and Overvoltage Protection (OVP) signal input
5	FB	Feedback signal input and Overload Protection (OLP) signal input
6	NC	No connection
7	OVP	Overvoltage Protection (OVP) signal input

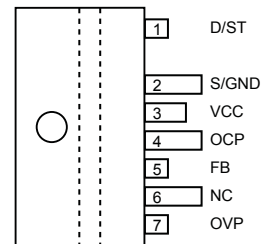
Pin-out Diagrams



(LF 3051)

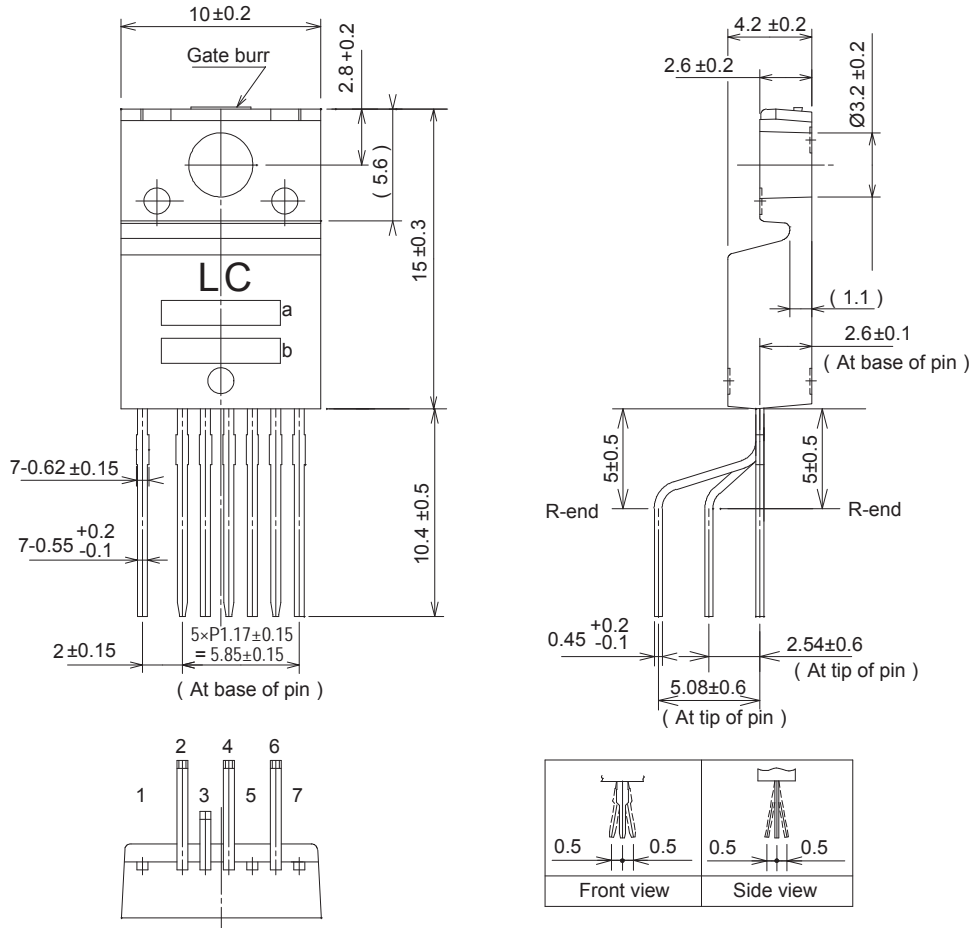


(LF 3052)



(LF 3054)

Package Outline Drawing Leadframe 3051

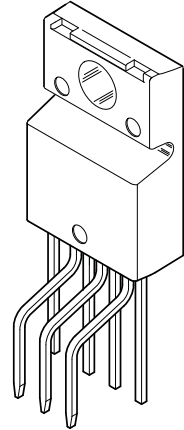
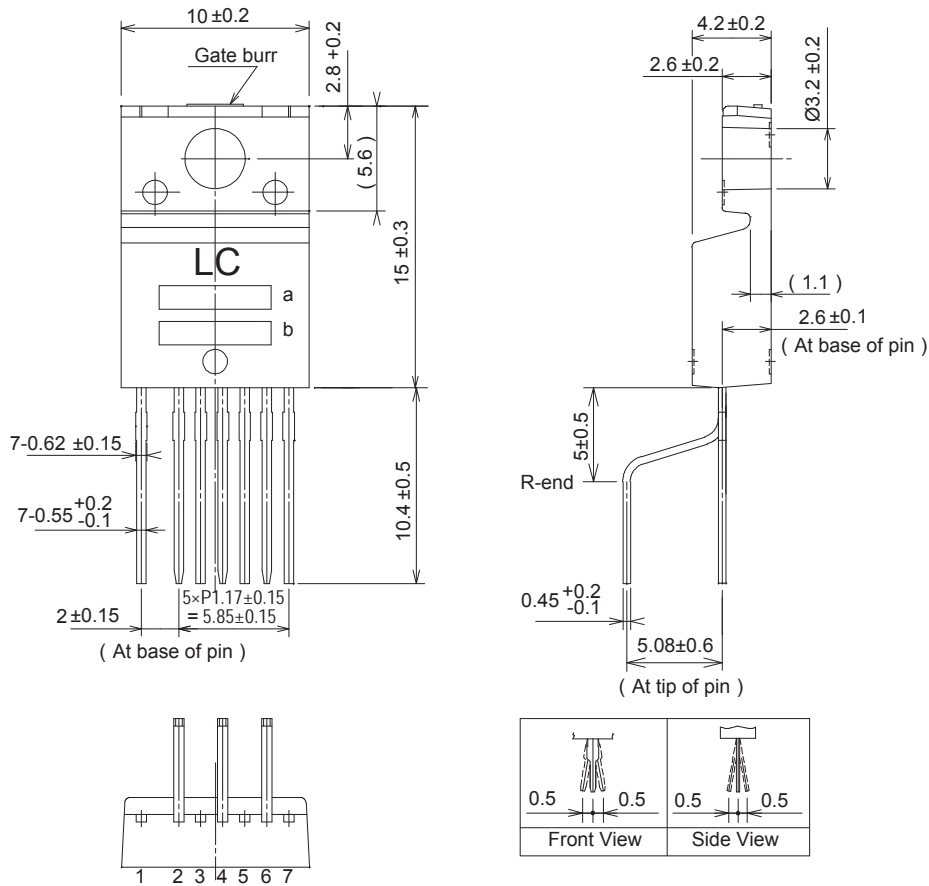


Unit: mm
 Package: TO-220F-7L
 Leadframe material: Cu
 Pin treatment: Solder dip
 Weight: Approximately 1.45 g
 Note: "Gate Burr" shows area where 0.3 mm (max) gate burr may be present.

a: Part # 556xLF
 b: Lot number
 1st letter: Last digit of year
 2nd letter: Month
 Jan to September: Numeric
 October: O
 November: N
 December: D
 3rd and 4th letter: Date
 01 to 31: Numeric
 5th letter: Internal use control number

Pin treatment Pb-free. Device composition compliant with the RoHS directive.

**Package Outline Drawing
Leadframe 3052**



Unit: mm
Package: TO-220F-7L

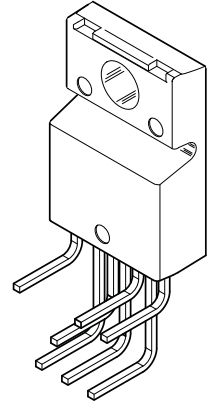
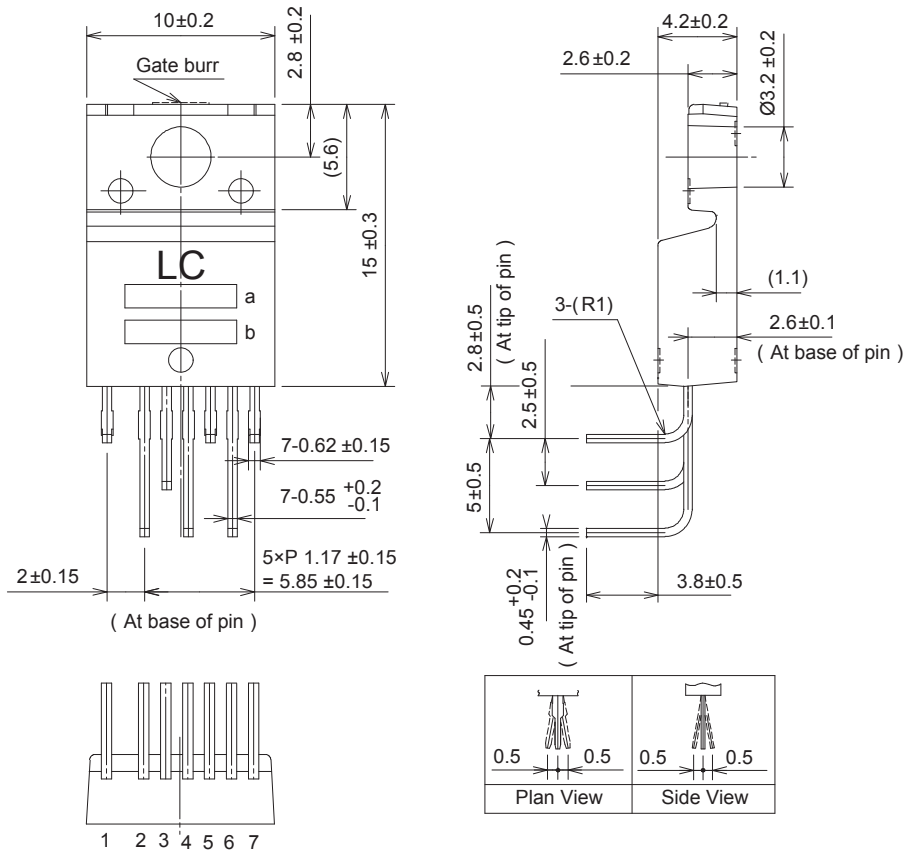
Leadframe material: Cu
Pin treatment: Solder dip
Weight: Approximately 1.45 g

"Gate Burr" shows area where 0.3 mm (max) gate burr may be present

a: Part # 556xLF
b: Lot number
1st letter: Last digit of year
2nd letter: Month
Jan to September: Numeric
October: O
November: N
December: D
3rd and 4th letter: Date
01 to 31: Numeric
5th letter: Internal use control number

Pin treatment Pb-free. Device composition compliant with the RoHS directive.

**Package Outline Drawing
Leadframe 3054**



Unit: mm
Package: TO-220F-7L

Leadframe material: Cu
Pin treatment: Solder dip
Weight: Approximately 1.45 g

"Gate Burr" shows area where 0.3 mm (max) gate burr may be present

a: Part # 556xLF
b: Lot number
1st letter: Last digit of year
2nd letter: Month
Jan to September: Numeric
October: O
November: N
December: D
3rd and 4th letter: Date
01 to 31: Numeric
5th letter: Internal use control number

Pin treatment Pb-free. Device composition compliant with the RoHS directive.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting this product on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone grease with low consistency (hard grease) may cause cracks in the mold resin when screwing the product to a heatsink.
- Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Type	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	MOMENTIVE Performance Materials, Inc
SC102	Dow Corning Toray Co., Ltd.

Cautions for Mounting to a Heatsink

- When the flatness around the screw hole is insufficient, such as when mounting the product to a heatsink that has an extruded (burred) screw hole, the product can be damaged, even with a lower than recommended screw torque. For mounting products, the mounting surface flatness should be 0.05 mm or less.

- Please select suitable screws for the product shape. Do not use a flat-head machine screw because of the stress to the product. Self-tapping screws are not recommended. When using self-tapping screws, the screw may enter the hole diagonally, not vertically, depending on the conditions of hole before threading or the work situation. That may stress the products and may cause failures.
- Recommended screw torque: 0.588 to 0.785 N•m (6 to 8 kgf•cm).
- For tightening screws, if a tightening tool (such as a driver) hits the product, the package may crack, and internal stress fractures may occur, which shorten the lifetime of the electrical elements and can cause catastrophic failure. Tightening with an air driver makes a substantial impact. In addition, a screw torque higher than the set torque can be applied and the package may be damaged. Therefore, an electric driver is recommended.

When the package is tightened at two or more places, first pre-tighten with a lower torque at all places, then tighten with the specified torque. When using a power driver, torque control is mandatory.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:

260±5°C	10±1 s	(Flow, 2 times)
380±10°C	3.5±0.5 s	(Solder iron, 1 time)
- Soldering iron should be at a distance of at least 1.5 mm from the body of the products

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 MΩ of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

- The contents in this document are subject to changes, for improvement and other purposes, without notice. Make sure that this is the latest revision of the document before use.
- Application and operation examples described in this document are quoted for the sole purpose of reference for the use of the products herein and Sanken can assume no responsibility for any infringement of industrial property rights, intellectual property rights or any other rights of Sanken or any third party which may result from its use.
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- In the case that you use our semiconductor devices or design your products by using our semiconductor devices, the reliability largely depends on the degree of derating to be made to the rated values. Derating may be interpreted as a case that an operation range is set by derating the load from each rated value or surge voltage or noise is considered for derating in order to assure or improve the reliability. In general, derating factors include electric stresses such as electric voltage, electric current, electric power etc., environmental stresses such as ambient temperature, humidity etc. and thermal stress caused due to self-heating of semiconductor devices. For these stresses, instantaneous values, maximum values and minimum values must be taken into consideration.
In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature (T_j) affects the reliability significantly.
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