

100mA/50V Digital transistors(with built-in resistors)

DTC014EM / DTC014EEB / DTC014EUB

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors. (See equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making the device design easy.

●Structure

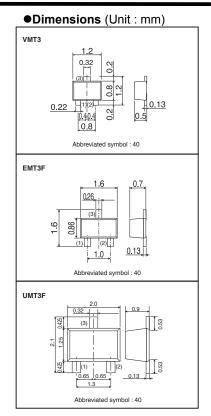
NPN epitaxial planar silicon transistor (Resistor built-in type)

Applications

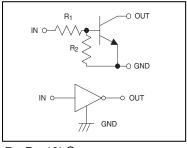
Inverter, Interface, Driver

Packaging specifications

	-			
	Package	VMT3	EMT3F	UMT3F
	Packaging Type	Taping	Taping	Taping
Туре	Code	T2L	TL	TL
	Basic ordering unit (pieces)	8000	3000	3000
DTC014EM		0	-	-
DTC014EEB		-	0	-
DTC014EUB		-	-	0



•Equivalent circuit



$R_1 = R_2 = 10 k \Omega$

●Absolute maximum (Ta=25°C)

Parameter	Symbol	Limits(DTC014E		Unit
Falameter		M EB	UB	Unit
Supply voltage	V _{CC}	50	V	
Input voltago	V _{IN}	40	V	
Input voltage	VIN	-10	V	
Collector current *1	I _{C(max)}	100	mA	
Output current	Ι _ο	50		mA
Power dissipation *2	PD	150	200	mW
Junction temperature	Tj	150		°C
Range of storage temperature	Tstg	-55 to +150		°C

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference land

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Input voltage	V _{I(off)}	-	-	0.8	V	V _{CC} =5V / I _O =100uA
Input voltage	V _{I(on)}	2.6	-	-	V	V _O =0.3V / I _O =5mA
Output voltage	V _{O(on)}	-	0.05	0.15	V	I _O =5mA / I _I =0.5mA
Input current	l _i	-	-	0.88	mA	V _I =5V
Output current	I _{O(off)}	-	-	500	nA	V _{CC} =50V / V _I =0V
DC current gain	GI	35	-	-	-	V _O =10V / I _O =5mA
Transition frequency *	f _T	-	250	-	MHz	V _{CE} =10V /I _E =-5mA f=100MHz
Input resistance	R ₁	7	10	13	kΩ	
Resistance ratio	R_2/R_1	0.8	1.0	1.2	-	

* Characteristics of built-in transistor

•Electrical characteristics curves

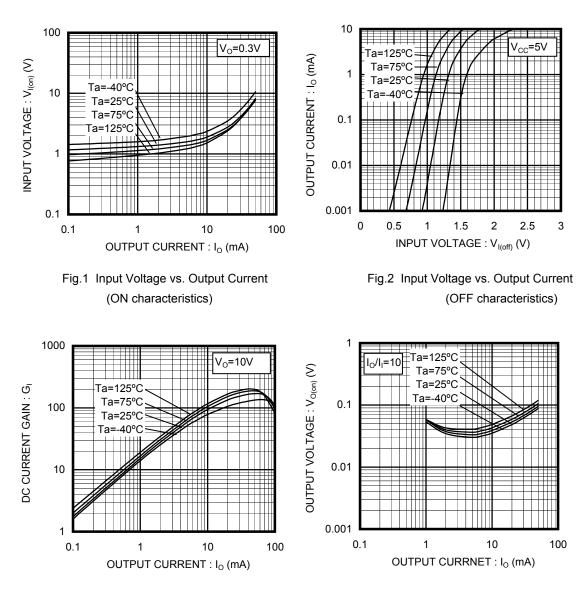




Fig.4 Output Voltage vs. Output Current

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