50Ω 3132 to 3352 MHz

# The Big Deal

- Fractional N synthesizer
- · Low phase noise and spurious
- Very small size 0.60" x 0.60" x 0.138"



CASE STYLE: KJ1367

## **Product Overview**

The SSN-3352A-119+ is a Frequency Synthesizer, designed to operate from 3132 to 3352 MHz for WiMAX application. The SSN-3352A-119+ is packaged in a metal case (size of  $0.60" \times 0.60" \times 0.138"$ ) to shield against unwanted signals and noise.

# **Key Features**

Feature	Advantages
Low phase noise and spurious:  • Phase Noise: -92 dBc/Hz typ. @ 10 kHz offset  • Step Size Spurious: -84 dBc typ.  • Comparison Spurious: -88 dBc typ.  • Reference Spurious: -88 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of SSN-3352A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.60" x 0.60" x 0.138"	The small size enables the SSN-3352A-119+ to be used in compact designs.







# Frequency Synthesizer

SSN-3352A-119+

 $50\Omega$  3132 to 3352 MHz

#### **Features**

- Fractional N synthesizer
- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+4.85V, VCC PLL=+3.2V)
- Small size 0.60" x 0.60" x 0.138"

### **Applications**

WiMAX



CASE STYLE: KJ1367 PRICE: \$29.95 ea. QTY (1-9)

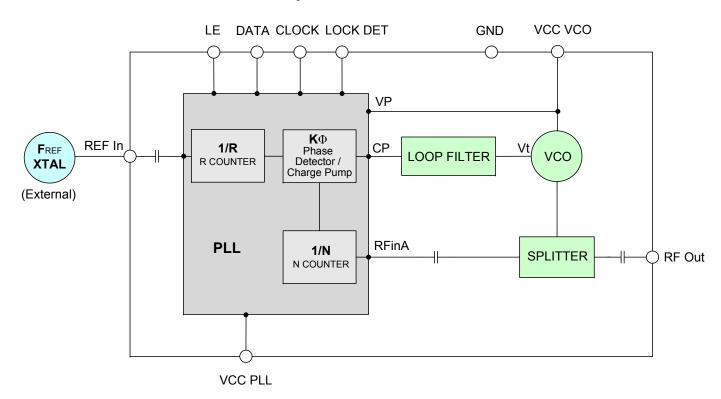
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

# **General Description**

The SSN-3352A-119+ is a Frequency Synthesizer, designed to operate from 3132 to 3352 MHz for WiMAX application. The SSN-3352A-119+ is packaged in a metal case (size of 0.60" x 0.60" x 0.138") to shield against unwanted signals and noise. To enhance the robustness of SSN-3352A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

#### **Simplified Schematic**





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REV. OR M127606 EDR-10128F1 SSN-3352A-119+ Category-A1 RAV 100630 Page 2 of 13

Notes: 1. Performance and quality attributes and conditions not expressly stated in this specification sheet are intended to be excluded and do not form a part of this specification sheet. 2. Electrical specifications and performance data contained herein are based on Mini-Circuits applicable established test performance criteria and measurement instructions. 3. The parts covered by this specification sheet are subject to Mini-Circui standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp.

#### Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units	
Frequency Range	-	3132	-	3352	MHz		
Step Size	-	-	125	-	kHz		
Comparison Frequency		-	-	26	-	MHz	
Settling Time		Within ± 1 kHz	-	5	-	mSec	
Output Power		-	0	+3	+6	dBm	
		@ 100 Hz offset	-	-75	-		
		@ 1 kHz offset	-	-90	-83	1	
SSB Phase Noise		@ 10 kHz offset	-	-92	-87	dBc/Hz	
		@ 100 kHz offset	-	-116	-109	]	
		@ 1 MHz offset	-	-137	-129		
Integrated SSB Phase Noise		@1kHz to 10MHz	-	-49	-45	dBc	
Step Size Spurious Suppressi	ion	Step Size 125 kHz	-	-84	-54		
0.5 Step Size Spurious Suppr	ession	0.5 Step Size 62.5 kHz	-	-70	-54		
Reference Spurious Suppress	sion	Ref. Freq. 52 MHz	-	-88	-80	dBc	
Comparison Spurious Suppre	ssion	Comp. Freq. 26 MHz	-	-88	-80	_ ubc	
Non - Harmonic Spurious Sup	pression	-	-	-90	-		
Harmonic Suppression		-	-	-30	-20		
VCO Supply Voltage		+4.85	+4.75	+4.85	+5.25	V	
PLL Supply Voltage		+3.20	+3.10	+3.20	+3.30	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
VCO Supply Current		-	-	44	55	mA	
PLL Supply Current		-	17		26	liiA	
	Frequency	52 (square wave)	-	52	-	MHz	
Reference Input	Amplitude	1	-	1	-	V <sub>p-P</sub>	
(External)	Input impedance	-	-	100	-	ΚΩ	
	Phase Noise @ 1 kHz offset	-	-	-135	-	dBc/Hz	
RF Output port Impedance		-	-	50	-	Ω	
Input Logic Level	Input high voltage	-	2.65	-	-	V	
Input Logic Level	Input low voltage	-	-	-	0.60	V	
Digital Lock Detect	Locked	-	2.70	-	3.30	V	
Digital Lock Detect	Unlocked	-	-	-	0.40	V	
Frequency Synthesizer PLL	-	ADF4153					
PLL Programming		-	3-wire seria	3-wire serial 3.2V CMOS			
	R0_Register	-	(MSB) 0010	000000000000000000000000000000000000000	01100000000	(LSB)	
Degister Man @ 2252 MU-	R1_Register	-	(MSB) 000101001000001101000001 (LSB)				
Register Map @ 3352 MHz	R2_Register	-	(MSB) 000000000000001111000011 (LSB)				
	R3_Register	-	(MSB) 0000	(MSB) 00000000000001111000111 (LSB)			

#### **Absolute Maximum Ratings**

Parameters	Ratings
VCO Supply Voltage	5.8V
PLL Supply Voltage	4.0V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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#### Typical Performance Data

FREQUENCY	PO	POWER OUTPUT			VCO CURRENT			PLL CURENT		
(MHz)		(dBm)			(mA)			(mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
3132	2.88	2.95	2.95	41.81	43.57	44.88	14.90	16.82	19.05	
3144	2.91	2.95	2.93	41.84	43.61	44.90	14.47	16.37	18.64	
3169	2.95	3.00	2.98	41.91	43.65	44.95	14.67	16.57	18.91	
3193	2.92	2.99	3.00	41.91	43.65	44.96	14.73	16.64	19.01	
3218	2.71	2.76	2.79	41.98	43.72	45.00	14.80	16.72	19.13	
3242	2.85	2.84	2.83	42.02	43.76	45.06	14.75	16.66	19.09	
3267	2.87	2.94	2.98	42.09	43.79	45.10	14.90	16.82	19.27	
3291	2.70	2.75	2.82	42.17	43.88	45.17	14.92	16.84	19.31	
3316	2.86	2.85	2.87	42.22	43.93	45.21	14.99	16.92	19.40	
3340	3.00	3.02	3.05	42.29	43.96	45.23	14.89	16.80	19.29	
3352	2.94	2.98	3.03	42.33	43.99	45.25	14.46	16.36	18.80	

FREQUENCY		HARMONICS (dBc)						
(MHz)	F2				F3			
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C		
3132	-34.40	-29.87	-28.05	-47.19	-47.36	-56.19		
3144	-32.01	-29.90	-27.58	-47.63	-47.51	-56.63		
3169	-30.66	-30.00	-28.92	-46.04	-47.47	-56.90		
3193	-30.67	-29.89	-29.63	-45.86	-46.28	-56.50		
3218	-28.47	-30.64	-30.18	-43.39	-44.80	-51.86		
3242	-30.14	-31.48	-30.24	-42.95	-44.38	-49.40		
3267	-30.27	-32.15	-32.53	-48.20	-44.74	-47.98		
3291	-31.82	-32.17	-30.75	-45.24	-44.46	-48.84		
3316	-33.63	-33.63	-33.99	-45.95	-44.36	-48.70		
3340	-33.36	-33.49	-33.51	-43.63	-44.50	-47.86		
3352	-31.82	-32.90	-32.98	-41.81	-44.48	-47.32		



EDECHENCY	PH	IASE NOIS	E (dBc/Hz	) @OFFSE	TS				
FREQUENCY (MHz)		+25°C							
, ,	100Hz	1kHz	10kHz	100kHz	1MHz				
3132	-74.73	-88.13	-92.40	-116.98	-137.51				
3144	-75.12	-90.71	-92.38	-116.97	-137.41				
3169	-76.00	-88.04	-92.46	-116.80	-137.35				
3193	-75.70	-89.95	-91.63	-116.68	-137.09				
3218	-79.23	-88.69	-91.35	-116.56	-137.13				
3242	-74.23	-90.15	-91.96	-116.28	-136.89				
3267	-77.39	-89.16	-91.56	-115.88	-136.68				
3291	-74.40	-91.72	-91.71	-115.80	-136.43				
3316	-73.57	-88.13	-91.49	-115.81	-136.29				
3340	-74.90	-89.54	-91.29	-116.22	-136.87				
3352	-75.00	-89.75	-91.43	-116.56	-137.23				

EDECHENCY	PH	IASE NOIS	E (dBc/Hz	) @OFFSE	TS					
FREQUENCY (MHz)		-45°C								
,	100Hz	1kHz	10kHz	100kHz	1MHz					
3132	-80.39	-89.98	-94.58	-117.94	-138.87					
3144	-76.54	-92.17	-93.87	-117.96	-138.74					
3169	-77.83	-93.28	-92.89	-117.84	-138.60					
3193	-80.72	-90.75	-92.92	-117.52	-138.35					
3218	-80.81	-92.71	-93.10	-117.35	-138.18					
3242	-79.34	-92.46	-92.82	-117.10	-138.03					
3267	-78.51	-92.42	-92.72	-116.80	-137.64					
3291	-79.44	-90.33	-92.99	-116.62	-137.52					
3316	-77.29	-91.06	-92.79	-116.13	-137.05					
3340	-77.30	-89.08	-91.87	-116.79	-137.99					
3352	-77.79	-90.23	-93.11	-117.20	-137.96					

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)		+85°C							
, ,	100Hz	1kHz	10kHz	100kHz	1MHz				
3132	-75.10	-89.87	-91.29	-115.23	-135.54				
3144	-78.20	-90.91	-90.82	-115.13	-135.69				
3169	-75.11	-89.39	-91.13	-115.23	-135.65				
3193	-76.35	-86.55	-91.36	-115.16	-135.64				
3218	-79.76	-90.17	-91.20	-115.13	-135.66				
3242	-78.50	-90.26	-90.68	-114.92	-135.44				
3267	-76.10	-90.26	-91.14	-114.66	-135.36				
3291	-76.95	-91.62	-90.43	-114.70	-135.11				
3316	-76.67	-88.56	-91.20	-114.94	-135.26				
3340	-76.59	-86.97	-90.60	-115.39	-135.87				
3352	-78.70	-86.22	-90.96	-115.49	-135.97				







COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS  @ Fcarrier 3132MHz+(n*Fcomparison) (dBc) note 1		COMPARISON SPURIOUS  @ Fcarrier  3242MHz+(n*Fcomparison)  (dBc) note 1			COMPARISON SPURIOUS  @ Fcarrier  3352MHz+(n*Fcomparison)  (dBc) note 1			
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-98.77	-98.08	-99.60	-98.78	-100.34	-99.45	-102.00	-101.72	-102.85
-4	-92.09	-94.93	-94.82	-96.97	-99.18	-97.49	-101.23	-101.34	-98.46
-3	-99.42	-97.58	-99.91	-99.60	-98.31	-99.69	-99.75	-98.95	-101.20
-2	-87.40	-87.16	-87.54	-90.00	-89.65	-89.04	-90.29	-90.45	-89.56
-1	-95.96	-101.48	-110.44	-102.57	-105.89	-108.85	-96.61	-102.25	-109.06
o <sup>note 2</sup>	-	-	-	-	-	-	-	-	-
+1	-95.67	-95.93	-95.39	-95.45	-95.17	-93.99	-92.49	-93.44	-95.54
+2	-98.31	-94.69	-95.83	-102.40	-100.84	-99.56	-100.34	-100.54	-98.23
+3	-95.44	-100.77	-97.59	-98.93	-99.78	-99.09	-99.37	-100.02	-103.07
+4	-94.34	-95.30	-95.50	-100.00	-98.56	-99.00	-103.42	-100.54	-99.29
+5	-109.16	-108.84	-115.21	-113.38	-114.06	-117.35	-110.90	-118.47	-112.14

Note 1: Comparison frequency 26 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS  @ Fcarrier  3132MHz+(n*Freference)  (dBc) note 3		@Fcarrier @Fcarrier 3132MHz+(n*Freference) 3242MHz+(n*Freference)				RENCE SPU @ Fcarrier Hz+(n*Frefe (dBc) no	erence)	
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-89.81	-94.14	-100.25	-97.45	-100.10	-112.72	-97.30	-100.01	-104.94
-4	-92.32	-93.81	-93.82	-105.08	-104.46	-101.41	-108.13	-110.02	-102.20
-3	-98.54	-99.03	-96.59	-112.88	-104.08	-99.73	-114.89	-105.11	-98.92
-2	-92.09	-94.93	-94.82	-96.97	-99.18	-97.49	-101.23	-101.34	-98.46
-1	-87.40	-87.16	-87.54	-90.00	-89.65	-89.04	-90.29	-90.45	-89.56
o <sup>note 4</sup>	-	-	-	-	-	-	-	-	-
+1	-98.31	-94.69	-95.83	-102.40	-100.84	-99.56	-100.34	-100.54	-98.23
+2	-94.34	-95.30	-95.50	-100.00	-98.56	-99.00	-103.42	-100.54	-99.29
+3	-107.64	-114.42	-109.44	-108.96	-109.57	-106.28	-110.58	-110.57	-105.60
+4	-96.40	-96.98	-95.29	-111.64	-103.51	-98.63	-111.85	-104.83	-96.69
+5	-90.48	-92.12	-95.48	-97.49	-96.21	-99.02	-97.96	-97.68	-99.85

Note 3: Reference frequency 52 MHz

Note 4: All spurs are referenced to carrier signal (n=0).



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STEP SIZE SPURIOUS ORDER	0.5 STEP SIZE & STEP SIZE SPURIOUS @ Fcarrier 3132MHz+(n*Fstep size) (dBc) note 5		S @ Fcarrier SPURIOUS @ Fcarrier n*Fstep size) 3242MHz+(n*Fstep size)			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 3352MHz+(n*Fstep size) (dBc) note 5			
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5.0	-111.93	-113.04	-110.36	-105.92	-110.68	-109.48	-108.42	-112.27	-110.64
-4.5	-107.27	-107.49	-106.03	-111.50	-107.11	-107.54	-109.96	-106.13	-111.21
-4.0	-108.37	-111.14	-108.38	-107.28	-110.31	-106.80	-109.76	-111.47	-108.32
-3.5	-108.49	-106.43	-109.05	-104.00	-106.71	-104.02	-109.20	-106.26	-107.45
-3.0	-106.56	-106.92	-104.90	-104.50	-104.71	-105.17	-106.35	-100.04	-104.20
-2.5	-101.13	-101.25	-102.25	-100.34	-99.57	-101.68	-101.57	-104.20	-102.96
-2.0	-99.41	-98.89	-100.60	-97.05	-99.33	-98.27	-96.27	-97.27	-95.11
-1.5	-95.66	-96.21	-90.83	-90.08	-94.46	-90.65	-93.95	-94.07	-89.97
-1.0	-83.93	-85.44	-85.72	-86.19	-83.61	-87.75	-80.11	-77.06	-87.40
-0.5	-73.51	-71.36	-68.07	-75.00	-72.24	-70.79	-80.12	-72.14	-82.79
o <sup>note 6</sup>	-	-	-	-	-	-	-	-	-
+0.5	-75.10	-70.48	-69.13	-74.45	-73.58	-72.57	-77.88	-69.59	-81.44
+1.0	-83.60	-88.33	-86.23	-83.65	-82.60	-84.95	-82.63	-78.41	-85.02
+1.5	-96.00	-96.43	-94.64	-90.18	-93.16	-90.09	-93.67	-93.34	-91.20
+2.0	-97.97	-99.68	-100.76	-96.39	-96.85	-100.37	-94.79	-97.71	-97.24
+2.5	-104.71	-103.85	-100.68	-104.36	-99.60	-102.44	-99.67	-105.20	-100.91
+3.0	-106.51	-104.37	-104.43	-107.67	-102.28	-103.35	-106.99	-100.91	-105.50
+3.5	-105.92	-105.19	-108.43	-107.20	-106.57	-105.13	-107.61	-108.44	-104.03
+4.0	-108.39	-110.56	-110.11	-105.93	-110.84	-105.06	-108.64	-104.34	-108.96
+4.5	-110.20	-109.42	-107.86	-109.07	-105.69	-107.78	-110.19	-106.48	-110.15
+5.0	-113.85	-110.57	-110.70	-108.21	-113.39	-110.89	-110.17	-109.51	-112.90

Note 5: Step size 125 kHz

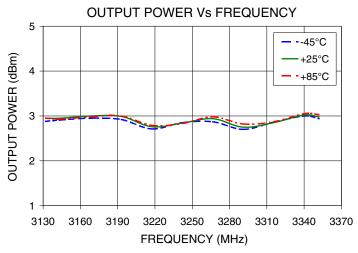
Note 6: All spurs are referenced to carrier signal (n=0).

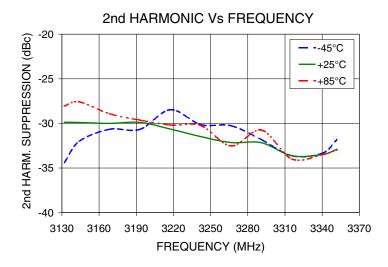


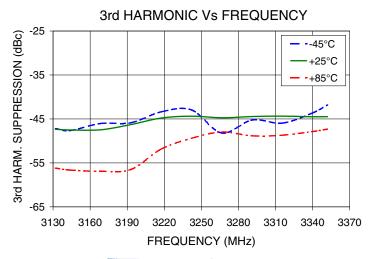




## **Typical Performance Curves**





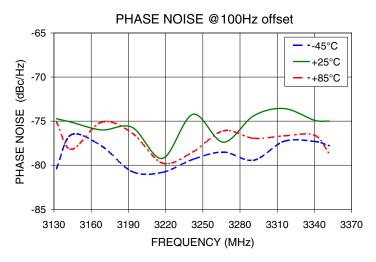


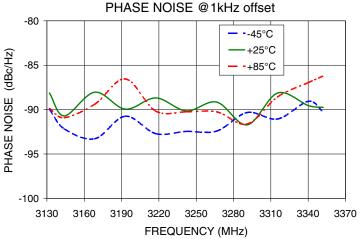
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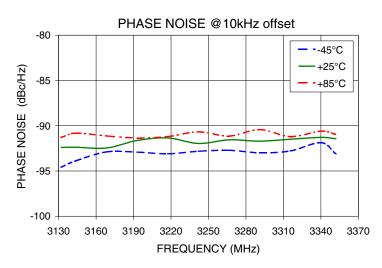
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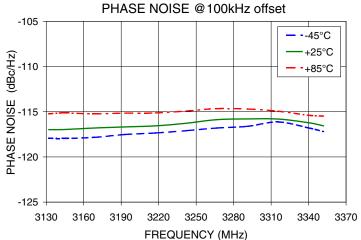
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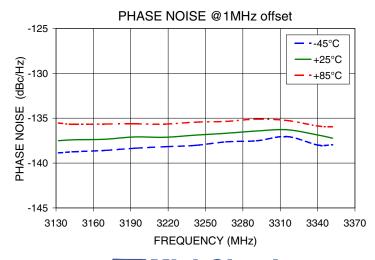
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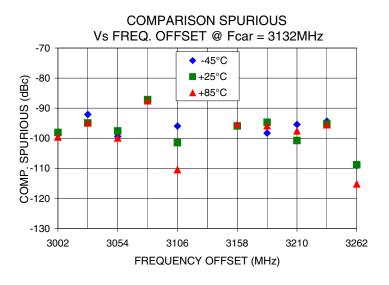
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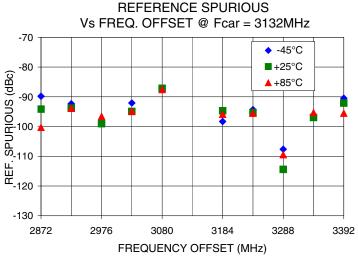
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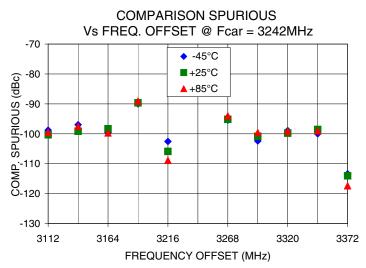
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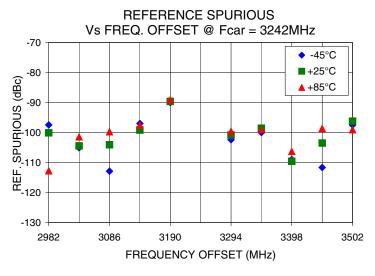
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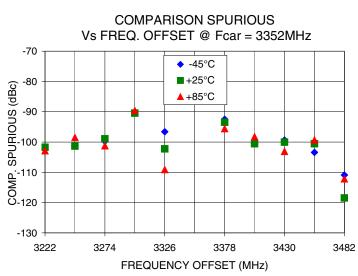


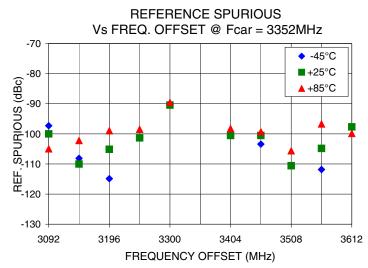










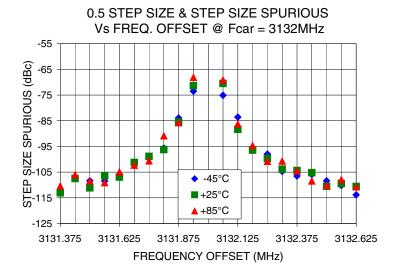


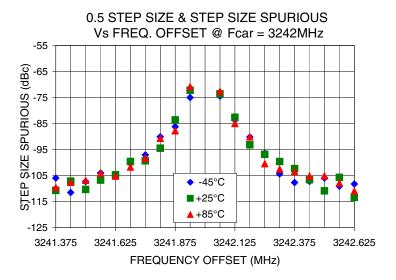
Mini-Circuits

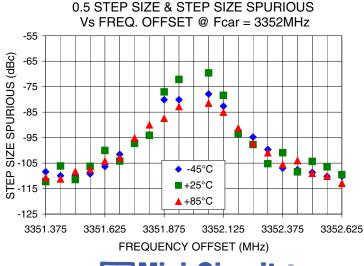
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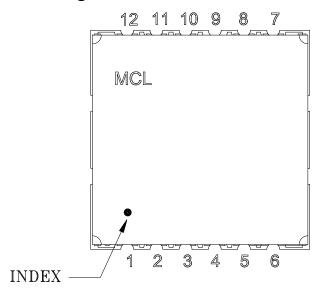
IF/RF MICROWAVE COMPONENTS • ISO 9001 ISO 14001 AS 9100 CERTIFIED ₺ RoHS compliant P.O. Box 350166, Brooklyn, New York 11235-0003 (718) 934-4500 Fax (718) 332-4661

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#### **Pin Configuration**

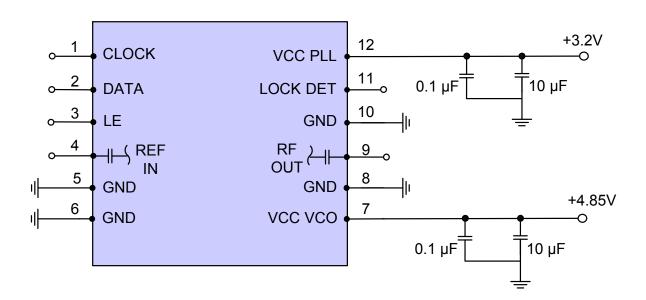


#### **Pin Connection**

Pin Number	Function
1	CLOCK
2	DATA
3	ENABLED
4	REF IN
5	GND
6	GND
7	VCC VCO
8	GND
9	RF OUT
10	GND
11	LOCK DET
12	VCC PLL

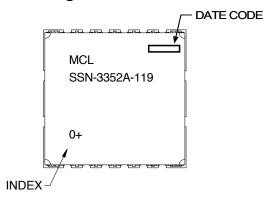
#### **Recommended Application Circuit**

Note: REF IN and RF OUT ports are internally AC coupled.





#### **Device Marking**



#### **Additional Detailed Technical Information**

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: KJ1367

Tape & Reel: TR-F95

Suggested Layout for PCB Design: PL-317

**Evaluation Board: TB-552+** 

**Environment Ratings: ENV03T2** 

