3030 to 3264 MHz  $50\Omega$ 

# **The Big Deal**

- · Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

## **Product Overview**

The KSN-3260A-1M19+ is a Frequency Synthesizer, designed to operate from 3030 to 3264 MHz for public safety communication application. The KSN-3260A-1M19+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

# **Key Features**

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -87 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -100 dBc typ. • Reference Spurious: -100 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-3260A-1M19+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-3260A-1M19+ to be used in compact designs.









# Frequency Synthesizer

KSN-3260A-1M19+

 $50\Omega$  3030 to 3264 MHz

#### **Features**

- Integrated VCO + PLL
- · Low phase noise and spurious
- Robust Design and Construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3V)
- Small size 0.80" x 0.58" x 0.15"

## **Applications**

· Public safety communication



CASE STYLE: DK1042 PRICE: \$29.95 ea. QTY (1-9)

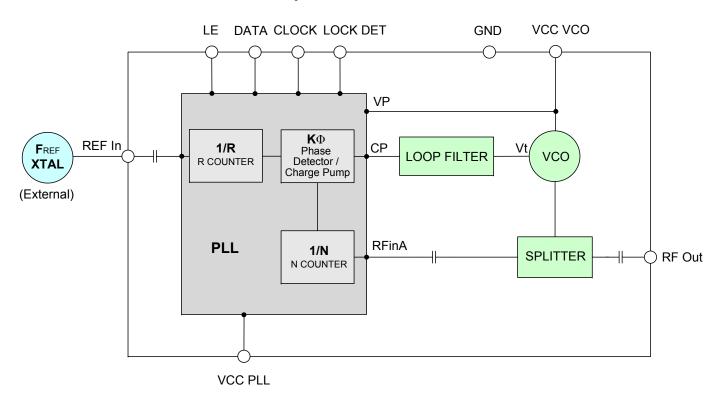
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

#### **General Description**

The KSN-3260A-1M19+ is a Frequency Synthesizer, designed to operate from 3030 to 3264MHz for public safety communication application. The KSN-3260A-1M19+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-3260A-1M19+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

#### **Simplified Schematic**





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#### Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters	Test Conditions	Min.	Тур.	Max.	Units			
Frequency Range	-	3030	-	3264	MHz			
Step size		-	-	1000	-	kHz		
Settling Time		Within ± 1 kHz	-	2	-	mSec		
Output Power		-	-1.0	+2.3	+4.0	dBm		
		@ 100 Hz offset	-	-79	-			
		@ 1 kHz offset	-	-85	-77	1		
SSB Phase Noise		@ 10 kHz offset	-	-87	-78	dBc/Hz		
		@ 100 kHz offset	-	-115	-111			
		@ 1 MHz offset	-	-138	-132			
Integrated SSB Phase Noise		@ 100 Hz to 1 MHz	-	-41	-			
Reference Spurious Suppress	sion	Ref. Freq. 40 MHz	-	-100	-80			
Comparison Spurious Suppre	ssion	Step Size 1000 kHz	-	-100	-80	dBc		
Non - Harmonic Spurious Sup	pression	-	-	-90	-			
Harmonic Suppression		-	-	-30	-20			
VCO Supply Voltage		+5.00	+4.75	+5.00	+5.25	V		
PLL Supply Voltage		+3.00	+2.85	+3.00	+3.15	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
VCO Supply Current		-	-	50	60	mA		
PLL Supply Current		-	-	15	20	IIIA		
	Frequency	40 (square wave)	-	40	-	MHz		
Reference Input	Amplitude	1	-	1	-	V <sub>P-P</sub>		
(External)	Input impedance	-	-	100	-	ΚΩ		
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz		
RF Output port Impedance		-	-	50	-	Ω		
Input Logic Level	Input high voltage	-	2.55	-	-	V		
Imput Logic Level	Input low voltage	-	-	-	0.55	V		
Digital Look Datast	Locked	-	2.45	-	3.15	V		
Digital Lock Detect	Unlocked	-	-	-	0.40	V		
Frequency Synthesizer PLL	-	ADF4106						
PLL Programming		-	3-wire serial 3V CMOS					
	F_Register	-	(MSB) 010	(MSB) 010101101000000010010011 (LSB)				
Register Map @ 3264 MHz	N_Register	-	(MSB) 000000001100110000000001 (LSB)					
	R_Register	-	(MSB) 000	(MSB) 00010000000000010100000 (LSB)				

#### **Absolute Maximum Ratings**

Parameters	Ratings
VCO Supply Voltage	5.3V
PLL Supply Voltage	3.6V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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### Typical Performance Data

EDEQUENCY	PO	POWER OUTPUT			VCO CURRENT			LL CUREN	Т
FREQUENCY (MHz)		(dBm)			(mA)		(mA)		
(	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
3030	2.64	2.38	2.01	47.58	50.29	53.10	12.77	14.54	17.47
3037	2.66	2.44	2.08	47.16	50.33	53.13	12.80	14.58	17.51
3064	2.55	2.52	2.20	47.20	50.41	53.20	12.83	14.61	17.53
3091	1.95	2.02	1.68	47.31	50.49	53.25	12.83	14.62	17.54
3118	2.10	1.95	1.58	47.62	50.72	53.43	12.82	14.64	17.57
3145	2.65	2.48	2.13	47.81	50.92	53.59	12.83	14.64	17.57
3172	2.36	2.45	2.15	47.83	50.97	53.63	12.88	14.68	17.59
3199	1.87	1.91	1.56	48.03	51.09	53.71	12.90	14.71	17.64
3226	2.29	2.09	1.67	48.30	51.29	53.87	12.85	14.68	17.62
3253	2.64	2.43	2.04	48.31	51.35	53.93	12.88	14.69	17.63
3264	2.55	2.48	2.05	48.27	51.36	53.93	12.89	14.69	17.62

		HARMONICS (dBc)							
FREQUENCY (MHz)		F2		F3					
(101112)	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C			
3030	-29.81	-31.30	-33.31	-33.13	-36.62	-39.73			
3037	-28.92	-30.63	-32.97	-30.77	-35.72	-38.88			
3064	-28.00	-28.54	-30.86	-31.50	-39.25	-40.70			
3091	-27.85	-30.06	-32.50	-35.22	-42.48	-40.55			
3118	-27.12	-27.85	-30.05	-42.11	-39.93	-37.60			
3145	-27.36	-29.64	-32.12	-46.31	-39.31	-37.77			
3172	-26.86	-28.14	-30.75	-44.53	-40.81	-39.70			
3199	-26.45	-29.27	-31.99	-43.46	-41.17	-39.61			
3226	-27.95	-29.19	-31.92	-40.64	-38.29	-36.41			
3253	-28.89	-31.71	-34.88	-36.74	-34.99	-35.72			
3264	-27.70	-30.26	-34.11	-38.53	-37.34	-37.23			



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EDECUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
FREQUENCY (MHz)	+25°C								
(111112)	100Hz	1kHz	10kHz	100kHz	1MHz				
3030	-79.64	-87.12	-86.32	-117.10	-137.64				
3037	-80.58	-85.26	-86.20	-117.04	-137.84				
3064	-83.09	-86.87	-86.55	-117.10	-138.19				
3091	-78.12	-89.28	-87.47	-117.29	-138.38				
3118	-80.55	-85.17	-86.81	-117.31	-138.13				
3145	-81.22	-85.02	-86.93	-116.76	-137.93				
3172	-81.05	-87.12	-86.02	-116.15	-137.27				
3199	-80.37	-85.06	-86.01	-115.89	-136.78				
3226	-79.58	-86.49	-87.29	-115.70	-136.63				
3253	-79.19	-85.43	-86.84	-115.27	-136.31				
3264	-81.18	-84.77	-87.48	-115.28	-136.44				

EDE QUENCY	PHASE NOISE (dBc/Hz) @OFFSETS								
FREQUENCY (MHz)	-45°C								
(111112)	100Hz	1kHz	10kHz	100kHz	1MHz				
3030	-82.13	-88.68	-84.83	-117.52	-138.42				
3037	-82.09	-87.45	-84.76	-117.56	-138.79				
3064	-82.71	-86.42	-84.96	-117.52	-138.35				
3091	-79.16	-88.14	-85.32	-117.81	-139.65				
3118	-81.41	-88.78	-85.66	-118.42	-140.05				
3145	-80.28	-86.42	-85.60	-118.01	-139.76				
3172	-81.98	-87.37	-85.43	-117.38	-139.01				
3199	-79.22	-84.80	-85.63	-117.06	-138.59				
3226	-81.57	-84.74	-86.63	-116.81	-137.69				
3253	-81.72	-83.36	-86.50	-116.25	-137.43				
3264	-78.02	-86.35	-86.76	-116.08	-137.01				

	PHASE NOISE (dBc/Hz) @OFFSETS								
FREQUENCY (MHz)	+85°C								
(111112)	100Hz	1kHz	10kHz	100kHz	1MHz				
3030	-80.20	-84.05	-84.34	-115.19	-136.17				
3037	-80.33	-83.78	-84.49	-115.27	-136.35				
3064	-87.18	-84.94	-84.73	-115.36	-136.61				
3091	-83.56	-85.95	-85.00	-115.44	-136.48				
3118	-83.13	-83.75	-85.23	-115.36	-136.55				
3145	-82.22	-85.04	-85.04	-114.98	-136.18				
3172	-81.29	-83.26	-84.93	-114.64	-135.60				
3199	-79.34	-82.12	-85.51	-114.75	-135.54				
3226	-80.82	-82.80	-86.31	-114.83	-135.75				
3253	-82.25	-80.26	-84.62	-114.83	-135.83				
3264	-81.16	-81.33	-85.03	-114.84	-135.78				



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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @ Fcarrier 3030MHz+(n*Fcomparison) (dBc) note 1				COMPARISON SPURIOUS  @ Fcarrier  3147MHz+(n*Fcomparison)  (dBc) note 1			COMPARISON SPURIOUS  @ Fcarrier  3264MHz+(n*Fcomparison)  (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
-5	-113.96	-111.72	-112.65	-113.28	-111.41	-112.46	-112.72	-111.32	-114.29	
-4	-114.20	-111.08	-112.59	-111.36	-120.77	-123.19	-119.70	-123.53	-123.64	
-3	-110.50	-111.80	-110.39	-108.19	-116.40	-120.70	-113.14	-118.90	-121.86	
-2	-106.78	-107.16	-107.16	-107.07	-110.31	-117.59	-112.45	-117.02	-118.16	
-1	-99.38	-101.04	-100.72	-99.39	-105.56	-106.96	-107.86	-110.33	-113.08	
0 <sup>note 2</sup>	-	-	-	-	-	-	-	-	-	
+1	-98.99	-100.53	-100.96	-100.54	-106.10	-110.37	-112.09	-111.39	-108.83	
+2	-106.74	-108.04	-108.25	-106.33	-112.29	-112.69	-113.46	-116.62	-117.53	
+3	-109.88	-110.02	-110.35	-113.05	-116.77	-118.32	-119.58	-117.81	-124.95	
+4	-113.86	-111.89	-112.73	-113.81	-120.65	-120.90	-123.96	-126.33	-124.54	
+5	-114.76	-113.73	-117.17	-115.93	-116.35	-124.70	-122.83	-119.08	-126.24	

Note 1: Comparison frequency 1000 kHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS  @ Fcarrier  3030MHz+(n*Freference)  (dBc) note 3			REFERENCE SPURIOUS  @Fcarrier  3147MHz+(n*Freference)  (dBc) note 3			REFERENCE SPURIOUS  @ Fcarrier  3264MHz+(n*Freference)  (dBc) note 3		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-93.95	-96.66	-101.20	-94.17	-96.84	-100.81	-96.40	-99.46	-101.64
-4	-94.19	-96.39	-99.25	-94.53	-97.13	-99.57	-95.70	-98.06	-100.84
-3	-105.44	-110.23	-114.99	-103.55	-107.87	-111.92	-106.25	-111.11	-115.82
-2	-99.19	-104.13	-106.15	-99.34	-103.37	-108.22	-100.58	-105.25	-108.70
-1	-95.61	-105.70	-100.55	-112.40	-104.11	-108.85	-97.71	-108.00	-95.19
0 <sup>note 4</sup>	-	-	-	-	-	-	-	-	-
+1	-96.48	-106.88	-90.90	-111.35	-104.27	-109.90	-99.85	-103.57	-93.46
+2	-100.51	-104.44	-108.67	-99.22	-103.63	-106.25	-100.66	-103.89	-107.45
+3	-104.58	-108.68	-113.04	-104.29	-107.93	-111.33	-105.72	-109.69	-110.79
+4	-95.13	-96.99	-100.02	-95.35	-97.25	-100.03	-96.37	-97.96	-100.30
+5	-95.99	-98.22	-103.13	-95.38	-97.40	-100.57	-96.69	-99.03	-101.59

Note 3: Reference frequency 40 MHz

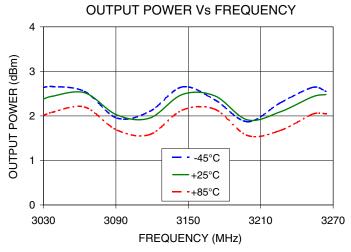
Note 4: All spurs are referenced to carrier signal (n=0).

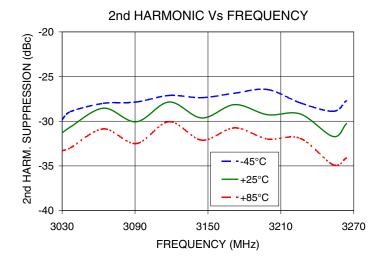


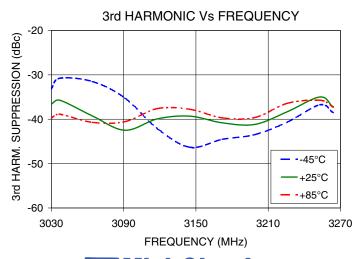
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#### **Typical Performance Curves**

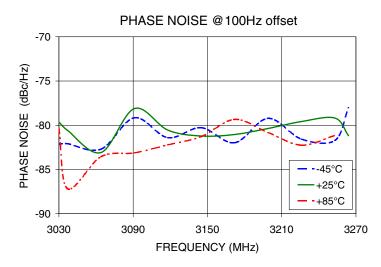


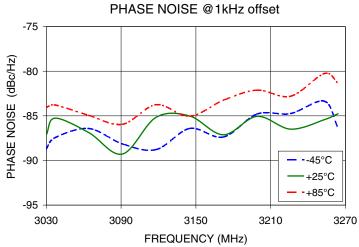


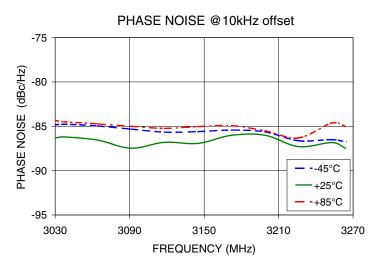


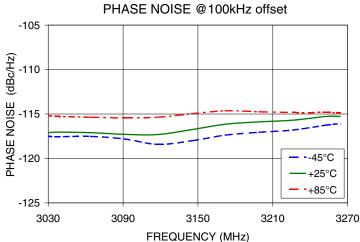
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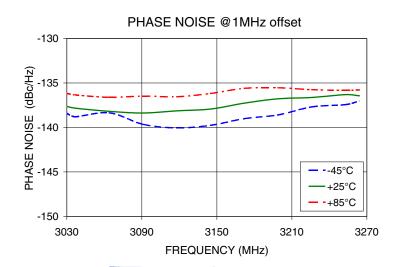
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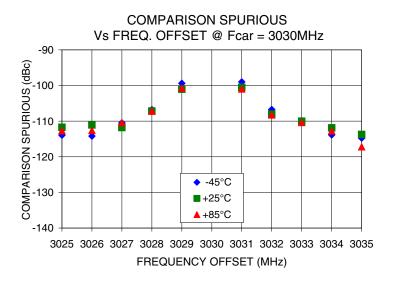
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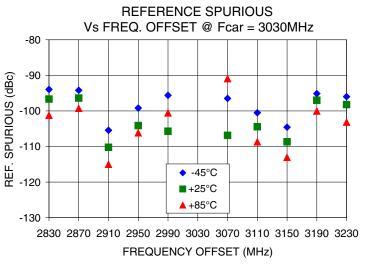
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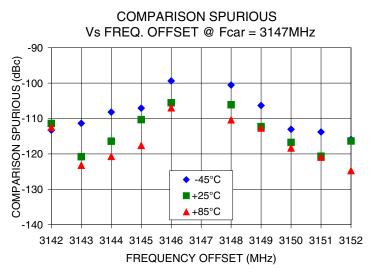
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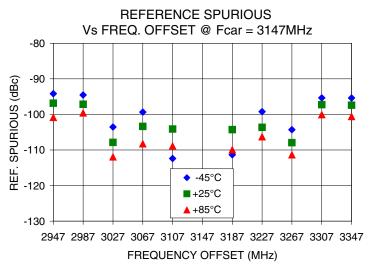
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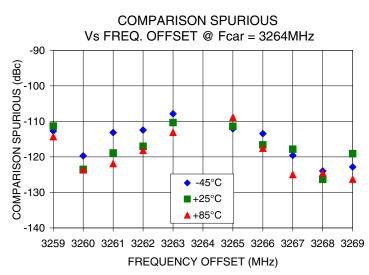


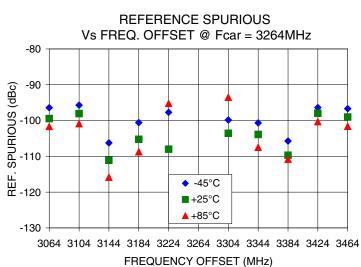












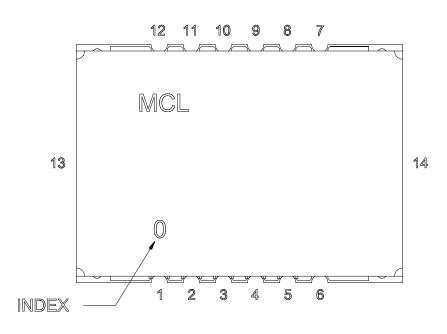
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### **Pin Configuration**

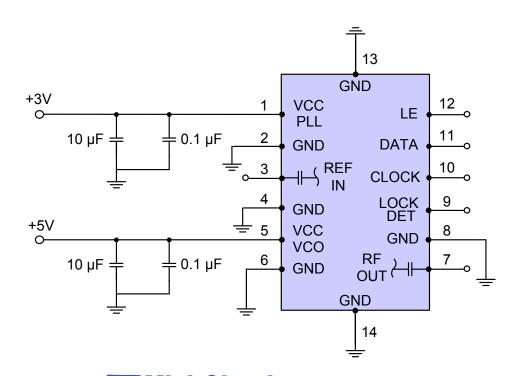


#### **Pin Connection**

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

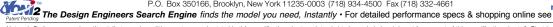
#### **Recommended Application Circuit**

Note: REF IN and RF OUT ports are internally AC coupled.



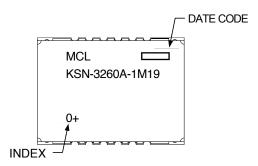


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#### **Device Marking**



#### **Additional Detailed Technical Information**

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

**Evaluation Board: TB-567-2+** 

**Environment Ratings:** ENV03T2

