New Product

VBT2045CBP

Vishay General Semiconductor

Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low $V_F = 0.33$ V at $I_F = 5.0$ A

TMBS[®] TO-263AB



S° AB

VBT2045CBP

$\begin{tabular}{|c|c|c|c|} \hline PRIMARY CHARACTERISTICS \\ \hline I_{F(AV)} & 2 x 10 A \\ \hline V_{RRM} & 45 V \\ \hline I_{FSM} & 160 A \\ \hline V_F at I_F = 10 A & 0.41 V \\ \hline T_{OP} max. & 150 \ ^{\circ}C \\ \hline \end{tabular}$

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF RoHS maximum peak of 245 °C COMPLIANT
- Not recommended for PCB bottom side wave mounting
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

MECHANICAL DATA

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		VBT2045CBP	UNIT		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V		
Maximum average forward rectified current (fig. 1)	per device	20	Α		
	ber diode	10	A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		160	А		
Operating junction and storage temperature range	T _{OP} , T _{STG}	- 40 to + 150	°C		
Junction temperature in DC forward current without reverse bias, t \leq 1 h		≤ 200	°C		

Notes

(1) With heatsink

⁽²⁾ Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	- V _F ⁽¹⁾	0.44	-	V
	I _F = 10 A			0.49	0.58	
	I _F = 5 A	- T _A = 125 °C		0.33	-	
	I _F = 10 A			0.41	0.52	
Reverse current per diode	V – 45 V	T _A = 25 °C	I _R ⁽²⁾	-	2000	μA
	V _R = 45 V	T _A = 125 °C		10	30	mA

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	VBT2045CBP	UNIT			
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	3.0	°C/W		
	per device		2.0	C/W		

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-263AB	VBT2045CBP-E3/4W	1.38	4W	50/tube	Tube
TO-263AB	VBT2045CBP-E3/8W	1.38	8W	800/reel	Tape and reel

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

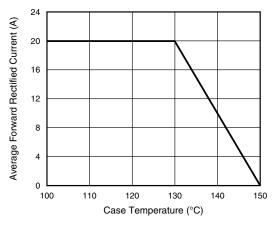


Fig. 1 - Maximum Forward Current Derating Curve

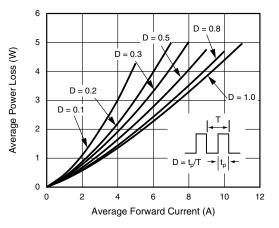


Fig. 2 - Forward Power Loss Characteristics Per Diode



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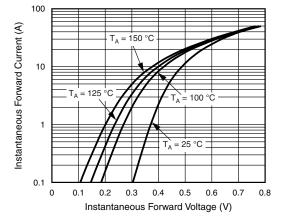


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

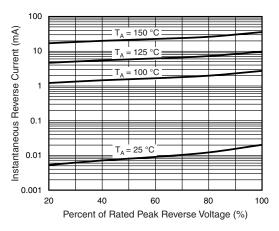


Fig. 4 - Typical Reverse Characteristics Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

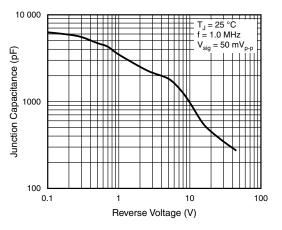


Fig. 5 - Typical Junction Capacitance Per Diode

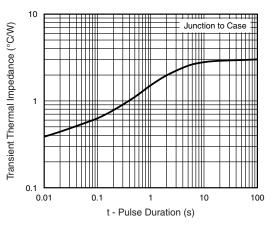


Fig. 6 - Typical Transient Thermal Impedance Per Diode

0.411 (10.45) 0.190 (4.83) **Mounting Pad Layout** 0.380 (9.65) 0.055 (1.40) 0.160 (4.06) 0.42 (10.66) MIN. 0.245 (6.22) 0.045 (1.14) MIN. 0.055 (1.40) 0.33 (8.38) MIN. 0.360 (9.14) 0.047 (1.19) 0.320 (8.13) 0.624 (15.85) K 2 0.591 (15.00) 0.670 (17.02) 0 to 0.01 (0 to 0.254) 0.591 (15.00) 0.110 (2.79) 0.110 (-0.090 (2.29) 0.037 (0.940) 0.15 (3.81) MIN. 0.021 (0.53) 0.027 (0.686) 0.014 (0.36) 0.105 (2.67) 0.140 (3.56) 0.08 (2.032) MIN. 0.095 (2.41) 0.205 (5.20) 0.110 (2.79) 0.195 (4.95) 0.105 (2.67) 0.095 (2.41)

TO-263AB

For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com



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