

Triacs

BT137B series

GENERAL DESCRIPTION

Glass passivated triacs in a plastic envelope suitable for surface mounting, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

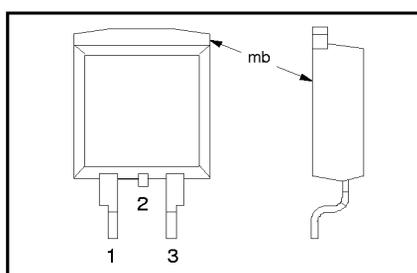
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V_{DRM}	Repetitive peak off-state voltages	BT137B-500	600	800	V
		BT137B-500F	600F	800F	
		BT137B-500G	600G	800G	
$I_{T(RMS)}$	RMS on-state current	8	8	8	A
I_{TSM}	Non-repetitive peak on-state current	55	55	55	A

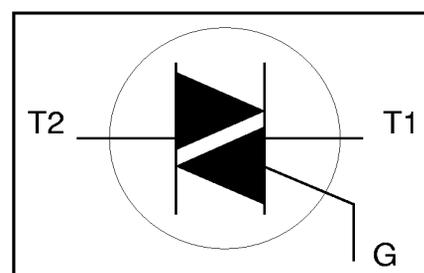
PINNING - SOT404

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
mb	main terminal 2

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
				-500	-600	-800	
V_{DRM}	Repetitive peak off-state voltages		-	500 ¹	600 ¹	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 102\text{ }^\circ\text{C}$	-	8			A
I_{TSM}	Non-repetitive peak on-state current	full sine wave; $T_j = 25\text{ }^\circ\text{C}$ prior to surge	-	55			A
I^2t	I^2t for fusing	$t = 20\text{ ms}$	-	60			A
di_T/dt	Repetitive rate of rise of on-state current after triggering	$t = 16.7\text{ ms}$	-	15			A ² s
		$t = 10\text{ ms}$	-	15			A ² s
		$I_{TM} = 12\text{ A}; I_G = 0.2\text{ A}; di_G/dt = 0.2\text{ A}/\mu\text{s}$	-	50			A/ μs
		T2+ G+	-	50			A/ μs
		T2+ G-	-	50			A/ μs
		T2- G-	-	50			A/ μs
		T2- G+	-	10			A/ μs
I_{GM}	Peak gate current		-	2			A
V_{GM}	Peak gate voltage		-	5			V
P_{GM}	Peak gate power		-	5			W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5			W
T_{stg}	Storage temperature		-40	150			$^\circ\text{C}$
T_j	Operating junction temperature		-	125			$^\circ\text{C}$

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/ μs .

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	full cycle	-	-	2.0	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	half cycle minimum footprint, FR4 board	-	55	2.4	K/W
			-		-	K/W

STATIC CHARACTERISTICS

 $T_j = 25\text{ }^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.			UNIT
I_{GT}	Gate trigger current	BT137- $V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	5F	...G	
		T2+ G+	-	8	35	25	50	mA
		T2+ G-	-	11	35	25	50	mA
		T2- G-	-	30	70	70	100	mA
		T2- G+	-	7	30	30	45	mA
I_L	Latching current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	16	45	45	60	mA
		T2+ G+	-	5	30	30	45	mA
		T2+ G-	-	7	45	45	60	mA
		T2- G-	-	5	45	45	60	mA
I_H	Holding current	$V_D = 12\text{ V}; I_{GT} = 0.1\text{ A}$	-	5	20	20	40	mA
		T2- G+	-	5	20	20	40	mA
V_T	On-state voltage	$I_T = 10\text{ A}$	-	1.3	1.65			V
V_{GT}	Gate trigger voltage	$V_D = 12\text{ V}; I_T = 0.1\text{ A}$	-	0.7	1.5			V
		$V_D = 400\text{ V}; I_T = 0.1\text{ A}; T_j = 125\text{ }^\circ\text{C}$	0.25	0.4	-			V
I_D	Off-state leakage current	$V_D = V_{DRM(max)}; T_j = 125\text{ }^\circ\text{C}$	-	0.1	0.5			mA

DYNAMIC CHARACTERISTICS

 $T_j = 25\text{ }^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			TYP.	MAX.	UNIT
dV_D/dt	Critical rate of rise of off-state voltage	BT137- $V_{DM} = 67\% V_{DRM(max)}; T_j = 125\text{ }^\circ\text{C};$ exponential waveform; gate open circuit	100F	...	200	V/ μs
dV_{com}/dt	Critical rate of change of commutating voltage	$V_{DM} = 400\text{ V}; T_j = 95\text{ }^\circ\text{C}; I_{T(RMS)} = 8\text{ A}; dl_{com}/dt = 3.6\text{ A/ms};$ gate open circuit	-	-	-	10	20	V/ μs
t_{gt}	Gate controlled turn-on time	$I_{TM} = 12\text{ A}; V_D = V_{DRM(max)}; I_G = 0.1\text{ A}; dl_G/dt = 5\text{ A}/\mu\text{s}$	-	-	-	2	-	μs

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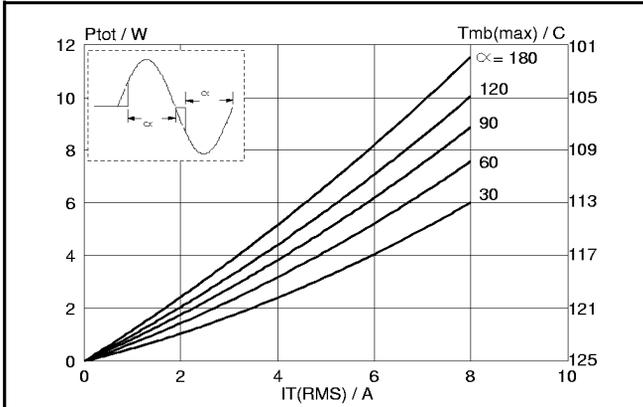


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α = conduction angle.

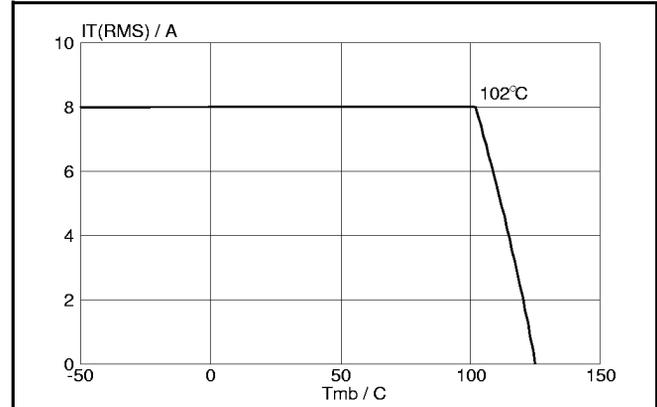


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

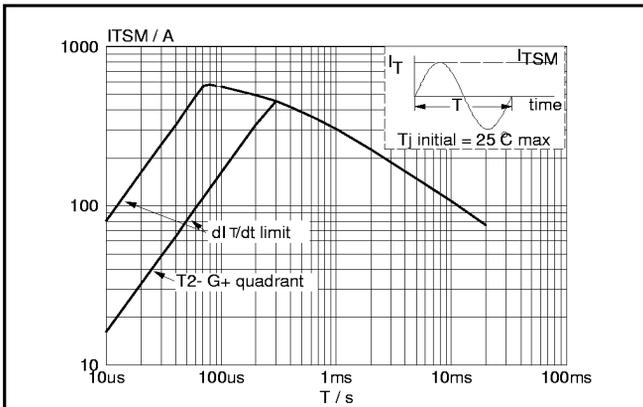


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \leq 20ms$.

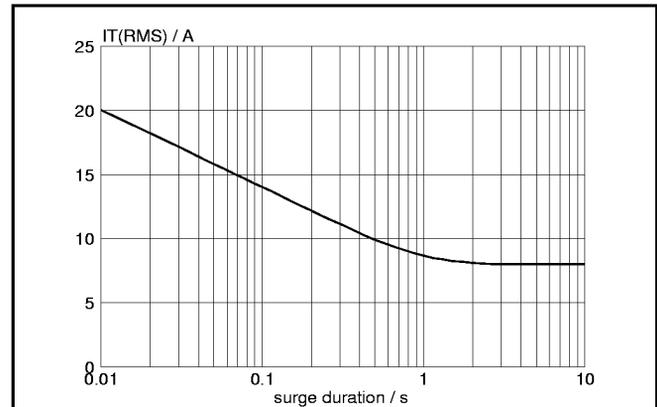


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, $f = 50\text{ Hz}$; $T_{mb} \leq 102^\circ\text{C}$.

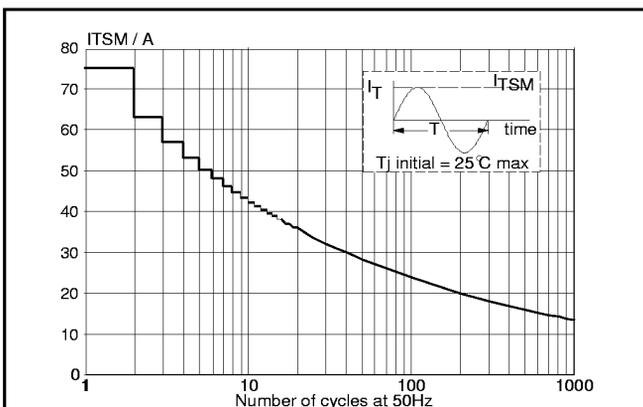


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, $f = 50\text{ Hz}$.

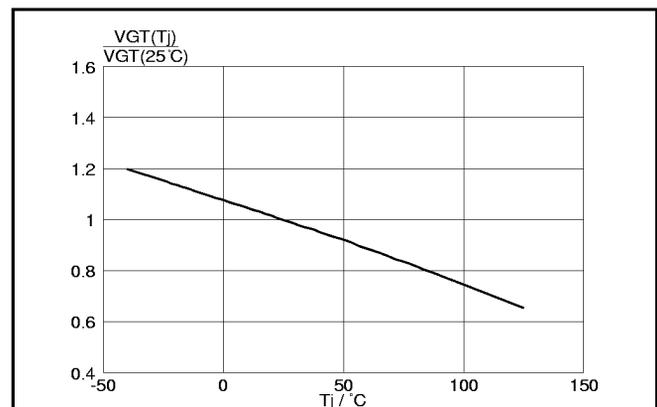
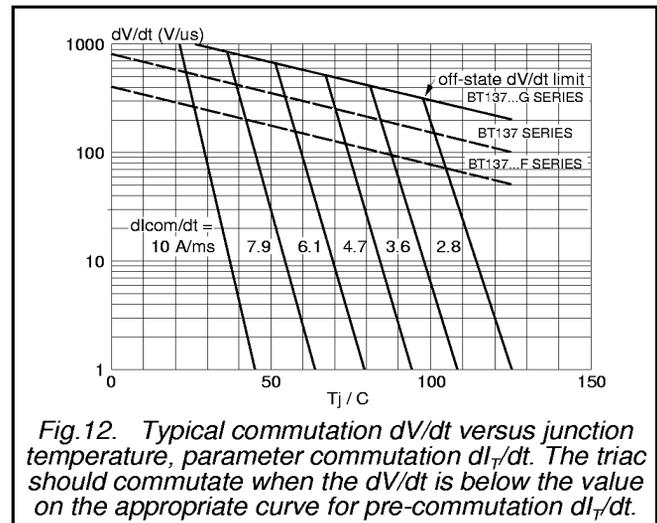
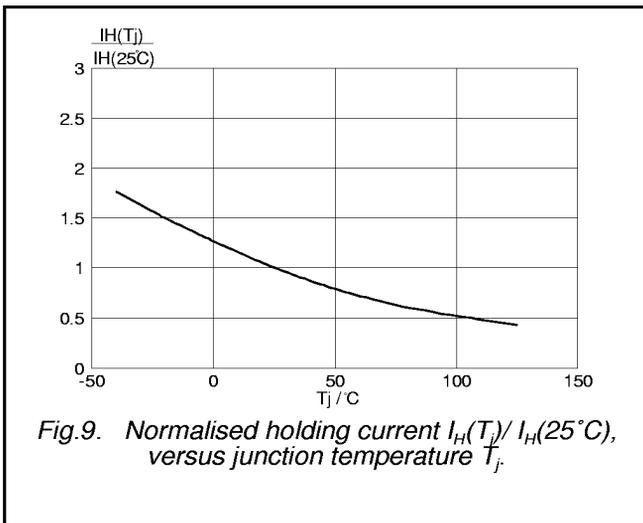
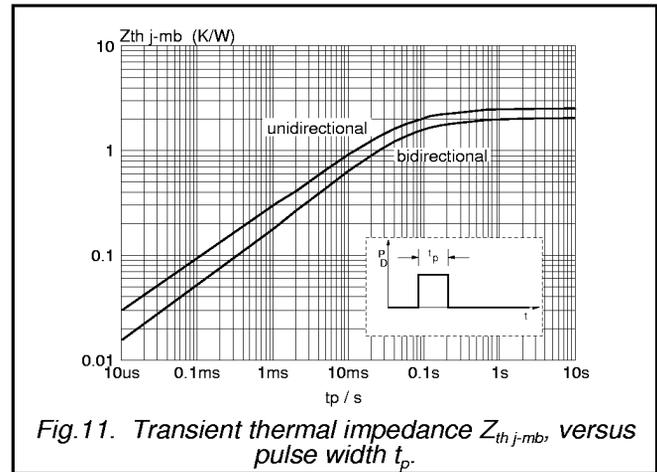
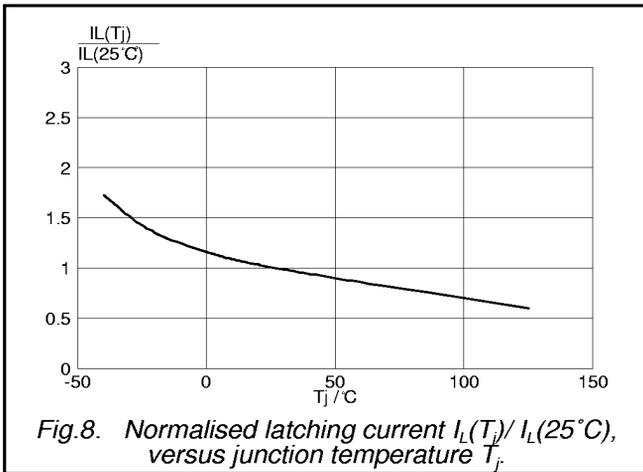
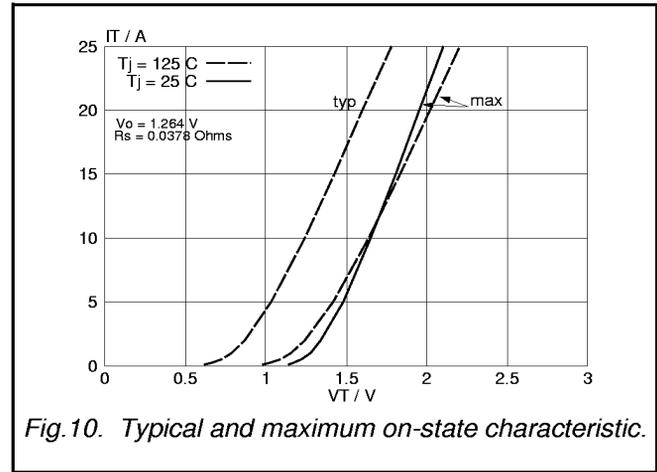
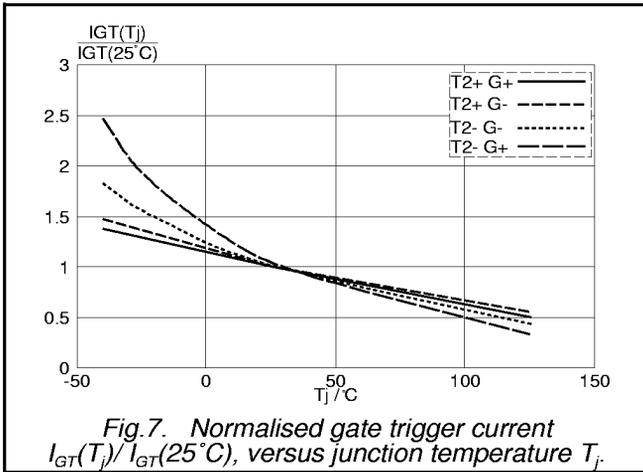


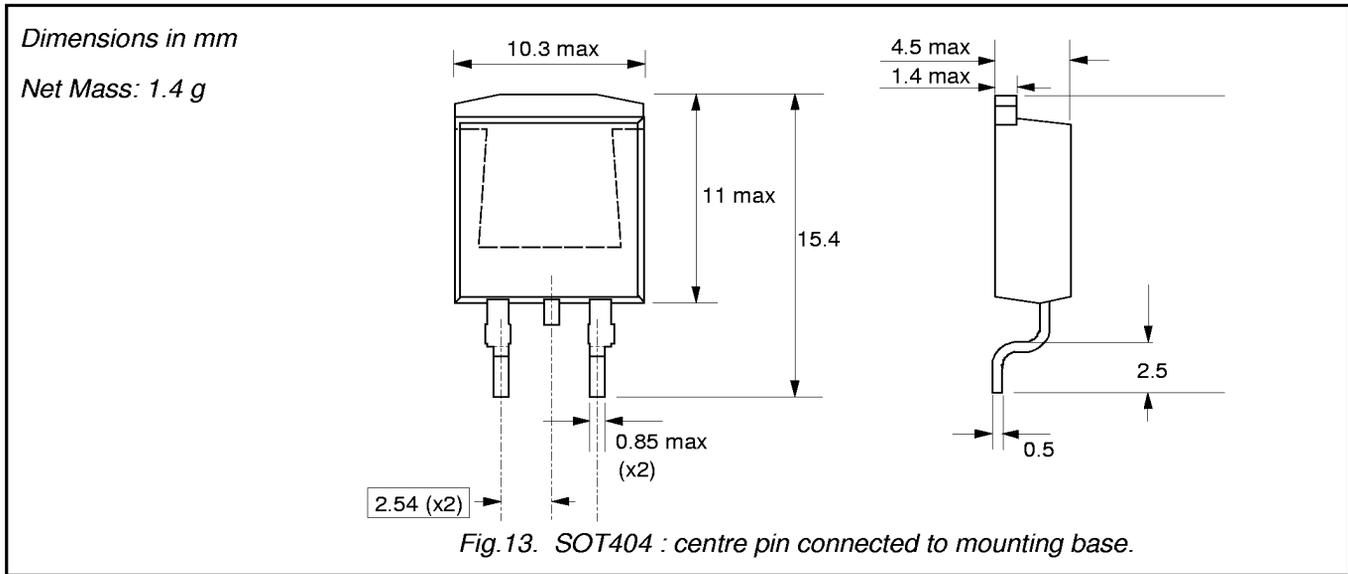
Fig.6. Normalised gate trigger voltage $V_{GT}(T_j) / V_{GT}(25^\circ\text{C})$, versus junction temperature T_j .

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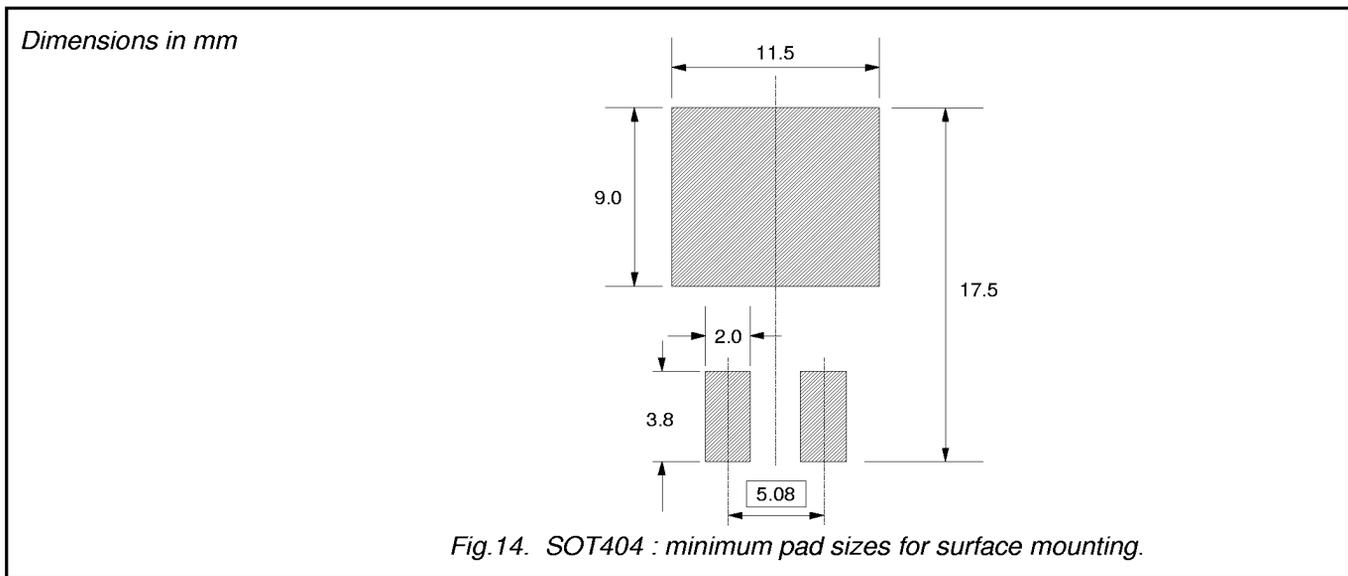
MECHANICAL DATA



Notes

- 1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS



Notes

- 1. Plastic meets UL94 V0 at 1/8".