



## Vishay General Semiconductor

## **Dual Trench MOS Barrier Schottky Rectifier**

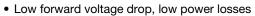
Ultra Low  $V_F = 0.32 \text{ V}$  at  $I_F = 5.0 \text{ A}$ 



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
$V_{RRM}$	60 V			
I <sub>FSM</sub>	240 A			
V <sub>F</sub> at I <sub>F</sub> = 20 A	0.48 V			
T <sub>J</sub> max.	150 °C			

### **FEATURES**

• Trench MOS Schottky technology



• High efficiency operation

Solder dip 275 °C max. 10 s, per JESD 22-B106
RoHS

Compliant to RoHS Directive 2002/95/EC an accordance to WEEE 2002/96/EC

# TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

### **MECHANICAL DATA**

Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VFT4060C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	60	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40	Α	
	per diode		20		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	240	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Isolation voltage from termal to heatsink t = 1 min		V <sub>AC</sub>	1500	V	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

## **VFT4060C**

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.43	-	. V	
	I <sub>F</sub> = 10 A			0.48	-		
	I <sub>F</sub> = 20 A			0.53	0.62		
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.32	-		
	I <sub>F</sub> = 10 A			0.39	-		
	I <sub>F</sub> = 20 A			0.48	0.57		
Reverse current per diode	V <sub>R</sub> = 60 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	6.0	mA	
	V <sub>R</sub> = 60 V	IR <sup>(−)</sup>	34	190	IIIA		

### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VFT4060C	UNIT	
Typical thermal resistance	per diode	- R <sub>θJC</sub>	5.0	- °C/W	
	per device		3.0		

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
ITO-220AB	VFT4060C-E3/4W	1.75	4W	50/tube	Tube	

### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

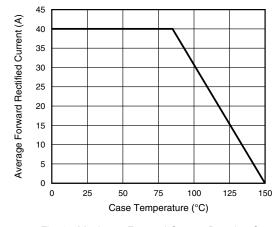


Fig. 1 - Maximum Forward Current Derating Curve

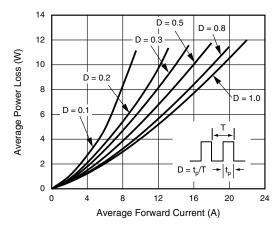


Fig. 2 - Forward Power Dissipation Characteristics Per Diode



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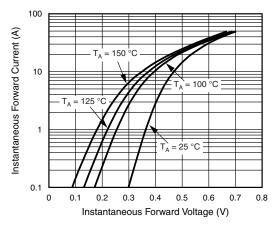


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

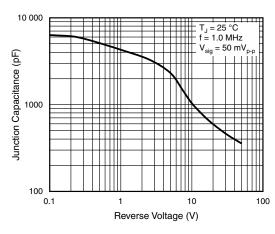


Fig. 5 - Typical Transient Thermal Impedance Per Diode

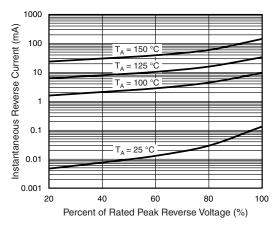


Fig. 4 - Typical Reverse Characteristics Per Diode

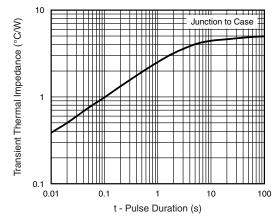
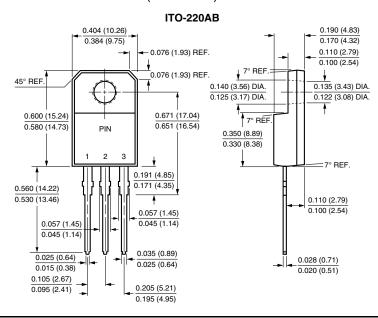


Fig. 6 - Typical Junction Capacitance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







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