

2SK3684-01L,S,SJ (500V/0.38Ω/19A)

1) Package

T-PACK

L... See Page 2/4

S... See Page 3/4

SJ... See Page 4/4

2) Absolute Maximum Ratings (Tc=25°C unless otherwise specified)

Items	Symbols	Ratings	Units
Drain-Source Voltage	V_{DS}	500	V
Continuous Drain Current	I_D	±19	A
Pulsed Drain Current	$I_{D(pulse)}$	±76	A
Gate-Source Voltage	V_{GS}	±30	V
Repetitive and Non-Repetitive Maximum Avalanche Current	I_{AR}	19	A
Non-Repetitive Maximum Avalanche Energy	E_{AS}	245.3	mJ
Maximum Drain-Source dV/dt	dV _{DS} /dt	20	kV/us
Peak Diode recovery dV/dt	dV/dt	5	kV/us
Maximum Power Dissipation	P_D @Tc=25°C	270	W
	P_D @Ta=25°C	1.67	W
Operating and Storage Temperature range	T_{ch}	150	°C
	T_{stg}	-55 ~ +150	°C

3) Electrical Characteristics (Tch=25°C unless otherwise specified)

Items	Symbols	Test Conditions	min.	typ.	max.	Units
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu A$ $V_{GS}=0V$	500	---	---	V
Gate Threshold Voltage	$V_{GS(th)}$	$I_D=250\mu A$ $V_{DS}=V_{GS}$	3.0	---	5.0	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=500V$ $T_{ch}=25^\circ C$	---	---	25	μA
		$V_{GS}=0V$ $T_{ch}=125^\circ C$	---	---	250	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V$ $V_{DS}=0V$	---	---	100	μA
Drain-Source On-State Resistance	$R_{DS(on)}$	$I_D=9.5A$ $V_{GS}=10V$	---	---	0.38	Ω
Input Capacitance	C_{iss}	$V_{DS}=25V$	---	1580	2370	pF
Output Capacitance	C_{oss}	$V_{GS}=0V$	---	240	360	
Reverse Transfer Capacitance	C_{rss}	f=1MHz	---	12	18	
Total Gate Charge	Q_g	$V_{CC}=250V$	---	32	48	nC
Gate to Source Charge	Q_{gs}	$I_D=19A$	---	16	24	
Gate to Drain (Miller) Charge	Q_{gd}	$V_{GS}=10V$	---	12	18	
Avalanche Capability	I_{AV}	L=1.25mH $T_{ch}=25^\circ C$	19	---	---	A
Diode Forward On-Voltage	V_{SD}	$I_F=19A$, $V_{GS}=0V$, $T_{ch}=25^\circ C$	---	1.0	1.5	V

4) Thermal Characteristics

Items	Symbols	Test Conditions	min.	typ.	max.	Units
Channel to Case	$R_{th(ch-c)}$				0.463	°C/W
Channel to Ambient	$R_{th(ch-a)}$				75.0	°C/W

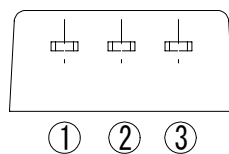
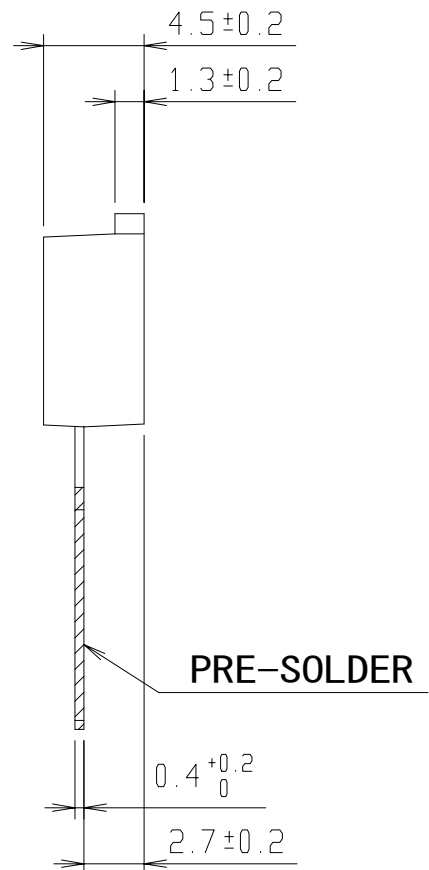
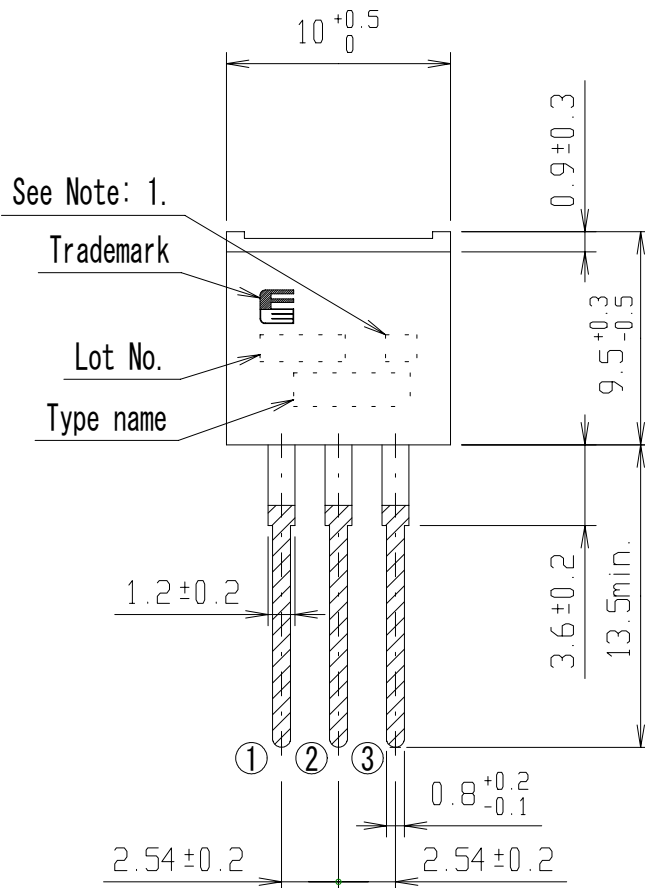
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REVISIONS

MA4LE

FUJI POWER MOS FET



CONNECTION

- ① GATE
- ② DRAIN
- ③ SOURCE

JEDEC : TO-220AB

Note: 1. Guaranteed mark of avalanche ruggedness.

DIMENSIONS ARE IN MILLIMETERS.

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FUJI POWER MOS FET

OUT VIEW

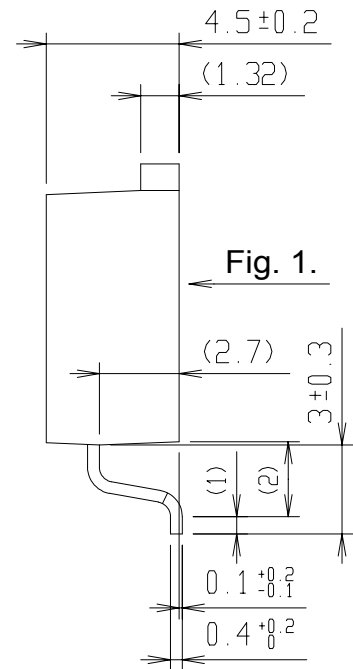
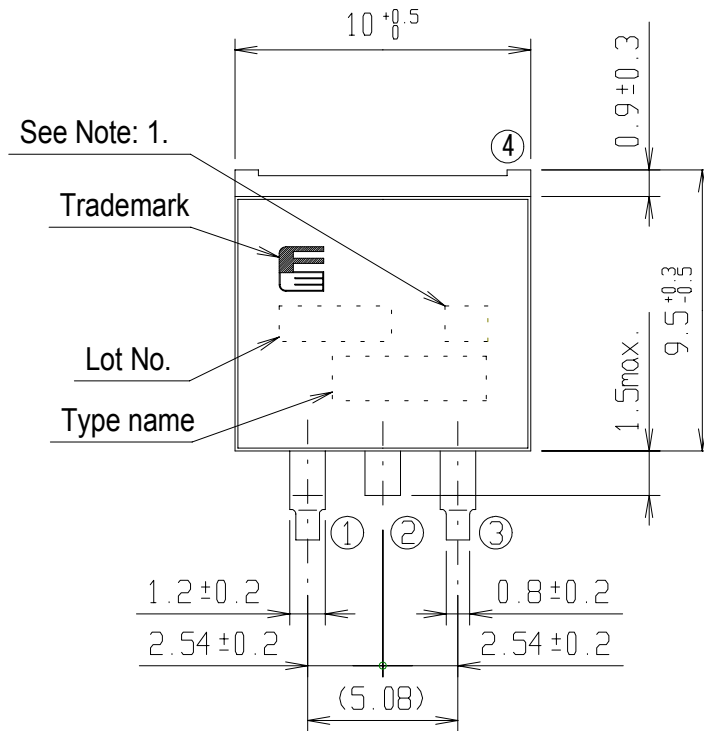
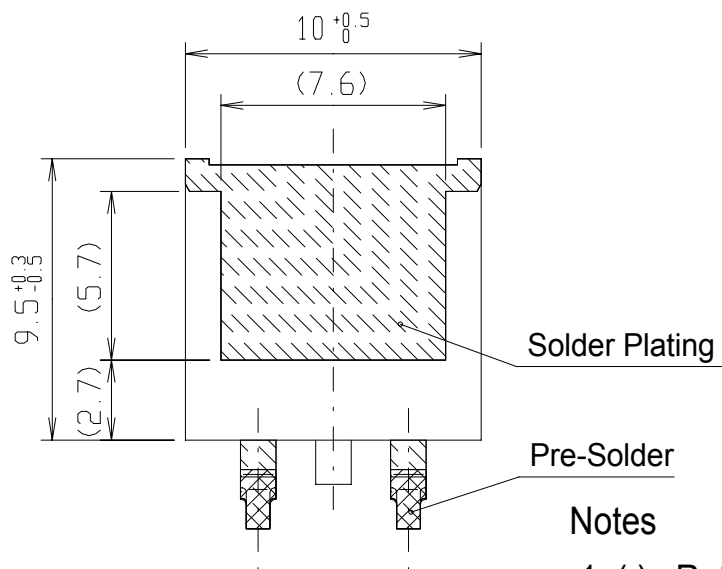


Fig. 1.



CONNECTION

- ① GATE
- ④ ② DRAIN
- ③ SOURCE

Notes

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- 1. () : Reference dimensions.
- 2. The metal part is covered with the solder plating, part of cutting is without the solder plating.

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OUT VIEW

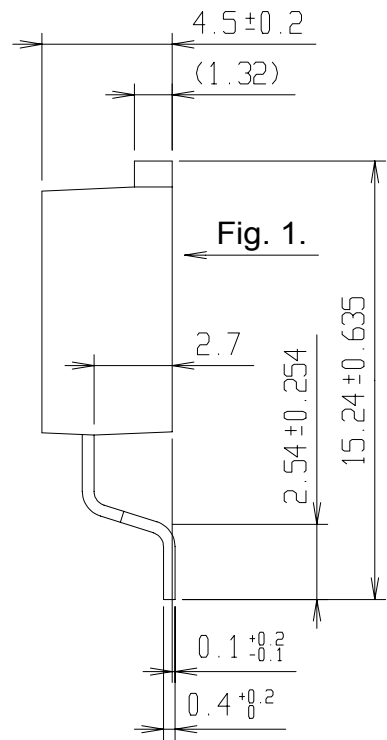
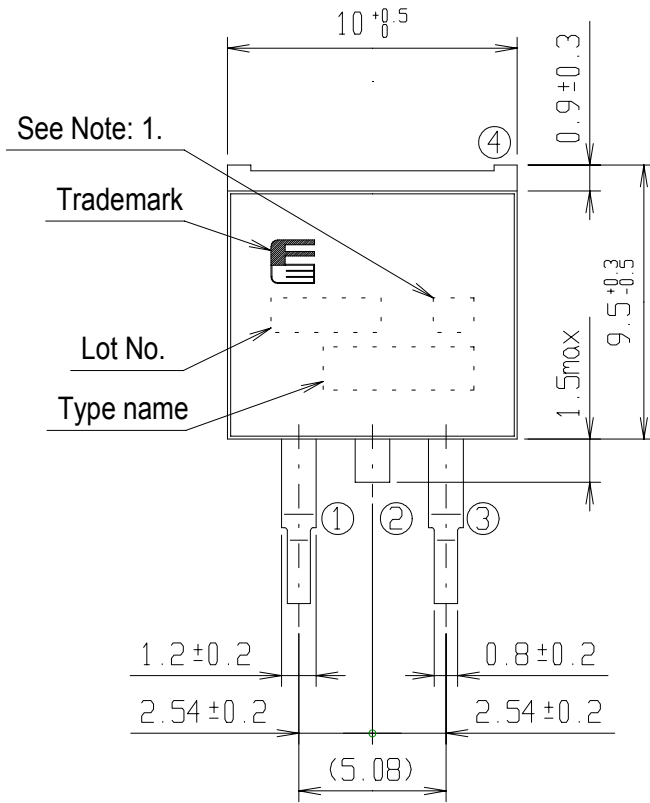
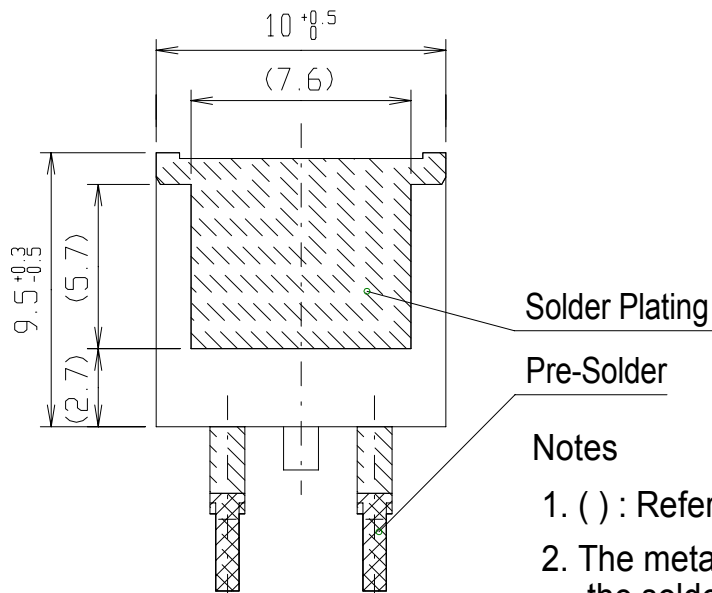


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