

Ethernet Line Ports Shaper/Scheduler Triple-100-FX 1000-X Speed Buffer Resource Manager MAC Queuing Subsystem Policer  $\times 24$ Ethernet Line Ports 100-FX Speed MAC Statistics 1000-X

Interface

- ▶ Supports full line rate (24G)
- ▶ Expandable to 48 triple-speed ports
- ▶ SPI-4.2 or XAUI host interface provides seamless connection to a variety of 10G and next-generation 20G/40G packet processors
- ▶ 10/100/1000M and 10G line ports reduce the number of components needed to build high-density line cards
- ▶ 2:1 and 4:1 oversubscription eliminates unused bandwidth at the client interface and fully utilizes shared network services processor
- ▶ Connects directly to 1G copper PHYs, 100M/1G optical SFP modules, and 10G XENPAK/X2 optical modules
- ▶ Advanced Quality of Service (QoS) guarantees delivery of differentiated services
- ▶ Full RMON 1 statistics and applicable IEEE-802.3 and SNMP statistics provide extensive support for performance mon and billing

# **KEY SPECIFICATIONS:**

- ▶ 1.2 V core power supply
- ▶ 1.8 V and 2.5 V interface power supply
- ▶ 10 W power dissipation
- ▶ 27 mm × 27 mm, 672-pin, flip chip ball grid array (FCBGA) package

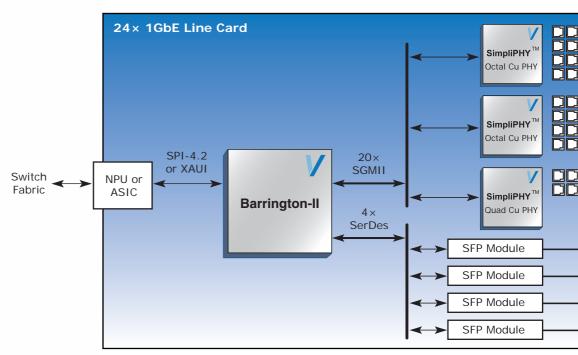


serializers/deserializers. The host interface can be configured as SPI-4.2, one XAUI, or two XAUI interfaces. Each SPI-4.2 and XAUI host interface

uses a SPI-4.2 status channel for flow control. To support the high oversubscription factor, the Barrington-II has a set of advanced QoS features

In addition to Barrington-II, Vitesse offers Scha advanced Ethernet MAC containing 12 10/100/1 the same features and functionality as Barringt

## APPLICATION DIAGRAM:



#### APPLICATIONS:

- Ethernet L2/L3 switches and routers
- ▶ Ethernet aggregation
- ▶ Ethernet over SONET
- ▶ Ethernet over DWDM
- Security appliances
- Storage networks

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