ServoDesigne	er™							
	RegisterDefs.irc - International F	Rectifier ServoDesigne	er(tm)				_ 🗆 ×	1
	Elle View Preferences Help							
		L						
	C:\RegisterDefs.irc	Write Register Name	Size	Offset	Value to Write	Current Value	Description	
	E Register Structure Definitions	Enc Count	16	0×00000000	0x00000000	0×0	New current value for 16-b	
	🖃 💋 Write Registers	Max Enc Count Z Enc Count	16 16	0×00000003 0×00000006	0×00000000 0×00000000	0×0 0×0	Maximum value of 16-bit Q Encoder count value when	
	🗈 🤷 Quad Decode	Enc Angle Scale	16	0x00000009	0x00000000	0x0	This value should be set to	
	PWM Config Gurrent Feedback Config	Enc Ctrl	8	0x0000000b	0x0	0×0	Quadrature decoder contro	٤.
	Current Feedback Config System Control							II.
	System Control							II.
	Trace Buffer Control							II.
	Velocity Control							II.
	DC Bus Voltage Monitor							II.
	Resolver Control							II.
	🕀 🕶 Read Registers							II.
	🕀 🔂 Register Allocation							II.
	E Function Definitions							II.
	🕀 🚳 Starting Angle							II.
	🕀 🔤 Configure Motor							II.
	🕀 🤷 Start Motor							II.
	🕀 😼 Stop Motor							II.
	- Status							II.
	Reference Speed							II.
	Carrier Frequency Initialize Motor							
	Monitor Definitions							II.
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International **TOR** Rectifier

IRACB201

Accelerator Bundled System with Source Code Accelerator[™] System Manual

Features

- Complete bundled system including design platform and source and object code (IRACS201, IRACO201 and IRACV201)
- Hardware torque control by Verilog code
- 230V/1.5kW output power with 600V/30A IGBTs
- Design Platform with configurable signal processing
- IR2137 and IR2171/IR2175 high voltage ICs for gate drive and current sense
- ServoDesigner[™] graphical user interface for configuration, control and monitoring

Description

IRACB201 is the bundled system for high performance servo drive development system. It contains the design platform, complete source code and the ServoDesigner application to assist with FPGA development. The design platform is based on advanced IR's high voltage IC power management component and can deliver continuous 1.5kW output. Complete Verilog library for accurate simulation up to 1.5kW.

Product Summary

Current loop bandwidth (-3dB)	5kHz		
Speed loop bandwidth (adjustable)	400Hz max		
PWM carrier frequency	40kHz max		
FOC(Field Orientation Control) execution time	e 1.5usec		
Continuous output current	8Arms		
Overload output current	22Arms		

