



A Product Line of Diodes Incorporated



ZXMP10A17K

100V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _(BR) dss	R _{DS(on)}	I _D T _A = 25°C
-100V	350mΩ @ V _{GS} = -10V	-3.9A
	450mΩ @ V _{GS} = -6.0V	-3.4A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor control
- DC-DC Converters
- Power management functions
- Uninterrupted power supply

Features and Benefits

- Fast switching speed
- Low gate drive
- Low input capacitance
- Qualified to AEC-Q101 Standards for High Reliability

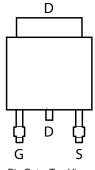
Mechanical Data

- Case: TO252-3L
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (approximate)

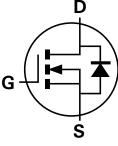


TO252-3L

Top View



Pin Out – Top View



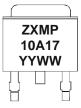
Equivalent Circuit

Ordering Information

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel	
ZXMP10A17KTC	See below	13	16	2,500	

Marking Information

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ZXMP = Product Type Marking Code, Line 1 10A17 = Product Type Marking Code, Line 2 YYWW = Date Code Marking YY = Year (ex: 09 = 2009) WW = Week (01-52)





Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source voltage			V _{DSS}	-100	V
Gate-Source voltage			V _{GS}	±20	V
Continuous Drain current		(Note 2)	- I _D	-3.9	
	$V_{GS} = 10V$	$T_{A} = 70^{\circ}C$ (Note 2)		-3.1	А
		(Note 1)		-2.4	
Pulsed Drain current V _{GS} = 10V (Note 3)		(Note 3)	I _{DM}	-11.3	А
Continuous Source current (Body diode)		(Note 2)	ls	-8.7	А
Pulsed Source current (Body diode) (N		(Note3)	I _{SM}	-11.3	А

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 1)		4.0 32.0		
Power dissipation Linear derating factor	(Note 2)	PD	10.2 80.8	W mW/°C	
	(Note 5)		2.0 16.1		
Thermal Resistance, Junction to Ambient	(Note 1) (Note 2) (Note 5)	R _{0JA}	31 12.3 62	°C/W	
Thermal Resistance, Junction to Case	(Note 4)	R _θ JL	2.4	°C/W	
Operating and storage temperature range		TJ, TSTG	-55 to 150	°C	

Notes: 1. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

2. Same as note (1), except the device is measured at t \leq 10 sec.

3. Same as note (1), except the device is pulsed with D= 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.

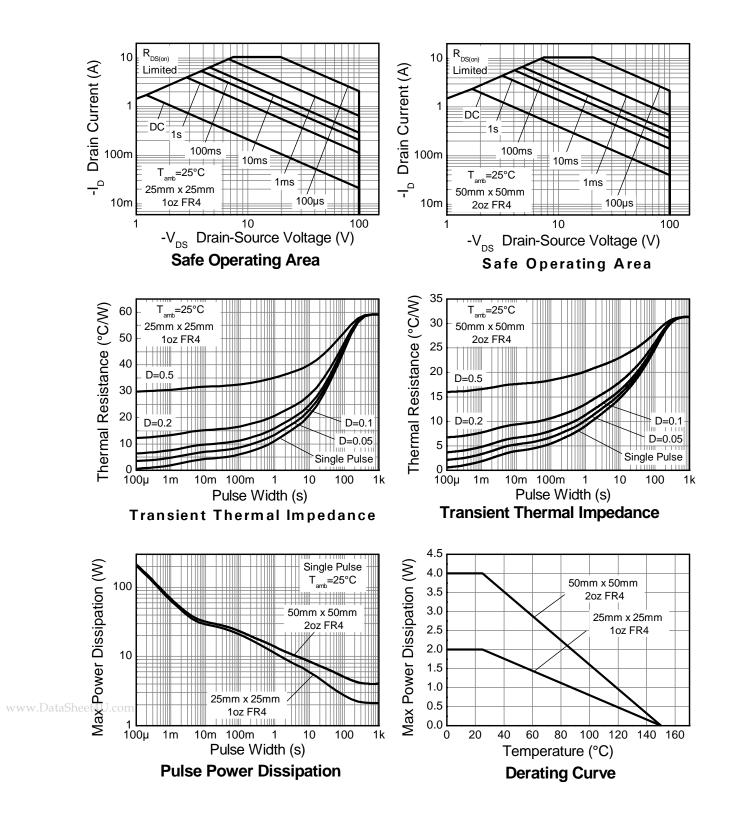
4. Thermal resistance from junction to solder-point (at the end of the drain lead).

5. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

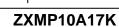
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Thermal Characteristics







Electrical Characteristics @T_A = 25°C unless otherwise specified

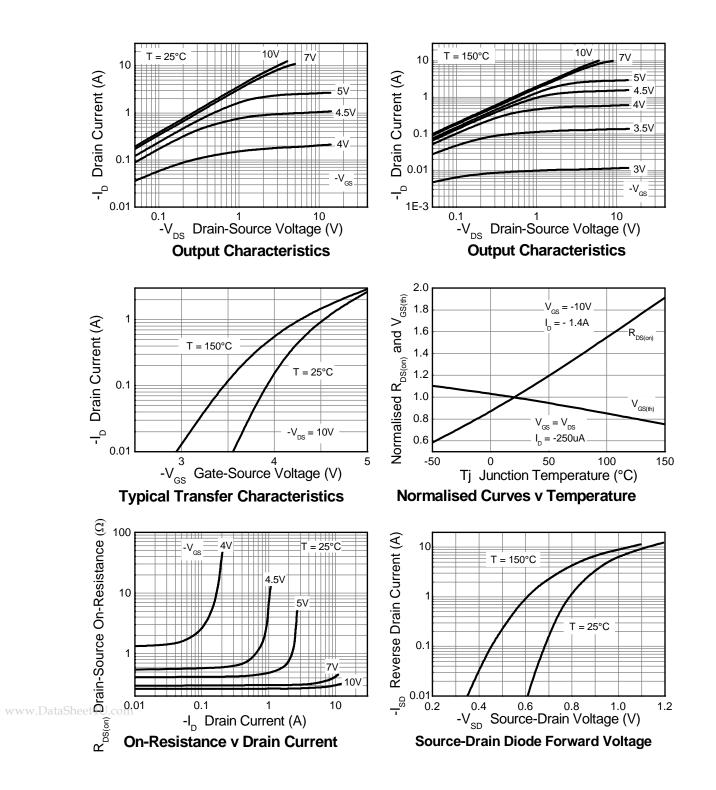
Characteristic	Symbol	Min	Тур	Max	Unit	Test	Condition		
OFF CHARACTERISTICS									
Drain-Source Breakdown Voltage	BV _{DSS}	-100		—	V	$I_D = -250 \mu A$, $V_{GS} = 0 V$			
Zero Gate Voltage Drain Current	I _{DSS}	_		-0.5	μA	V _{DS} = -100V, V ₀	V _{DS} = -100V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_		±100	nA	V _{GS} = ±20V, V _{DS} = 0V			
ON CHARACTERISTICS									
Gate Threshold Voltage	V _{GS} (th)	-2.0		-4.0	V	I_D = -250 μ A, V_D	s= V _{GS}		
Static Drain-Source On-Resistance (Note 6)	P			0.350	Ω	V _{GS} = -10V, I _D =	-1.4A		
Static Drain-Source On-Resistance (Note 6)	R _{DS (ON)}	_	_	0.450	12	V _{GS} = -6V, I _D = -	1.2A		
Forward Transconductance (Notes 6 & 7)	g fs	_	2.8	_	S	V _{DS} = -15V, I _D = -1.4A			
Diode Forward Voltage (Note 6)	V _{SD}	_	-0.85	-0.95	V	I _S = -1.7A, V _{GS} = 0V			
Reverse recovery time (Note 7)	t _{rr}		33	_	ns	la- 1 54 di/dt- 1004/00			
Reverse recovery charge (Note 7)	Qrr	_	48	_	nC	I _S = -1.5A, di/dt= 100A/μs			
DYNAMIC CHARACTERISTICS (Note 7)									
Input Capacitance	C _{iss}	—	424	—	pF				
Output Capacitance	C _{oss}	_	36.6		pF	─V _{DS} = -50V, V _{GS} = 0V ─f= 1MHz			
Reverse Transfer Capacitance	C _{rss}	_	29.8	_	pF				
Total Gate Charge (Note 8)	Qg	_	7.1	_	nC	V _{GS} = -6.0V			
Total Gate Charge (Note 8)	Qg	_	10.7	_	nC		V _{DS} = -50V		
Gate-Source Charge (Note 8)	Q _{gs}	_	1.7	_	nC	V _{GS} = -10V	I _D = -1.4A		
Gate-Drain Charge (Note 8)	Q _{gd}	_	3.8		nC]			
Turn-On Delay Time (Note 8)	t _{D(on)}	_	3.0		ns				
Turn-On Rise Time (Note 8)	tr		3.5		ns	V _{DD} = -50V, V _{GS} = -10V			
Turn-Off Delay Time (Note 8)	t _{D(off)}	_	13.4		ns	I _D = -1A, R _G ≅ 6.0Ω			
Turn-Off Fall Time (Note 8)	t _f	_	7.2	_	ns				

Notes:

Measured under pulsed conditions. Pulse width ≤ 300µs; duty cycle ≤ 2%
For design aid only, not subject to production testing.
Switching characteristics are independent of operating junction temperatures.

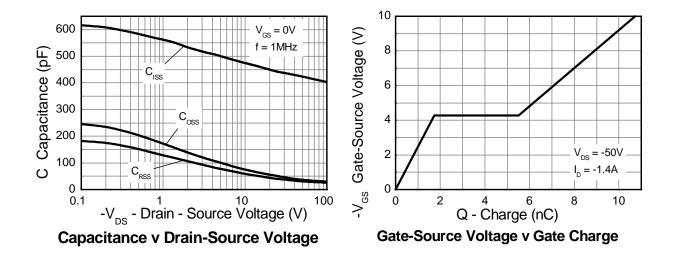


Typical Characteristics

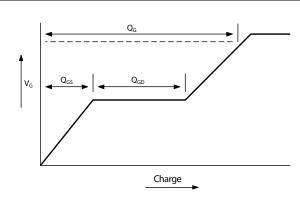




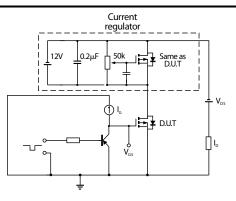
Typical Characteristics - continued



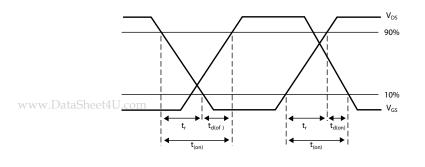
Test Circuits



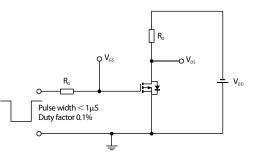
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

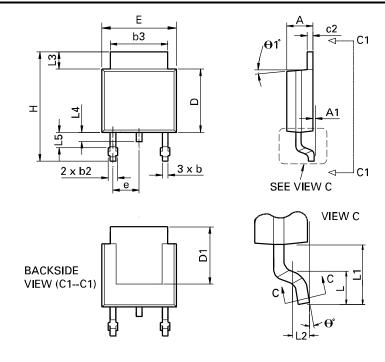


Switching time test circuit



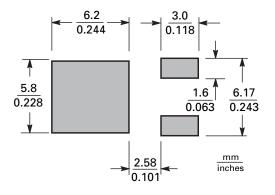


Package Outline Dimensions



DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
A1	-	0.005	-	0.127	Н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
С	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	θ1°	0°	10°	0°	10°
E	0.250	0.265	6.35	6.73	θ°	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	-	-	-	-

Suggested Pad Layout



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