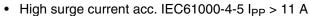


# **4-Line BUS-Port ESD-Protection**

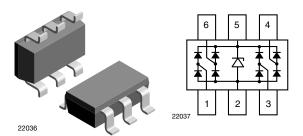
#### **Features**

- Ultra compact SOT-23-6L package
- 4-line USB ESD-protection
- · Low leakage current
- Low load capacitance C<sub>D</sub> = 1.2 pF
- ESD-protection acc. IEC 61000-4-2
  - ± 30 kV contact discharge
  - ± 30 kV air discharge





 Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC



### **Marking** (example only)



YYY = Type code (see table below) XX = Date code

### **Ordering Information**

Device name Ordering code		Taped units per reel (8 mm tape on 7" reel)	Minimum order quantity	
VBUS054CV-06S	VBUS054CV-06S-G-08	3000	15 000	

### **Package Data**

Device name	Package name	Marking code	Weight	Molding compound flammability rating	Moisture sensitivity level	Soldering conditions
VBUS054CV-06S	SOT-23-6L	4CV	15.2 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

## **Absolute Maximum Ratings**

Parameter	Test conditions	Symbol	Value	Unit
Peak pulse current	Pin 1, 3, 4 or 6 to pin 2 acc. IEC 61000-4-5; $t_P = 8/20 \mu s$ ; single shot	I <sub>PPM</sub>	11	А
	Pin 5 to pin 2 acc. IEC 61000-4-5; $t_P = 8/20 \mu s$ ; single shot	I <sub>PPM</sub>	13	Α
Peak pulse power	Pin 1, 3, 4 or 6 to pin 2 acc. IEC 61000-4-5; $t_P = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	242	W
	Pin 5 to pin 2 acc. IEC 61000-4-5; $t_P = 8/20 \mu s$ ; single shot	P <sub>PP</sub>	246	W
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV
Operating temperature	Junction temperature	$T_J$	- 40 to + 85	°C

<sup>\*\*</sup> Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

# VBUS054CV-06S

### Vishay Semiconductors



#### **Electrical Characteristics**

Ratings at 25 °C, ambient temperature unless otherwise specified

#### VBUS054CV-06S

Date line: pin 1, 3, 4 or 6 to pin 2

Parameter	Test conditions/remarks	Symbol	Min.	Тур.	Max.	Unit
Protection paths	Number of line which can be protected	N <sub>lines</sub>			4	lines
Reverse working voltage	at I <sub>R</sub> = 0.1 μA	V <sub>RWM</sub>	5.5			V
Reverse current	at $V_{IN} = V_{RWM} = 5.5 \text{ V}$	I <sub>R</sub>		0.01	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	7	7.9	8.6	V
Reverse clamping voltage	at I <sub>PP</sub> = 11 A; acc. IEC 61000-4-5	V <sub>C</sub>		18	22	V
Forward clamping voltage	at I <sub>F</sub> = 11 A; acc. IEC 61000-4-5	V <sub>F</sub>		5	6.5	V
Data line capacitance	$V_R$ (at I/O pin) = 0 V; $V_R$ (at pin 5) = 5 V; f = 1 MHz	C <sub>D</sub>		1.2	2.5	pF
Line Symmetry	Difference of the line capacitances	dC <sub>D</sub>			0.2	pF

#### VBUS054CV-06S

V<sub>BUS</sub>-line: pin 5 to pin 2

Parameter	Test conditions/remarks	Symbol	Min.	Тур.	Max.	Unit
Reverse working voltage	at $I_R = 0.1 \mu A$	$V_{RWM}$	5.5	6.6		V
Reverse current	at $V_{IN} = V_{RWM} = 5.5 V$	I <sub>R</sub>		0.01	0.1	μΑ
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	V <sub>BR</sub>	6.3	7.1	8	V
Reverse clamping voltage	at I <sub>PP</sub> = 13 A; acc. IEC 61000-4-5	V <sub>C</sub>		18	22	V
Forward clamping voltage	at I <sub>F</sub> = 13 A; acc. IEC 61000-4-5	V <sub>F</sub>			7	V
Line capacitance	$V_{R}$ (at pin 5) = 0 V; f = 1 MHz	C <sub>D</sub>		190		pF

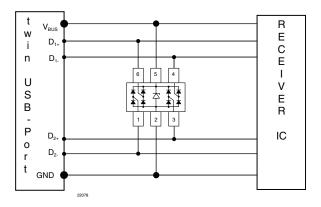
### **Application Note**

With the VBUS054CV-06S a double, high speed USB-port can be protected against transient voltage signals. Negative transients will be clamped close below the ground level while positive transients will be clamped close above the 5 V working range. An avalanche diode clamps the supply line ( $V_{BUS}$  at pin 5) to ground (pin 2). The high speed data lines, D1+, D2+, D1- and D2-, are connected to pin 1, 3, 4 and 6. As long as the signal voltage on the data lines is between the ground- and the  $V_{BUS}$ -level, the low capacitance PN-diodes offer a very high isolation to  $V_{BUS}$ , ground and to the other data lines. But as soon as any transient signal exceeds this working range, one of the PN-diodes gets in the forward mode and clamps the transient to ground or the avalanche break through voltage level.

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### **Typical Characteristics** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

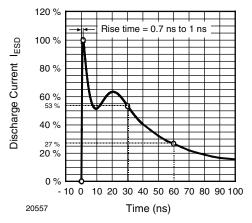


Figure 1. ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega/150$  pF)

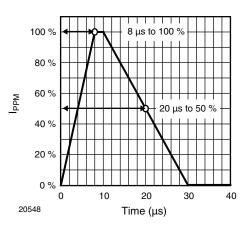


Figure 2. 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

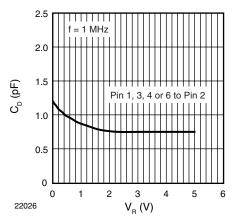


Figure 3. Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

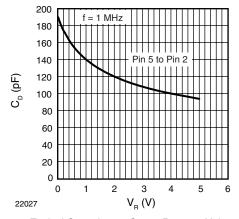


Figure 4. Typical Capacitance  $C_{\mathsf{D}}$  vs. Reverse Voltage  $V_{\mathsf{R}}$ 



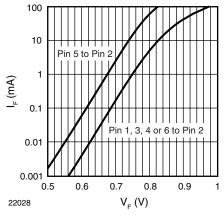


Figure 5. Typical Forward Current I<sub>F</sub> vs. Forward Voltage V<sub>F</sub>

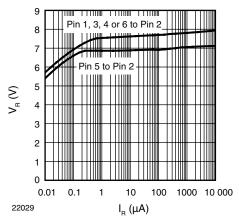


Figure 6. Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

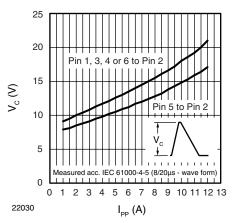


Figure 7. Typical Peak Clamping Voltage  $V_C$  vs. Peak Pulse Current  $I_{PP}$ 

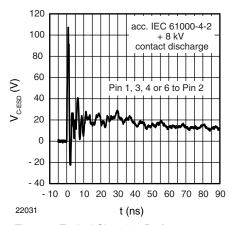


Figure 8. Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

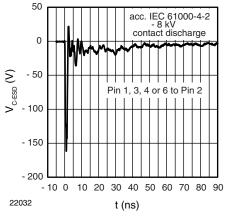


Figure 9. Typical Clamping performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

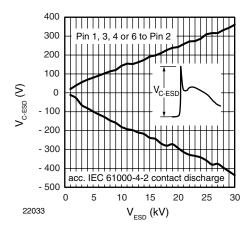
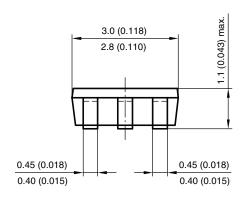
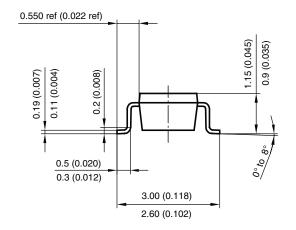


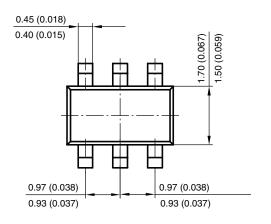
Figure 10. Typical Peak Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

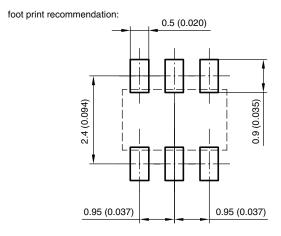


### Package Dimensions in millimeters (inches): SOT-23-6L





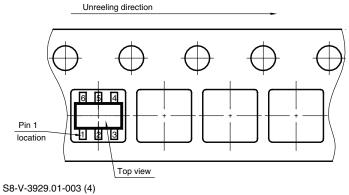




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### **Orientation in Blistertape**



Date: 23. 11. 2009

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