

Description

The μPD98001 enables simultaneous and bidirectional communication of voice and data using conventional twisted pair wire. It is a communication LSI which performs the 2-wire, time-sharing, bidirectional data transmission (Ping-Pong) in digital subscriber systems of a local communication network. This device conforms to the basic access of ISDN and enables 2B + D transmission of two 64 kb/s voice data channels and one 16 or 32 kb/s signal data channel.

Features

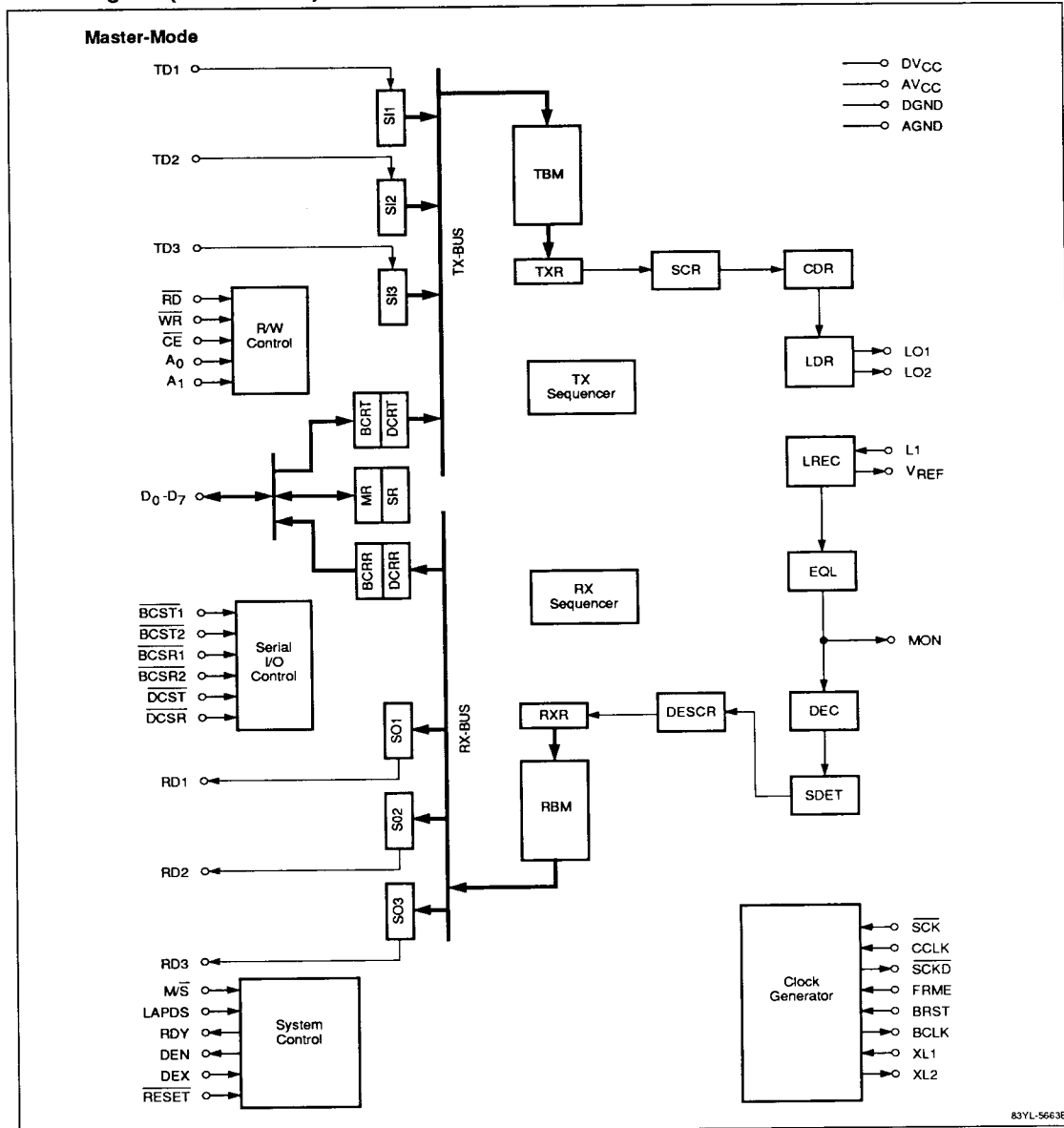
- 2-wire, time-sharing bidirectional transmission (Ping-Pong)
 - Normal mode: 144 kb/s, full duplex
 - Expansion mode: 160 kb/s, full duplex
- Built-in adaptive line equalization circuit using a switched capacitor circuit
- Attenuation
 - 18 dB (at 144 kb/s)
 - 13 dB (at 160 kb/s)

- Various built-in interface circuits:
 - General purpose MPU parallel interface
 - Multipurpose serial interface
 - CODEC interface
 - RS-232C serial interface
 - General purpose MPU serial interface
 - Selectable master/slave
 - One-chip, CMOS, monolithic IC
 - + 5 V single power supply

Ordering Information

Part Number	Package
μPD98001CW	64-pin plastic shrink DIP
L	68-pin PLCC

Block Diagram (Master Mode)



Block Diagram (Slave Mode)

