

REVISIONS																											
LTR	DESCRIPTION	DATE (YR-MO-DA)	APPROVED																								
A	Figure 1, terminal connections, changed to tabular format. Add new figure 4. Technical changes to table I. Editorial changes throughout.	1989 March 8	<i>M.A. Lye</i>																								

REV																														
SHEET																														
REV																														
SHEET																														
REV STATUS OF SHEETS	REV	A	A	A	A	A	A	A	A	A	A	A	A	A	A	A														
	SHEET	1	2	3	4	5	6	7	8	9	10	11	12	13	14															

PMIC N/A  <b>STANDARDIZED MILITARY DRAWING</b>  THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE  AMSC N/A	PREPARED BY <i>Larry T. Gauder</i> CHECKED BY <i>Wm. Johnson</i> APPROVED BY <i>[Signature]</i> DRAWING APPROVAL DATE 24 July 1987 REVISION LEVEL A	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444  MICROCIRCUIT, DIGITAL, ECL, TRIPLE, 4-3-3 INPUT NOR GATE, MONOLITHIC SILICON  <table style="width: 100%;"> <tr> <td style="width: 10%;">SIZE <b>A</b></td> <td style="width: 20%;">CAGE CODE <b>67268</b></td> <td style="width: 70%;"><b>5962-87564</b></td> </tr> <tr> <td colspan="2">SHEET</td> <td>1 OF 14</td> </tr> </table>	SIZE <b>A</b>	CAGE CODE <b>67268</b>	<b>5962-87564</b>	SHEET		1 OF 14
SIZE <b>A</b>	CAGE CODE <b>67268</b>	<b>5962-87564</b>						
SHEET		1 OF 14						

DESC FORM 193  
SEP 87

• U.S. GOVERNMENT PRINTING OFFICE: 1987 — 748-129/60911

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

5962-E1159-3

# 1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

5962-87564	01	E	X
┆	┆	┆	┆
┆	┆	┆	┆
┆	┆	┆	┆
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	10H506	Triple 4-3-3 input NOR gate

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, .840" x .310" x .200"), dual-in-line package
F	F-5 (16-lead, .440" x .285" x .085"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

## 1.3 Absolute maximum ratings.

Supply voltage range ( $V_{EE}$ )	- - - - -	-8.0 V dc minimum to 0.0 V dc maximum
Input voltage range	- - - - -	-5.2 V dc to 0.0 V dc
Storage temperature range	- - - - -	-65°C to +165°C
Lead temperature (soldering 10 seconds)	- - - - -	+300°C
Junction temperature ( $T_J$ )	- - - - -	+165°C
Maximum power dissipation ( $P_D$ )	- - - - -	150 mW
Thermal resistance, junction-to-case ( $\theta_{JC}$ )	- - - - -	See MIL-M-38510, appendix C

## 1.4 Recommended operating conditions.

Supply voltage range ( $V_{EE}$ )	- - - - -	-5.46 V dc minimum to -4.94 V dc maximum
Supply voltage range ( $V_{CC}$ )	- - - - -	-0.02 V dc to 0.02 V dc or 1.98 V dc to 2.02 V dc
Ambient operating temperature range ( $T_A$ )	- - - - -	-55°C to +125°C
Minimum high level input voltage ( $V_{IH}$ ):		
$T_A = +25^\circ\text{C}$	- - - - -	-0.780 V dc
$T_A = +125^\circ\text{C}$	- - - - -	-0.650 V dc
$T_A = -55^\circ\text{C}$	- - - - -	-0.840 V dc
Maximum low level input voltage ( $V_{IL}$ )	- - - - -	-1.950 V dc

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL <b>A</b>	SHEET <b>2</b>

DESC FORM 193A  
SEP 87

\* U. S. GOVERNMENT PRINTING OFFICE: 1968-549-904

## 2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

### SPECIFICATION

#### MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

### STANDARD

#### MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

## 3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth tables. The truth tables shall be as specified on figure 2.

3.2.3 Logic diagrams. The logic diagrams shall be as specified on figure 3.

3.2.4 Test circuit and switching waveforms. The test circuit and switching waveforms shall be as specified on figure 4.

3.2.5 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL <b>A</b>	SHEET <b>3</b>

DESC FORM 193A  
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

TABLE I. Electrical performance characteristics.								
Test	Symbol	Conditions -55°C < T <sub>A</sub> < +125°C unless otherwise specified	Group A subgroups	Limits		Unit		
				Min	Max			
Cases E, F, and 2		Quiescent conditions 1/						
High level output voltage	V <sub>OH</sub>	Outputs terminated through 100Ω to -2.0 V	V <sub>IH</sub>	V <sub>IL</sub>				
			-0.780	-1.950	1	-1.010	-0.780	V
			-0.650	-1.950	2	-0.860	-0.650	
Low level output voltage	V <sub>OL</sub>	V <sub>CC</sub> = 0.0 V V <sub>EE</sub> = -5.2 V  2/	-0.840	-1.950	3	-1.060	-0.840	
			-0.780	-1.950	1	-1.950	-1.580	V
			-0.650	-1.950	2	-1.950	-1.565	
High level threshold output voltage	V <sub>OHA</sub>		-0.840	-1.950	3	-1.950	-1.610	
			-1.110	-1.480	1	-1.010	-0.780	V
			-0.960	-1.465	2	-0.860	-0.650	
Low level threshold output voltage	V <sub>OLA</sub>		-1.160	-1.510	3	-1.060	-0.840	
			-1.110	-1.480	1	-1.950	-1.580	V
			-0.960	-1.465	2	-1.950	-1.565	
Power supply drain current 3/	I <sub>EE</sub>	V <sub>EE</sub> = -5.46 V V <sub>CC</sub> = 0.0 V			1	-20		mA
					2, 3	-23		
High level input current	I <sub>IH</sub>	V <sub>IH</sub> = -0.780 at +25°C V <sub>IH</sub> = -0.650 at +125°C V <sub>IH</sub> = -0.840 at -55°C			1, 2		310	μA
					3		500	
Low level input current	I <sub>IL</sub>	V <sub>EE</sub> = -4.94 V V <sub>IL</sub> = -1.950 V 3/ V <sub>CC</sub> = 0.0 V			1, 3	0.5		μA
					2	0.3		
Functional tests		See 4.3.1c			7, 8			
See footnotes at end of table.								
STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444			SIZE A		5962-87564			
				REVISION LEVEL A	SHEET 4			

DESC FORM 103A  
SEP 87

\* U. S. GOVERNMENT PRINTING OFFICE: 1968-549-904

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T <sub>A</sub> < +125°C unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
Cases E and F DC rapid test conditions 4/						
High level output voltage	V <sub>OH</sub>	Outputs terminated through 100Ω to -2.0 V V <sub>CC</sub> = 0.0 V V <sub>EE</sub> = -5.2 V  2/          3/   				

See footnotes at end of table.

**STANDARDIZED  
MILITARY DRAWING**

DEFENSE ELECTRONICS SUPPLY CENTER  
DAYTON, OHIO 45444

SIZE  
**A**

5962-87564

REVISION LEVEL

A

SHEET

5

DESC FORM 193A  
SEP 87

\* U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T <sub>A</sub> < +125°C unless otherwise specified	Group A subgroups	Limits		Unit		
				Min	Max			
Case 2 DC rapid test conditions 4/								
High level output voltage	V <sub>OH</sub>	Outputs terminated through 100Ω to -2.0 V V <sub>CC</sub> = 0.0 V V <sub>EE</sub> = -5.2 V	V <sub>IH</sub>	V <sub>IL</sub>				
			-0.789	-1.950	1	-1.019	-0.789	V
			-0.660	-1.950	2	-0.869	-0.660	
Low level output voltage	V <sub>OL</sub>	2/	-0.850	-1.950	3	-1.069	-0.850	
			-0.789	-1.950	1	-1.950	-1.583	V
			-0.660	-1.950	2	-1.950	-1.568	
High level threshold output voltage	V <sub>OHA</sub>		-0.850	-1.950	3	-1.950	-1.613	
			-1.119	-1.483	1	-1.019	-0.789	V
			-0.969	-1.468	2	-0.869	-0.660	
Low level threshold output voltage	V <sub>OLA</sub>		-1.169	-1.513	3	-1.069	-0.850	
			-1.119	-1.483	1	-1.950	-1.583	V
			-0.969	-1.468	2	-1.950	-1.568	
Power supply drain current 3/	I <sub>EE</sub>	V <sub>EE</sub> = -5.46 V V <sub>CC</sub> = 0.0 V	1 2, 3	-20 -22		mA		
High level input current	I <sub>IH</sub>	V <sub>IH</sub> = -0.789 at +25°C = -0.660 at +125°C = -0.850 at -55°C	1, 2 3		295 485	μA		
Low level input current	I <sub>IL</sub>	V <sub>EE</sub> = -4.94 V V <sub>IL</sub> = -1.950 V V <sub>CC</sub> = 0.0 V 3/	1, 3 2	0.5 0.3		μA		
Functional tests		See 4.3.1c	7, 8					
Cases E, F, and 2 AC test conditions								
Transition time	t <sub>TLH</sub> t <sub>THL</sub>	V <sub>EE</sub> = -2.94 V <sub>CC</sub> = 2.0 V C <sub>L</sub> ≤ 5 pF Load all outputs through 100Ω to GND	9	0.50	1.80	ns		
			10	0.55	1.90			
			11	0.50	1.70			
Propagation delay time, any input to output	t <sub>PHH</sub> t <sub>PLL</sub> t <sub>PHL</sub> t <sub>PLH</sub>		9	0.50	1.75	ns		
			10	0.55	1.80			
			11	0.50	1.70			
See footnotes on next page.								
STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		SIZE A	5962-87564					
			REVISION LEVEL A	SHEET 6				

DESC FORM 193A  
SEP 87

\* U. S. GOVERNMENT PRINTING OFFICE: 1988-548-904

- 1/ The quiescent limits are determined after a device has reached thermal equilibrium. This is defined as the reading taken with the device in a socket with > 500 LFPM of +25°C, +125°C or -55°C (as applicable) air blowing on the unit in a transverse direction with power applied for at least 4 minutes before the reading is taken. This method was used for theoretical limit establishment only. All devices shall be tested to the delta V (rapid test) conditions specified herein. The rapid test method is an equivalent method of testing quiescent conditions.
- 2/ The high and low level output currents vary with temperature, and shall be calculated using the following formulas:  

$$I_{OH} = (V_{OH} - 2.0 \text{ V}) / 100\Omega$$

$$I_{OL} = (V_{OL} - 2.0 \text{ V}) / 100\Omega$$
- 3/ The  $I_{EE}$  and  $I_{IL}$  limits, although specified in the minimum column, shall not be exceeded, in magnitude, as a maximum value.
- 4/ The dc rapid test forcing functions and limits are used for all dc testing. These limits are determined for each device type based on the power dissipation and package type. The rapid test (delta V) limits and forcing functions are skewed allowing rapid testing to be performed at standard temperatures without the addition of delta T's.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

#### 4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
  - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
  - (2)  $T_A = +125^\circ\text{C}$ , minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL <b>A</b>	SHEET <b>7</b>

DESC FORM 193A  
SEP 87

☆ U. S. GOVERNMENT PRINTING OFFICE: 1988-549-904

Device type	01		
Case outlines	E	F	2
Terminal number	Terminal symbol		
1	VCC1	C2	NC
2	Y2	C3	VCC1
3	Y1	Y3	Y2
4	A1	VCC2	Y1
5	A2	VCC1	A1
6	A3	Y2	NC
7	A4	Y1	A2
8	VEE	A1	A3
9	B1	A2	A4
10	B2	A3	VEE
11	B3	A4	NC
12	C1	VEE	B1
13	C2	B1	B2
14	C3	B2	B3
15	Y3	B3	C1
16	VCC2	C1	NC
17	---	---	C2
18	---	---	C3
19	---	---	Y3
20	---	---	VCC2

NC = No connection

FIGURE 1. Terminal connection.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>	5962-87564	
		REVISION LEVEL A	SHEET 8

DESC FORM 193A  
SEP 87

☆ U.S. GOVERNMENT PRINTING OFFICE: 1987 - 748-129-60913



Input				Output
A <sub>1</sub>	A <sub>2</sub>	A <sub>3</sub>	A <sub>4</sub>	Y <sub>1</sub>
L	L	L	L	H
L	L	L	H	L
L	L	H	L	L
L	L	H	H	L
L	H	L	L	L
L	H	L	H	L
L	H	H	L	L
L	H	H	H	L
H	L	L	L	L
H	L	L	H	L
H	L	H	L	L
H	L	H	H	L
H	H	L	L	L
H	H	L	H	L
H	H	H	L	L
H	H	H	H	L

L = Low voltage level  
H = High voltage level

Input			Output
B <sub>1</sub> /C <sub>1</sub>	B <sub>2</sub> /C <sub>2</sub>	B <sub>3</sub> /C <sub>3</sub>	Y <sub>2</sub> /Y <sub>3</sub>
L	L	L	H
L	L	H	L
L	H	L	L
L	H	H	L
H	L	L	L
H	L	H	L
H	H	L	L
H	H	H	L

L = Low voltage level  
H = High voltage level

FIGURE 2. Truth tables.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL A	SHEET 9

DESC FORM 193A  
SEP 87

☆ U.S. GOVERNMENT PRINTING OFFICE: 1987 - 748-129-60913

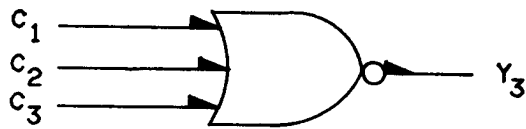
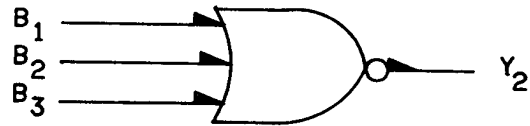
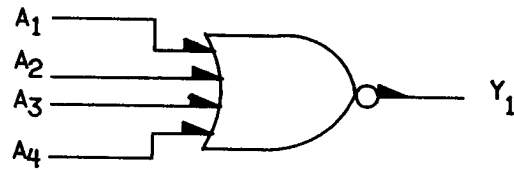


FIGURE 3. Logic diagram.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL A	SHEET 10

DESC FORM 193A  
SEP 87

☆U.S. GOVERNMENT PRINTING OFFICE: 1987 - 746-129-60913

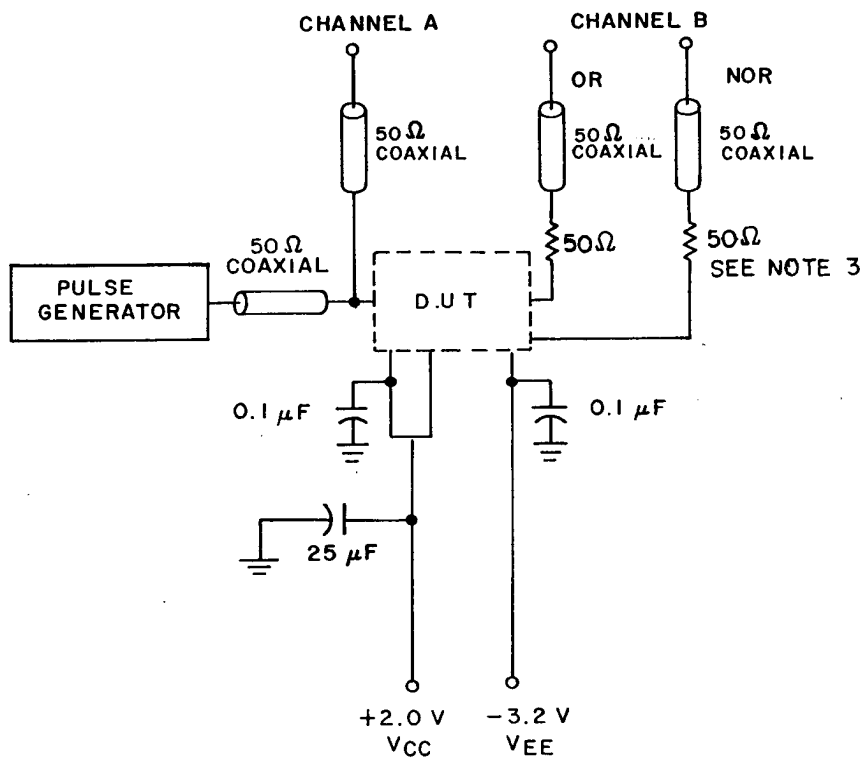
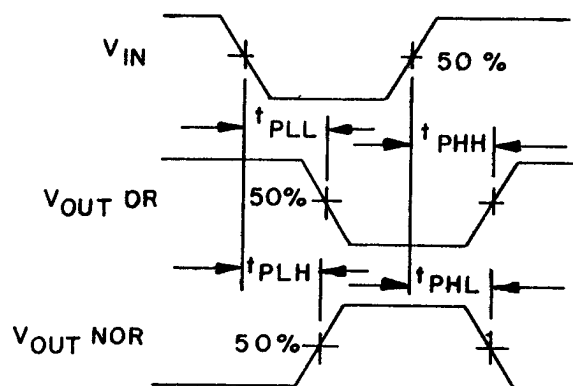


FIGURE 4. Test circuit and switching waveform.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL A	SHEET 11

DESC FORM 193A  
SEP 87

☆ U.S. GOVERNMENT PRINTING OFFICE: 1987 - 748-129-60913



NOTES:

1. Pulse generator characteristics:  
 $PRR = 1 \text{ MHz}$ ,  $t_{THL} = t_{TLH} = 1.0 \pm 0.2 \text{ ns}$  (20% to 80%), duty cycle = 50%.
2. All other outputs are loaded through  $100\Omega$  to GND.
3. The  $50\Omega$  resistor in series with the  $50\Omega$  coaxial constitutes the  $100\Omega$  load.

FIGURE 4. Test circuit and switching waveform - Continued.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>	5962-87564	
	REVISION LEVEL A		SHEET 12

DESC FORM 193A  
SEP 87

U.S. GOVERNMENT PRINTING OFFICE: 1987 748-129-60913

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroups 7 and 8 tests shall verify the truth tables as specified on figure 2.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
  - (1) Test condition D, using the circuit submitted with the certificate of compliance (see 3.5 herein).
  - (2)  $T_A = +125^{\circ}\text{C}$ , minimum.
  - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*,2,3,7*,8,9
Group A test requirements (method 5005)	1,2,3,7,8,9, 10,11
Groups C and D end-point electrical parameters (method 5005)	1,2,3

\* PDA applies to subgroups 1 and 7.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL <b>A</b>	SHEET <b>13</b>

DESC FORM 193A  
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1988-549-804

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8756401EX	04713	10H506/BEAJC
5962-8756401FX	04713	10H506/BFAJC
5962-87564012X	04713	10H506M/B2AJC

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

04713

Vendor name and address

Motorola, Incorporated  
7402 South Price Road  
Tempe, AZ 85283

<b>STANDARDIZED MILITARY DRAWING</b> DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE <b>A</b>		5962-87564
		REVISION LEVEL <b>A</b>	SHEET <b>14</b>

DESC FORM 193A  
SEP 87

★ U. S. GOVERNMENT PRINTING OFFICE: 1988-548-904

011857 \_ \_ \_