2-channle Multiplexer

HITACHI

ADE-205-686 (Z)

Rev.0 May. 2002

Description

The HD74LV2GT157A has 2–channel multiplexer in 8 pin package. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

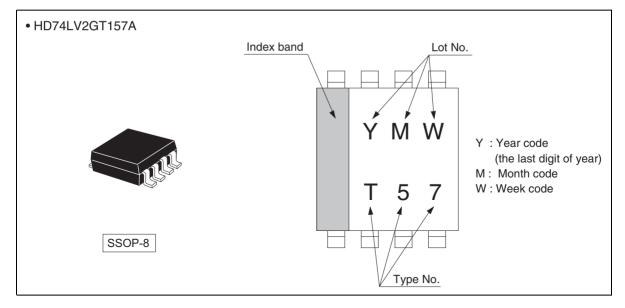
Features

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- TTL compatible input level. Supply voltage range : 4.5 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{cc} = 0 V to 5.5 V) All outputs V_{0} (Max.) = 5.5 V (@V_{cc} = 0 V)
- Output current $\pm 12 \text{ mA} (@V_{cc} = 4.5 \text{ V to } 5.5 \text{ V})$
- All the logical input has hysteresis voltage for the slow transition.
- Package type

Package type	Package code	Package suffix	Taping code
SSOP-8 pin	TTP-8DB	US	E (3,000 pcs / Reel)



Outline and Article Indication



Function Table

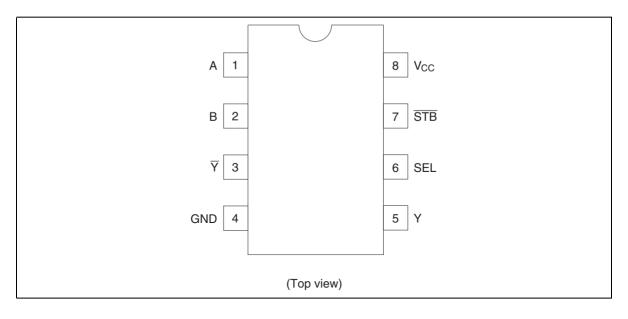
Inputs				Outputs		
STB	SEL	Α	В	Y	Ϋ́	
Н	Х	Х	Х	L	Н	
L	L	L	Х	L	Н	
L	L	Н	Х	Н	L	
L	Н	Х	L	L	Н	
L	Н	Х	Н	Н	L	

H : High level

L : Low level

X : Immaterial

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range *1	V	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	–0.5 to V $_{\rm cc}$ + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{cc} : OFF
Input clamp current	Ι _{ικ}	-20	mA	V ₁ < 0
Output clamp current	I _{ок}	±50	mA	V_{o} < 0 or V_{o} > V_{cc}
Continuous output current	I _o	±25	mA	$V_{o} = 0$ to V_{cc}
Continuous current through V_{cc} or GND	$I_{\rm cc}$ or $I_{\rm gnd}$	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ³	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

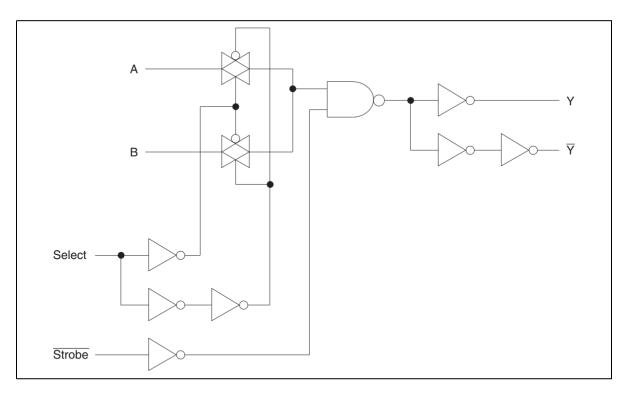
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{cc}	4.5	5.5	V	
Input voltage range	V	0	5.5	V	
Output voltage range	Vo	0	V _{cc}	V	
Output current	I _{ol}	_	12	mA	V_{cc} = 4.5 to 5.5 V
	I _{он}	_	-12		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	$\Delta t / \Delta v$	0	20	ns / V	$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	T _a	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



Electrical Characteristic

• Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
Input voltage	V _{IH}	4.5 to 5.5	2.0	_	_	V	
	V _{IL}	4.5 to 5.5	_	_	0.8		
Hysteresis voltage	V _H	5.0	_	0.15	_	V	$V_{T}^{+} - V_{T}^{-}$
Output voltage	V _{oh}	Min to Max	V _{cc} -0.1	_	_	V	I _{он} = -50 μA
		4.5	3.8	_	_		I _{он} = –12 mA
	V _{ol}	Min to Max	_	_	0.1		I _{oL} = 50 μA
		4.5	_	_	0.55		I _{oL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{cc}	5.5	_	_	10	μA	$V_{IN} = V_{CC}$ or GND, $I_{O} = 0$
	ΔI_{cc}	5.5	—	—	1.5	mA	One input $V_{IN} = 3.4 V$, other input V_{CC} or GND
Output leakage current		0	_	—	5	μA	$V_{\rm IN}$ or $V_{\rm o}$ = 0 to 5.5 V
Input capacitance	C	5.0		3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{cc} = 5.0 \pm 0.5 \text{ V}$

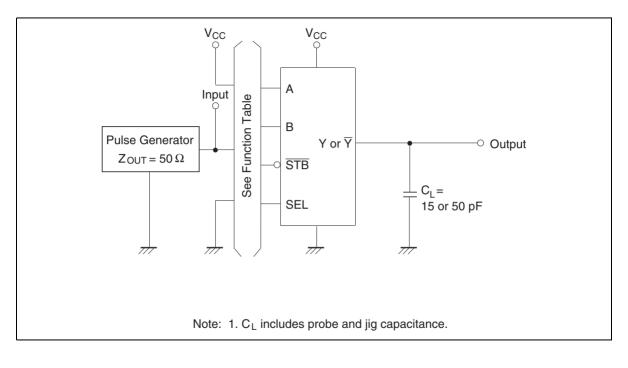
Item	Symbol	Ta =	25°C		Ta = -40 to 85°C		Unit			то
		Min	Тур	Max	Min	Max	-	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	5.1	7.4	1.0	8.5	ns	C _L = 15 pF	A or B	Y
delay time	t _{PHL}	_	6.6	9.4	1.0	10.5	_	$C_{L} = 50 \text{ pF}$	-	
		_	6.3	9.1	1.0	10.5		C _L = 15 pF	SEL	Y
		_	7.8	11.1	1.0	12.5	_	$C_{L} = 50 \text{ pF}$	-	
		_	5.6	8.6	1.0	10.0		$C_{L} = 15 \text{ pF}$	STB	Y
		_	7.1	10.6	1.0	12.0	_	$C_{L} = 50 \text{ pF}$	_	

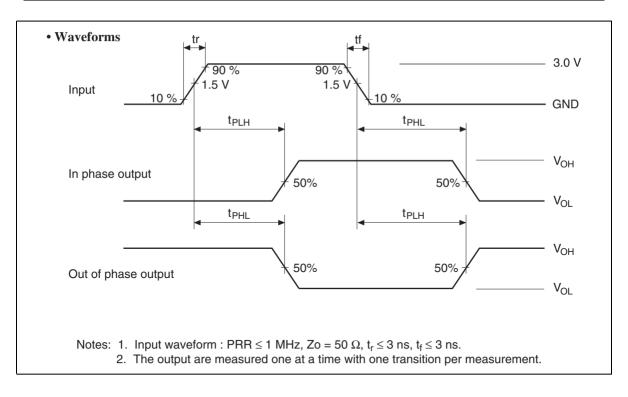
Operating Characteristics

• $C_1 = 50 \text{ pF}$

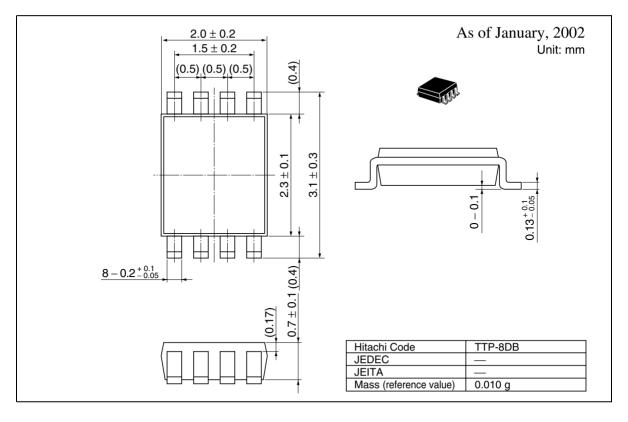
Item	Symbol	V _{cc} (V)	Ta = 25°C			Unit	Test Conditions
			Min	Тур	Max		
Power dissipation capacitance	C _{PD}	5.0	_	35.0	_	pF	f = 10 MHz

Test Circuit





Package Dimensions



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