

FMI13N60ES

FUJI POWER MOSFET

Super FAP-E^{3S} series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (4.2±0.5V) High avalanche durability

Applications

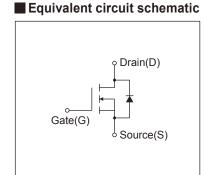
Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

		3 · L 1
T-Pack(L)	10*8.5	_,,4.5±0.2_
	0.940.3	1.3±0.2 Fig. 1. (See Notes)
Trademark	F O 22	Fig. 1.
Type name Lot No.		- FIG. 1.
1.2±0.2	,6±0.2	Solder Plating to the
	3.6±0.2	Solder Ploting
2,54±0.:	0.8*0.2	0.4*0.7
1,0120.		CONNECTION
1 [+++	① GATE ② DRAIN
	000	③ SOURCE

■ Outline Drawings [mm]



Description	Symbol	Characteristics	Unit	Remarks
Brain Saurea Valtara	V _{DS}	600	V	
Drain-Source Voltage	V _{DSX}	600	V	V _{GS} = -30V
Continuous Drain Current	Io	±13	А	
Pulsed Drain Current	IDP	±52	А	
Gate-Source Voltage	V _{GS}	±30	V	
Repetitive and Non-Repetitive Maximum AvalancheCurrent	IAR	13	А	Note*1
Non-Repetitive Maximum Avalanche Energy	Eas	471.5	mJ	Note*2
Repetitive Maximum Avalanche Energy	Ear	22.5	mJ	Note*3
Peak Diode Recovery dV/dt	dV/dt	4.7	kV/μs	Note*4
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5
Maniana Barra Biasinatian	Po	1.67	10/	Ta=25°C
Maximum Power Dissipation		225	W	Tc=25°C
O	Tch	150	°C	
Operating and Storage Temperature range	T _{stq}	-55 to + 150	°C	

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions	Conditions		typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I _D =250μA, V _{GS} =0V		600	-	-	V	
Gate Threshold Voltage	V _{GS} (th)	I _D =250μA, V _{DS} =V _{GS}		3.7	4.2	4.7	V	
Zero Gate Voltage Drain Current		V _{DS} =600V, V _{GS} =0V	T _{ch} =25°C	-	-	25		
	IDSS	V _{DS} =480V, V _{GS} =0V	T _{ch} =125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA	
Drain-Source On-State Resistance	R _{DS} (on)	In=6.5A, Vgs=10V	I _D =6.5A, V _{GS} =10V		0.50	0.58	Ω	
Forward Transconductance	g fs	I _D =6.5A, V _{DS} =25V		5	10	-	S	
Input Capacitance	Ciss	V _{DS} =25V		-	1700	2550	pF	
Output Capacitance	Coss	V _{GS} =0V	V _{GS} =0V		190	285		
Reverse Transfer Capacitance	Crss	f=1MHz		-	10	15		
Turn-On Time	td(on)	V _{cc} =300V V _{SS} =10V I _D =6.5A R _G =18Ω		-	38	57	ns	
	tr			-	24	36		
Turn-Off Time	td(off)			-	86	129		
	tf			-	16	24		
Total Gate Charge	Q _G	1/ 0001/			48	72	nC	
Gate-Source Charge	QGS	V _{cc} =300V		-	16	24		
Gate-Drain Charge	Q _{GD}	U _{GS} =10V	- I _D =13A		16	24		
Gate-Drain Crossover Charge	Qsw	VGS=10V		-	7	10.5		
Avalanche Capability	lav	L=2.36mH, Tch=25°C	L=2.36mH, Tch=25°C		-	-	А	
Diode Forward On-Voltage	V _{SD}	I _F =13A, V _{GS} =0V, T _{ch} =25°	I _F =13A, V _{GS} =0V, T _{ch} =25°C		0.90	1.08	V	
Reverse Recovery Time	trr	I _F =13A, V _{GS} =0V	I _F =13A, V _{GS} =0V		0.7	-	μS	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	8	-	μC	

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.560	°C/W
	Rth (ch-a)	Channel to ambient			75.0	°C/W

Note *1 : Tch≤150°C

Note 12: Stating Tch=25°C, Ias=6A, L=24.0mH, Vcc=60V, R_G=50Ω

Eas limited by maximum channel temperature and avalanche current.

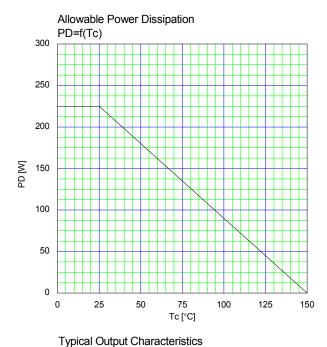
See to 'Avalanche Energy' graph.

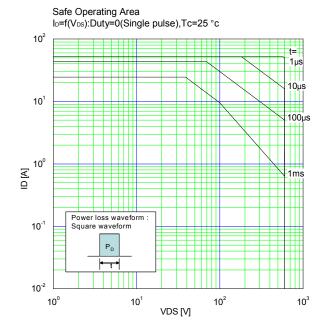
Note $^{\star}3$: Repetitive rating : Pulse width limited by maximum channel temperature

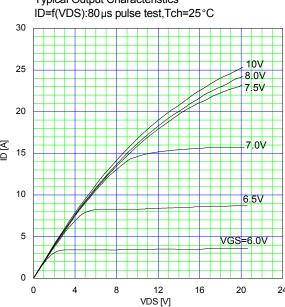
See to the 'Transient Themal impeadance' graph.

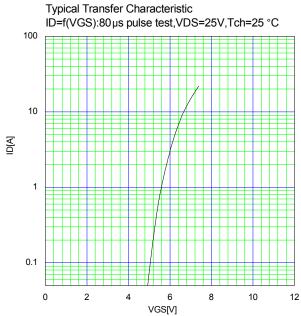
Note *4 : I₅≤-I₀, -di/dt=100A/μ₅, Vcc≤BV₀ss, Tch≤150°C.

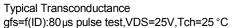
Note *5 : I₅≤-I₀, dv/dt=4.7kV/μ₅, Vcc≤BV₀ss, Tch≤150°C.

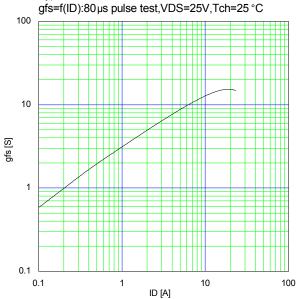


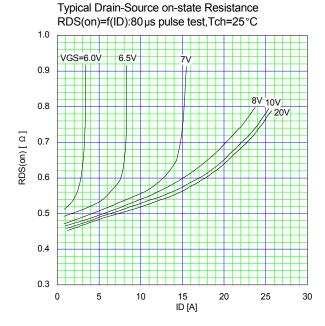




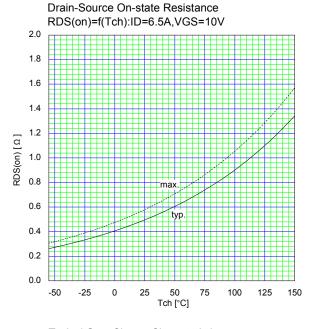


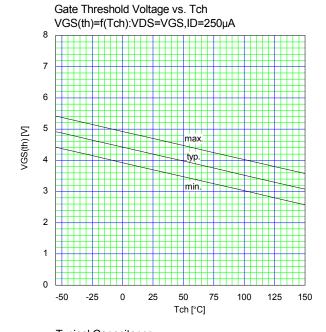


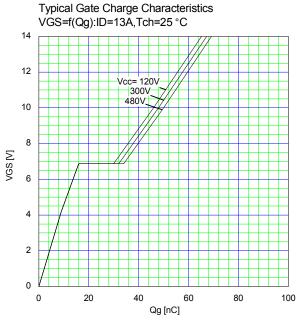


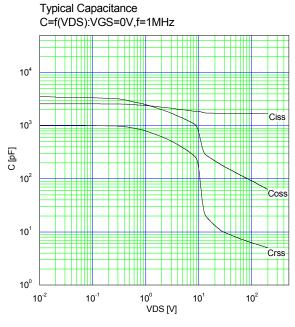


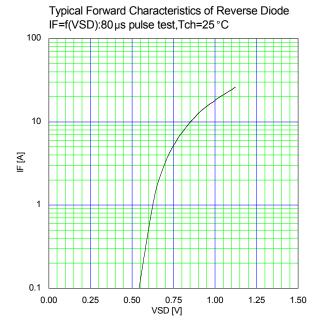
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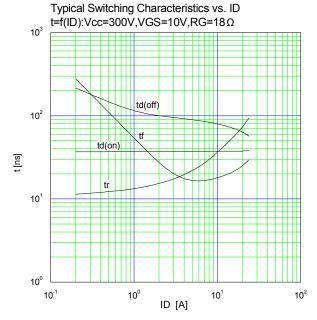


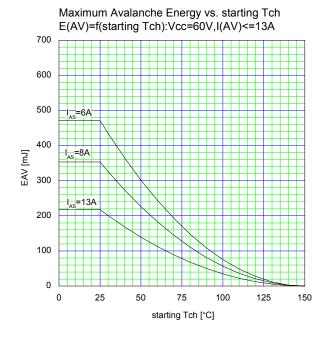


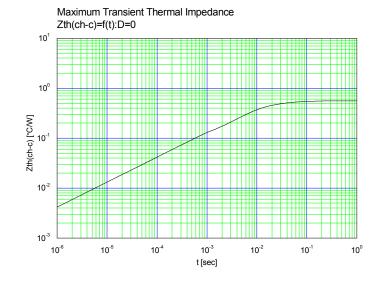












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