Not for new design, this product will be obsoleted soon S505TX/S505TXR/S505TXRW



Vishay Semiconductors

# **MOSMIC<sup>®</sup>** for TV-Tuner Prestage with 5 V Supply Voltage

e3

### Comments

MOSMIC - MOS Monolithic Integrated Circuit

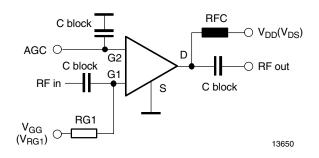
#### Features

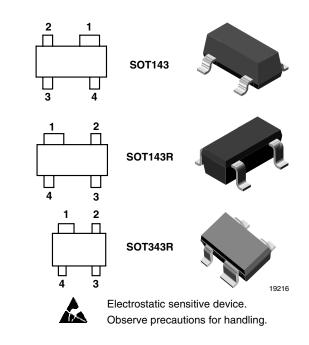
- Easy Gate 1 switch-off with PNP switching transistors inside PLL
- High AGC-range with less steep slope
- · Integrated gate protection diodes
- Low noise figure
- High gain, high forward transadmittance (30 mS typ.)
- · Improved cross modulation at gain reduction
- SMD package
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

### Applications

Low noise gain controlled input stages in UHF-and VHF- tuner with 5 V supply voltage.

### **Typical Application**





### **Mechanical Data**

Typ: S505TX Case: SOT-143 Plastic case Weight: approx. 8.0 mg Pinning: 1 = Source, 2 = Drain, 3 = Gate 2, 4 = Gate 1 Typ: S505TXR Case: SOT-143R Plastic case Weight: approx. 8.0 mg Pinning: 1 = Source, 2 = Drain, 3 = Gate 2, 4 = Gate 1 Typ: S505TXRW Case: SOT-343R Plastic case Weight: approx. 6.0 mg Pinning: 1 = Source, 2 = Drain, 3 = Gate 2, 4 = Gate 1

#### Parts Table

Part	Marking	Package
S505TX	X05	SOT-143
S505TXR	X7R	SOT-143R
S505TXRW	WX7	SOT-343R

### **Vishay Semiconductors**



### Absolute Maximum Ratings

 $T_{amb} = 25 \ ^{\circ}C$ , unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Drain - source voltage		V <sub>DS</sub>	8	V
Drain current		۱ <sub>D</sub>	30	mA
Gate 1/Gate 2 - source peak current		± I <sub>G1/G2SM</sub>	10	mA
Gate 1 - source voltage		+ V <sub>G1S</sub>	6	V
		- V <sub>G1S</sub>	1.5	V
Gate 2 - source voltage		± V <sub>G2SM</sub>	6	V
Total power dissipation	$T_{amb} \le 60 \ ^{\circ}C$	P <sub>tot</sub>	200	mW
Channel temperature		T <sub>Ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C

#### **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit
Channel ambient	t 1)		450	K/W

 $^{1)}$  on glass fibre printed board (25 x 20 x 1.5)  $\text{mm}^3$  plated with 35  $\mu\text{m}$  Cu

## **Electrical DC Characteristics**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Drain - source breakdown voltage	$I_D = 10 \ \mu A, \ V_{G1S} = V_{G2S} = 0$	V <sub>(BR)DSS</sub>	12			V
Gate 1 - source breakdown voltage	$\pm I_{G1S} = 10 \text{ mA}, V_{G2S} = V_{DS} = 0$	$\pm V_{(BR)G1SS}$	7		10	V
Gate 2 - source breakdown voltage	$\pm I_{G2S} = 10 \text{ mA}, V_{G1S} = V_{DS} = 0$	$\pm V_{(BR)G2SS}$	7		10	V
Gate 1 - source leakage current	$+ V_{G1S} = 5 V, V_{G2S} = V_{DS} = 0$	+ I <sub>G1SS</sub>			20	nA
Gate 2 - source leakage current	$\pm V_{G2S} = 5 V, V_{G1S} = V_{DS} = 0$	± I <sub>G2SS</sub>			20	nA
Drain - source operating current	$V_{DS} = V_{RG1} = 5 V$ , $V_{G2S} = 4 V$ , $R_{G1} = 56 k\Omega$	I <sub>DSO</sub>	8	14	20	mA
Gate 1 - source cut-off voltage	$V_{DS} = 5 \text{ V}, V_{G2S} = 4, I_D = 20 \ \mu\text{A}$	V <sub>G1S(OFF)</sub>	0.5		1.3	V
Gate 2 - source cut-off voltage	$V_{DS} = V_{RG1} = 5 \text{ V}, \text{ R}_{G1} = 56 \text{ k}\Omega,$ $I_D = 20 \mu\text{A}$	V <sub>G2S(OFF)</sub>	0.8	1.0	1.4	V

Remark on improving intermodulation behavior:

By setting  $R_{G1}$  smaller than 56 k $\Omega$ , typical value of  $I_{DSO}$  will raise and improved intermodulation behavior will be performed.

# **Electrical AC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified  $V_{DS}$  =  $V_{RG1}$  = 5 V,  $V_{G2S}$  = 4 V,  $R_{G1}$  = 56 kΩ,  $I_D$  =  $I_{DSO,}$  f = 1 MHz

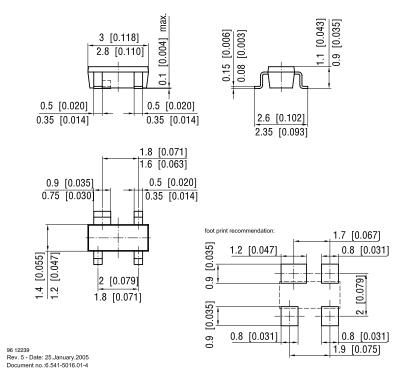
Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Forward transadmittance		y <sub>21s</sub>	27	30	35	mS
Gate 1 input capacitance		C <sub>issg1</sub>		1.8	2.2	pF
Feedback capacitance		C <sub>rss</sub>		20	30	fF
Output capacitance		C <sub>oss</sub>		1.0		pF



# **Vishay Semiconductors**

Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
	$G_{S} = 2 \text{ mS}, G_{L} = 0.5 \text{ mS},$ f = 200 MHz	G <sub>ps</sub>		28		dB
	G <sub>S</sub> = 3,3 mS, G <sub>L</sub> = 1 mS, f = 800 MHz	G <sub>ps</sub>	17	22		dB
AGC range	$V_{DS} = 5 V$ , $V_{G2S} = 1 to 4 V$ , f = 800 MHz	$\Delta G_{ps}$	45	50		dB
Noise figure	$G_{S} = 2 \text{ mS}, G_{L} = 0.5 \text{ mS},$ f = 200 MHz	F		1		dB
	$G_{S} = 3.3 \text{ mS}, G_{L} = 1 \text{ mS},$ f = 800 MHz	F		1.3		dB
Cross modulation	Input level for k = 1 % @ 0 dB AGC $f_w$ = 50 MHz, $f_{unw}$ = 60 MHz	X <sub>mod</sub>	90			dBμV
	Input level for k = 1 % @ 40 dB AGC $f_w$ = 50 MHz, $f_{unw}$ = 60 MHz	X <sub>mod</sub>	100	105		dBμV

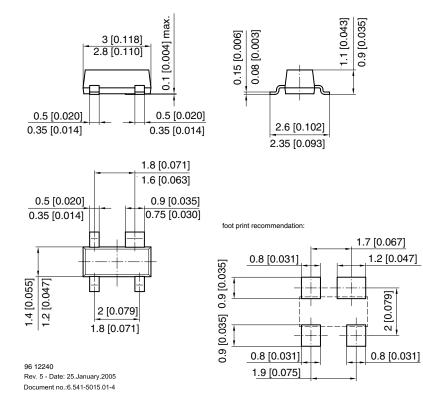
## Package Dimensions in mm



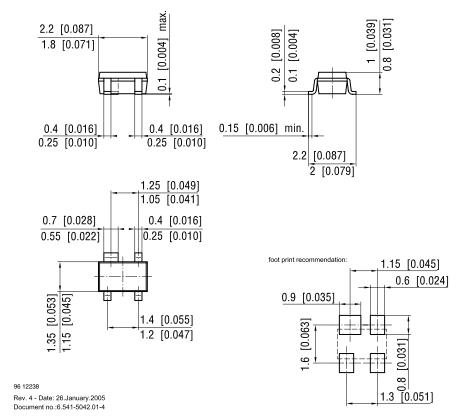
### **Vishay Semiconductors**



### Package Dimensions in mm



### Package Dimensions in mm





#### **Vishay Semiconductors**

# **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



Vishay

# Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.