

AK594096AS/AK594006AG 4,194,304 Word by 9 Bit CMOS Dynamic Random Access Memory

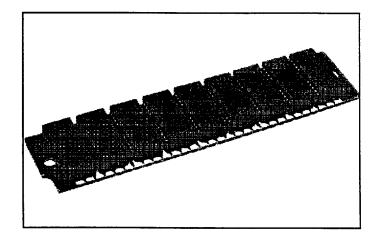
DESCRIPTION

The Accutek AK594096AS or AK594096AS high density memory module is a CMOS dynamic RAM organized in 4 Meg x 9 bit words. The assembly consists of nine 4 Meg x 1 DRAMs in plastic SOJ packages mounted to a printed wiring board in 30 pad SIM (leadless) er SIP (leaded) configuration. The module is only 0.8 inch high (same height as standard 1 Meg modules), making it ideally suited for applications with low height restrictions.

The operation of the AK594096A is identical to nine 4 Meg x 1 dynamic RAMs. For the lower eight bits, the data input is tied to the data output and brought out separately for each device, with common RAS and $\overline{\text{CAS}}$ control. This common I/O feature dictates the use of early-write cycles to prevent contention of D and Q. Since the Write-Enable ($\overline{\text{WE}}$) signal must always go low before $\overline{\text{CAS}}$ in a write cycle, Read-Write and Read-Modify-Write operation is not possible. For the ninth bit, the data input (D₉) and data output (Q₉) pins are brought out separately and controlled by a separate $\overline{\text{CAS}}$ 9 for that bit. Bit nine is generally used for parity.

FEATURES

- · 4,194,304 x 9 bit organization
- 30 pad (SIM) Single In-Line Module er (SIP) Single In-Line package
- · JEDEC approved pinout
- Common CAS and common RAS control for eight common DQ lines
- Separate CAS control for one separate pair of D and Q lines



- CAS-before-RAS refresh
- Power
 4.45 Watt Max Active (80 nS)
 3.96 Watt Max Active (100 nS)
 49.5 mWatt Max Standby
- Operating free air temperature 0°C to 70°C
- Downward compatible with AK491024 and AK59256/49256
- Available in leadless (S) er-leaded version (C)
- Fast Page Mode, Nibble Mode and Static Column Mode versions available
- 8 bit version (no parity bit) also available

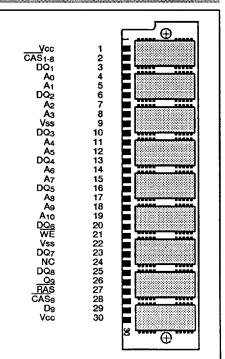
PIN NOMENCLATURE

DQ ₁ - DQ ₈	Data In/Data Out
D ₉	Data In Parity Bit
Q ₉	Data Out Parity Bit
A ₀ - A ₁₀	Address Inputs
CAS ₁₋₈	Column Address Strobe
RAS	Row Address Strobe
WE	Write Enable
Vcc	5v Supply
Vss	Ground
NC	No Connection

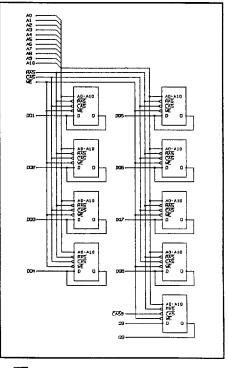
MODULE OPTIONS

Leadless SIM: AK594096AS Single In-Line Module	
Leaded SIP: AK594896AG Single In-Line Paskage	

PIN ASSIGNMENT



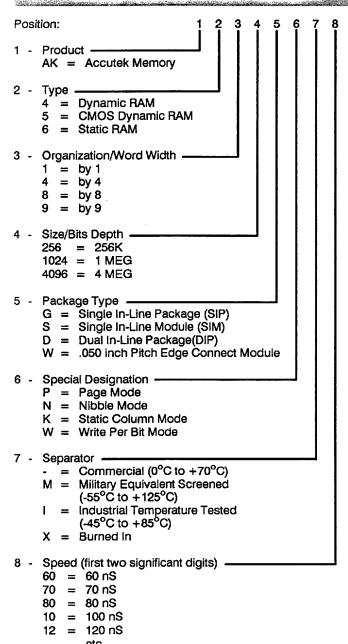
FUNCTIONAL DIAGRAM



■ 0107647 0000041 377 ■

ORDERING INFORMATION

PART NUMBER CODING INTERPRETATION



The numbers and coding on this page do not include all variations available, but are shown as examples of the most widely used variations. Contact Accutek if other information is required.

EXAMPLES:

AK594096ASP-80

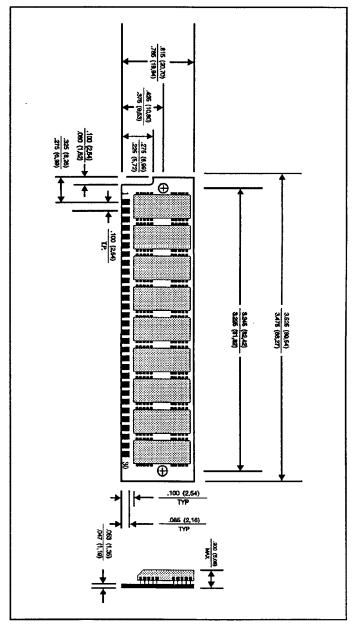
4 Meg x 9 CMOS Dynamic RAM, Leadless SIM, Page Mode, Commercial, 80 nS Access Time

AKE04006ACK-70

4-Meg x 9 OMOS Bynamic RAM, Loaded SIP, Static Solumin Mode, Commercial, 70 nS Assess Time

MECHANICAL DIMENSIONS

Inches (millimeters)



PHONE:

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