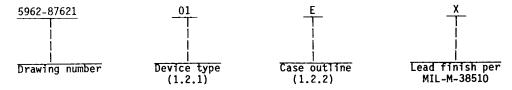
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<u>DISTRIBUTION STATEMENT A.</u> Approved for public release; distribution is unlimited. **DESC FORM 193 MAY 86**



1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of $\overline{\text{MIL}}$ -STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:



1.2.2 <u>Case outlines</u>. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-lead, $1/4$ " x $7/8$ "), dual-in-line package
F	F-5 (16-lead, 1/4" x 3/8"), flat package
2	C-2 (20 terminal, .350" x .350"), square chip

1.3 Absolute maximum ratings.

1.4 Recommended operating conditions.

```
Supply voltage (V_{CC}) - - - - - - - - - - - - - - - - - 4.5 V dc to 5.5 V dc Minimum high-level input voltage (V_{IH}) - - - - - - - 0.7 V Maximum low-level input voltage (V_{IL}) - - - - - - - 0.7 V Maximum low-level output current, RCO (I_{OL}) - - - - - - - - 0.0 MA Counter clock frequency (f_{CCK}) - - - - - - - - - 0.0 MHz to 20.0 MHz Minimum counter clock pulse width (t_{WCCK}) - - 25 ns Minimum counter clear pulse width (t_{WCCK}) - 20 ns Minimum register clock pulse width (t_{WCCK}) - 20 ns Minimum counter load pulse width (t_{WCC}) - 20 ns 40 ns
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 $\overline{1/}$ Must withstand the added P_D due to short circuit test (e.g., I_{OS}).

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Minimum setup time (t_{sy}):

CCKEN low before CCK+ - - - - - - - - - - 30 ns

CCLR inactive before CCK+ - - - - - - - 20 ns

CLOAD inactive before CCK+ - - - - - - 20 ns

RCK+ before CLOAD 2/ - - - - - - 30 ns

Data A thru H before RCK+ - - - - - 20 ns

Minimum hold time (t_h) - - - - - - - - 0 ns

Case operating temperature range (T_C) - - - - - - - - - - - - 55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510

Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883

Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Logic diagram. The logic diagram shall be as specified on figure 2.
 - 3.2.3 Operating sequence. The operating sequence shall be as specified on figure 3.
 - 3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended case operating temperature range.

2/ This setup time ensures the data saved by RCK * will also be loaded into the counter.

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- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).
- 3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.
 - 4. QUALITY ASSURANCE PROVISIONS
- 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria snall apply:
 - a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_{\Delta} = +125^{\circ}C$, minimum.
 - b. Interim and final electrical test parameters snall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.
- 4.3 Quality conformance inspection. Quality conformance inspection snall be in accordance with method 5005 of MIL-SID-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - c. Subgroup 7 tests shall verify the operating sequence.

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Test	 Symbol	Conditions $\frac{1}{2}$	Group A	İ	Limits 	
		Conditions $\frac{1}{2}$ $-55^{\circ}C < T_{C} < +125^{\circ}C$ unless otherwise specified	subgroups	Min	Max	<u>†</u>
High-level output voltage	V _{ОН}	$V_{CC} = 4.5 \text{ V}$ $V_{IH} = 2.0 \text{ V}$ $I_{OH} = -1.0 \text{ mA}$ $V_{IL} = 0.7 \text{ V}$	1, 2, 3	2.4		v
Low-level output voltage	V _{OL}	$V_{CC} = 4.5 \text{ V}$ $V_{IH} = 2.0 \text{ V}$ $I_{OL} = 8.0 \text{ mA}$ $V_{IL} = 0.7 \text{ V}$	1, 2, 3		0.4	V
Input clamp voltage	VIC	V _{CC} = 4.5 V I _{IN} = -18 mA	1, 2, 3		 -1.5 	 V
High-level input current	IIIH	V _{CC} = 5.5 V V _{IH} = 2.7 V	1, 2, 3		 20 	 μ A
Low-level input current	IIL	V _{CC} = 5.5 V	1, 2, 3	 	-0.4 -0.2 -0.8	mA
Maximum input current	II	V _{CC} = 5.5 V V _I = 7.0 V	1, 2, 3		0.1	mA
Short-circuit output current	Ios	V _{CC} = 5.5 V V _{OUT} = 0.0 V <u>1</u> /	1, 2, 3	-20	-100 	mA
Supply current	ICCH	 V _{CC} = 5.5 V All possible inputs grounded	1, 2, 3		60	l mA
	ICCL	All outputs open	1, 2, 3		60	mA
Maximum clock frequency	f _{MAX}	$V_{CC} = 5.0 \text{ V}$ $R_L = 1 \text{k}\Omega \pm 5\%$ $C_1 = 30 \text{ pF} \pm 10\%$	9 10, 11	20		MHz

See footnote at end of table.

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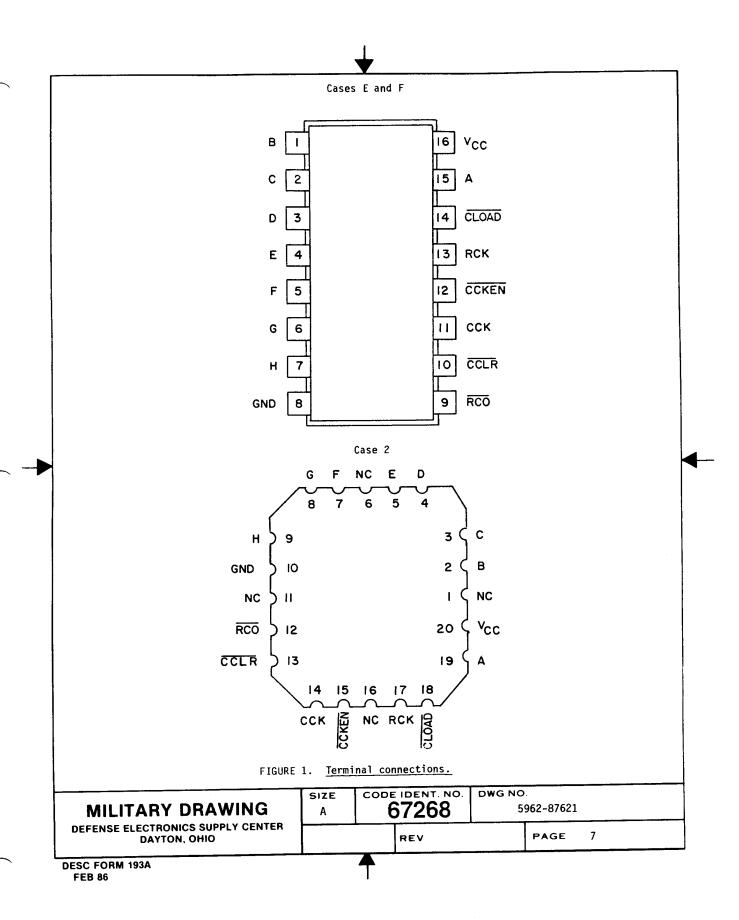
TABLE	I. <u>Electric</u>	al performance characteristics - C	Continued.		
Test	Symbol	Conditions 1/ -55°C < T _C < +125°C		Limits Min Max	 Unit
		-55 C \(\frac{7}{2}\) \(\frac{125}{125}\) C	Tabby oups		<u> </u>
Propagation delay time,	t _{PLH1}	V _{CC} = 5.0 V R _L = 1 kΩ ±5% C _L = 30 pF ±10%	9 10, 11	i i 23	ns
OKT TO NOT	tpHL1	CL = 30 pF ±10%	10, 11	30	
Propagation delay time,	t _{PLH2}		9	47 65.8	l I I ns
CLOĂD♦ to RCO	tpHL2		10, 11 9		j
Propagation delay time,	t _{PLH3}		9	45	
CCLR to RCO	i FEIIS		10, 11	63	ns
Propagation delay time,	t _{PLH4}	T	9 10,11	53	ns
RCK+ to RCO	t _{PHL4}		10, 11	45	j

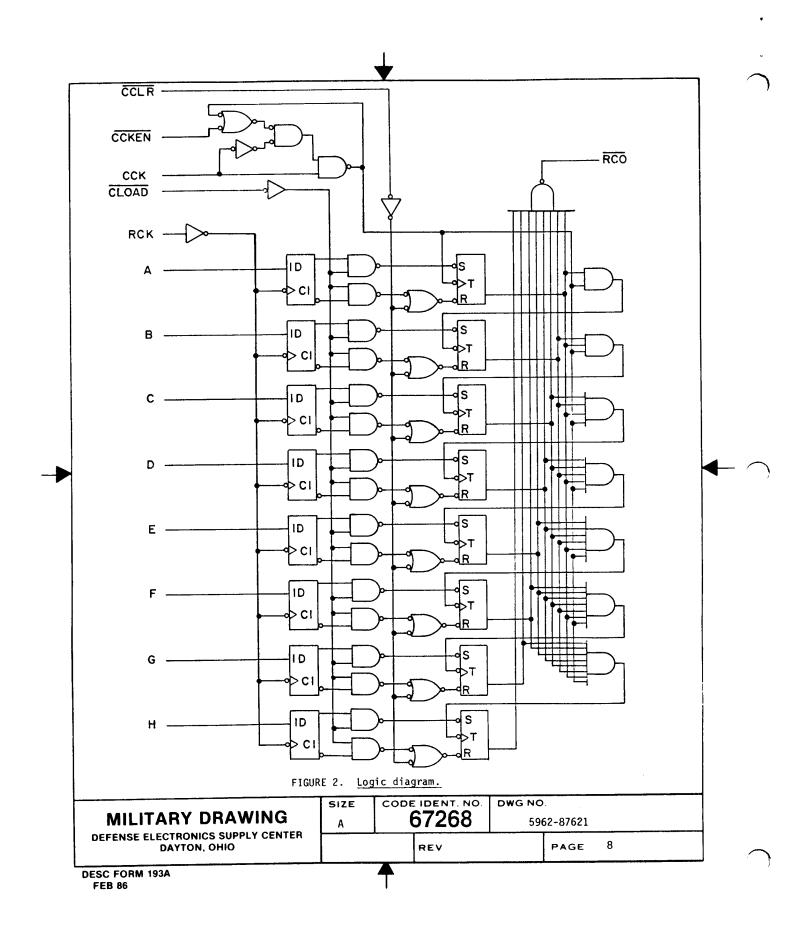
^{1/} Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition A, B, C or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125$ °C, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

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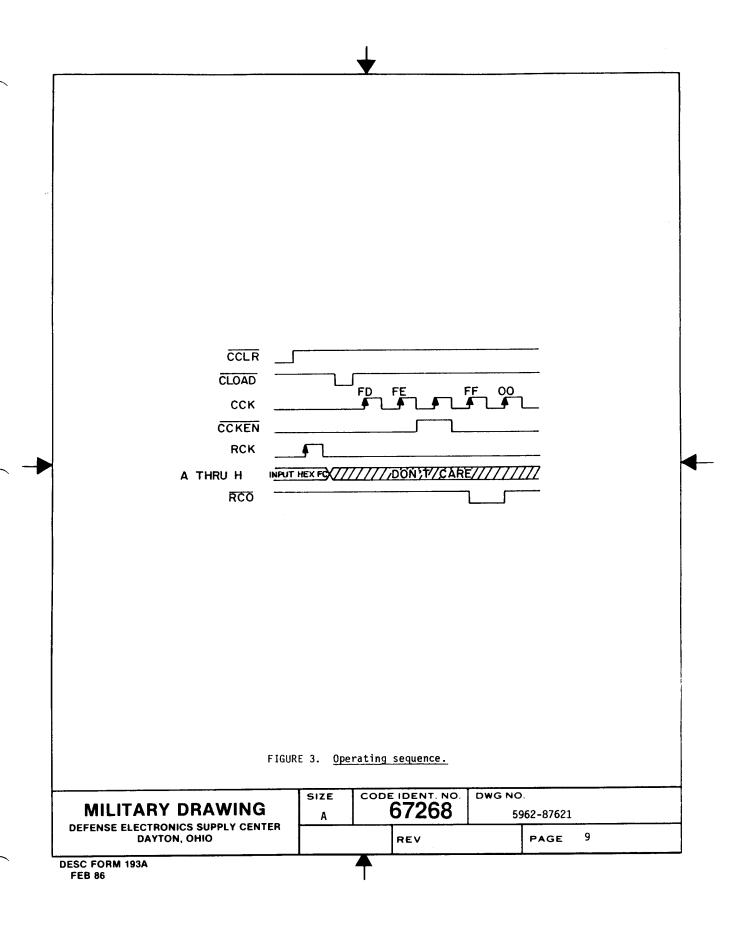


TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	1
Final electrical test parameters (method 5004)	1*, 2, 3, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 9, 10, 11**
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

*PDA applies to subgroup 1.
**Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

- 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.
- NOTES
- 6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
- 6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

 Military drawing part number 	Vendor CAGE number	Vendor similar part number <u>1</u> /
 5962-8762101EX 	01295	SNJ54LS592J
 5962-8762101FX 	01295	SNJ54LS592W
 5962-87621012X 	01295	SNJ54LS592FK

Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

01295

Vendor name and address

Texas Instruments, Incorporated P.O. Box 6448 Midland, TX 79701

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