TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2$ - $\pi$ -MOSV)

## 2SK2844

# Chopper Regulator, DC-DC Converter and Motor Drive Applications

4-V gate drive

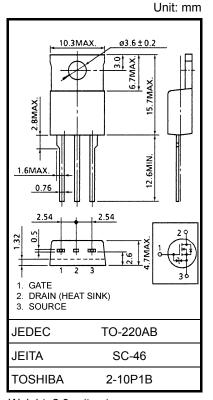
• Low drain-source ON resistance :  $R_{DS (ON)} = 16 \text{ m}\Omega \text{ (typ.)}$ 

High forward transfer admittance : |Y<sub>fs</sub>| = 26 S (typ.)
 Low leakage current : I<sub>DSS</sub> = 100 µA (max) (V<sub>DS</sub> = 30 V)

Enhancement mode : V<sub>th</sub> = 0.8 to 2.0 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	30	V
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	30	V
Gate-source voltage		$V_{GSS}$	±20	V
Drain current	DC (Note 1)	I <sub>D</sub>	35	Α
	Pulse (Note 1)	I <sub>DP</sub>	140	Α
Drain power dissipatio	n (Tc = 25°C)	$P_{D}$	60	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	259	mJ
Avalanche current		I <sub>AR</sub>	35	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	6	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 2.0 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.08	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 152  $\mu$ H,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 35 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.

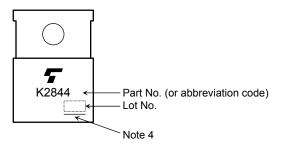
#### **Electrical Characteristics (Ta = 25°C)**

Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cu	rrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V		_	±10	μΑ	
Drain cut-off cur	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	_	_	100	μΑ	
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	30	_	_	V	
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V	
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 18 A	_	26	35	mO	
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 18 A		16	20	mΩ	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 18 A	13	26	_	S	
Input capacitano	е	C <sub>iss</sub>		_	980	_		
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	270	_	pF	
Output capacitance		Coss		_	580	_		
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ $0V$ $R_{L}=0.8\Omega$ $V_{DD}=14.4V$	_	14	_		
	Turn-on time	t <sub>on</sub>		_	23	_	- ns	
	Fall time	t <sub>f</sub>		_	64	_		
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , $t_{\mathbf{W}} = 10 \mu s$	_	190	_		
Total gate charge (gate-source plus gate-drain)		Qg		_	40	_		
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 24 \text{ V, V}_{GS} = 10 \text{ V, I}_{D} = 35 \text{ A}$		32		nC -	
Gate-drain ("miller") Charge		Q <sub>gd</sub>			8	_		

## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	50	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	200	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 35 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 35 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 50 A / μs	_	120	_	ns
Reverse recovery charge	Q <sub>rr</sub>	1DR - 33 A, vGS - 0 v, αιDR / αι - 30 A / μs	_	180	_	nC

#### Marking

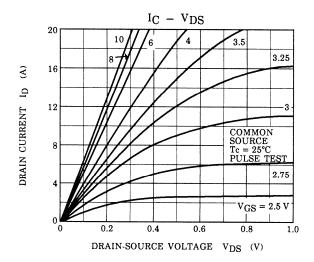


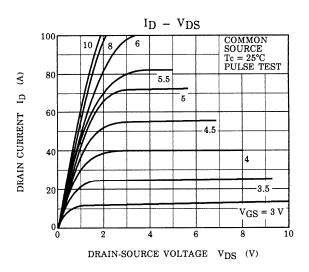
Note 4: A line under a Lot No. identifies the indication of product Labels.

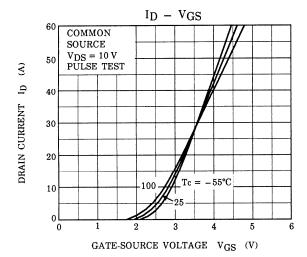
Not underlined: [[Pb]]/INCLUDES > MCV

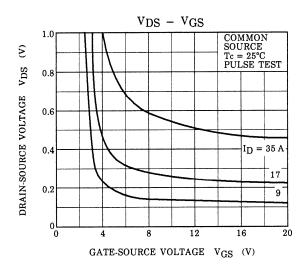
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

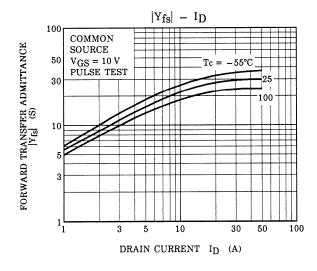
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

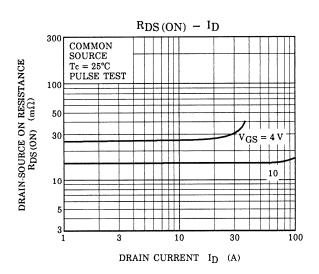


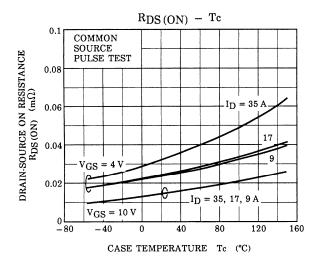


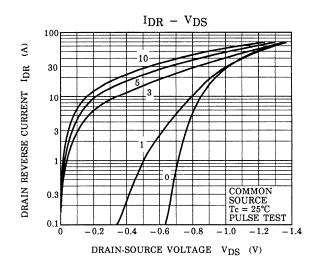


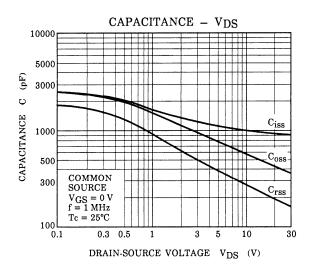


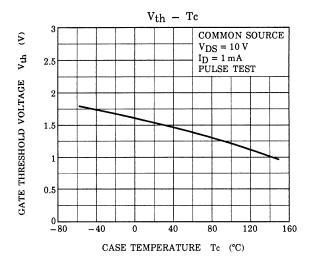


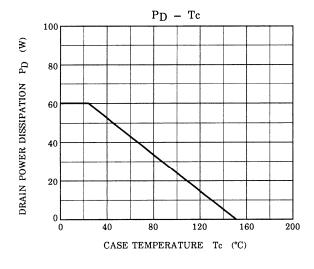


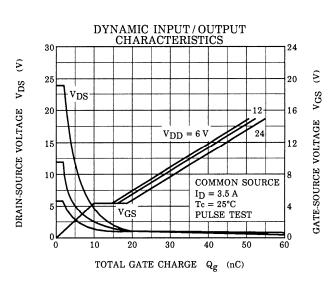




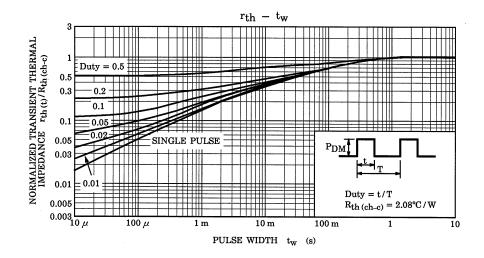


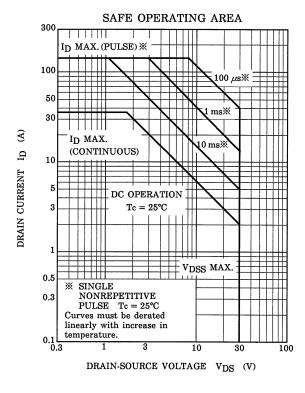


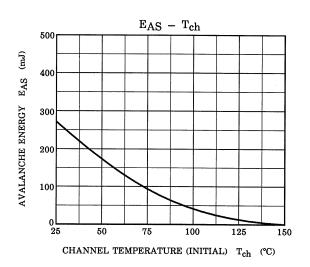


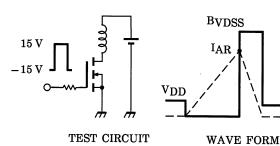


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$$R_G$$
 = 25  $\Omega$   
 $V_{DD}$  = 25 V, L = 152  $\mu$ H

$$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS - VDD} \right)$$

 $v_{DS}$ 

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