# 2:1 Multiplexer

The NLASB3157 is an advanced CMOS analog switch fabricated with silicon gate CMOS technology. It achieves very low propagation delay and RDS $_{
m ON}$  resistances while maintaining CMOS low power dissipation. Analog and digital voltages that may vary across the full power–supply range (from  $V_{
m CC}$  to GND). This device is a drop in replacement for the NC7SB3157.

The select pin has overvoltage protection that allows voltages above  $V_{CC}$ , up to 7.0 V to be present on the pin without damage or disruption of operation of the part, regardless of the operating voltage.

#### **Features**

- High Speed:  $t_{PD} = 1.0 \text{ ns}$  (Typ) at  $V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation:  $I_{CC} = 2.0 \mu A$  (Max) at  $T_A = 25^{\circ}C$
- Standard CMOS Logic Levels
- High Bandwidth, Improved Linearity
- Switches Standard NTSC/PAL Video, Audio, SPDIF and HDTV
- May be used for Clock Switching, Data Mux'ing, etc.
- Low RDSON
- Break Before Make Circuitry, Prevents Inadvertent Shorts
- Can Switch Balanced Signal Pairs, e.g. LVDS > 200-Bits/Sec
- Latchup Performance Exceeds 300 mA
- Pin for Pin Drop in for NC7SB3157
- Tiny SC88 Package Only 2.0 x 2.1 mm
- ESD Performance: HBM > 2000 V; MM > 200 V

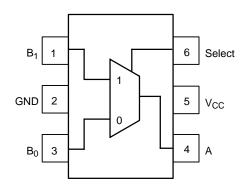


Figure 1. Pinout (Top View)

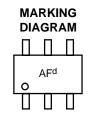
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AF = Specific Device Code d = Date Code

#### ORDERING INFORMATION

Device	Package	Shipping
NLASB3157DFT2	SC88/SOT-363/ SC-70	Tape & Reel

#### **FUNCTION TABLE**

Select Input	Function
L	B0 Connected to A
Н	B1 Connected to A

#### MAXIMUM RATINGS (Note 1)

Rating	Symbol	Value	Unit
Supply Voltage	V <sub>CC</sub>	-0.5 to +7.0	V
DC Switch Voltage (Note 2)	V <sub>S</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC Input Voltage (Note 2)	V <sub>IN</sub>	-0.5 to + 7.0	V
DC Input Diode Current @ V <sub>IN</sub> < 0 V	I <sub>IK</sub>	-50	mA
DC Output Current	I <sub>OUT</sub>	128	mA
DC V <sub>CC</sub> or Ground Current	I <sub>CC</sub> /I <sub>GND</sub>	+100	mA
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Junction Temperature Under Bias	T <sub>J</sub>	150	°C
Junction Lead Temperature (Soldering, 10 Seconds)	TL	260	°C
Power Dissipation @ +85°C	P <sub>D</sub>	180	mW

## **RECOMMENDED OPERATING CONDITIONS** (Note 3)

Characteristic	Symbol	Min	Max	Unit
Supply Voltage Operating	V <sub>CC</sub>	1.65	5.5	V
Select Input Voltage	V <sub>IN</sub>	0	V <sub>CC</sub>	V
Switch Input Voltage	V <sub>IN</sub>	0	V <sub>CC</sub>	V
Output Voltage	V <sub>OUT</sub>	0	V <sub>CC</sub>	V
Operating Temperature	T <sub>A</sub>	-40	+85	°C
Input Rise and Fall Time Control Input $V_{CC} = 2.3 \text{ V} - 3.6 \text{ V}$ Control Input $V_{CC} = 4.5 \text{ V} - 5.5 \text{ V}$	t <sub>r</sub> , t <sub>f</sub>	0	10 5	ns/V
Thermal Resistance	$\theta_{\sf JA}$	-	350	°C/W

<sup>1.</sup> Maximum ratings are DC values beyond which the device may be damaged or have its useful life impaired. The data sheet specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. Fairchild does not recommend operation outside data sheet specifications.

The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.
 Select input must be held HIGH or LOW, it must not float.

### DC ELECTRICAL CHARACTERISTICS

			V <sub>CC</sub>	-	T <sub>A</sub> = +25°(	$T_A = -40^{\circ}C$ to		C to +85°C	+85°C	
Symbol	Parameter	Test Conditions	(V)	Min	Тур	Max	Min	Max	Unit	
V <sub>IH</sub>	HIGH Level Input Voltage		1.65–1.95 2.3–5.5	0.75 V <sub>CC</sub> 0.7 V <sub>CC</sub>			0.75 V <sub>CC</sub> 0.7 V <sub>CC</sub>		٧	
V <sub>IL</sub>	LOW Level Input Voltage		1.65–1.95 2.3–5.5			0.25 V <sub>CC</sub> 0.3 V <sub>CC</sub>		0.25 V <sub>CC</sub> 0.3 V <sub>CC</sub>	V	
I <sub>IN</sub>	Input Leakage Current	$0 \le V_{IN} \le 5.5 \text{ V}$	0–5.5		±0.05	± 0.1		±1	μΑ	
I <sub>OFF</sub>	OFF State Leakage Current	$0 \le A, B \le V_{CC}$	1.65–5.5		±0.05	± 0.1		±1	μΑ	
R <sub>ON</sub>	Switch On Resistance (Note 4)	$V_{IN} = 0$ V, $I_O = 30$ mA $V_{IN} = 2.4$ V, $I_O = -30$ mA $V_{IN} = 4.5$ V, $I_O = -30$ mA	4.5		8 13 9	12 17 15		14 20 15	Ω	
		$V_{IN} = 0 \text{ V, } I_{O} = 24 \text{ mA}$ $V_{IN} = 3 \text{ V, } I_{O} = -24 \text{ mA}$	3.0		10 12	14 18		18 18	Ω	
		$V_{IN} = 0 \text{ V, } I_O = 8 \text{ mA}$ $V_{IN} = 2.3 \text{ V, } I_O = -8 \text{ mA}$	2.3		13 15	18 25		22 25	Ω	
		V <sub>IN</sub> = 0 V, I <sub>O</sub> = 4 mA V <sub>IN</sub> = 1.65 V, I <sub>O</sub> = -4 mA	1.65		20 20	24 40		30 40	Ω	
I <sub>CC</sub>	Quiescent Supply Current All Channels ON or OFF	$V_{IN} = V_{CC}$ or GND $I_{OUT} = 0$	5.5			1		10	μА	
	Analog Signal Range		V <sub>CC</sub>	0		V <sub>CC</sub>	0	V <sub>CC</sub>	V	
R <sub>RANGE</sub>	On Resistance Over Signal Range (Note 4) (Note 8)	$I_A = -30 \text{ mA}, 0 \le V_{Bn}$ $\le V_{CC}$ $I_A = -24 \text{ mA}, 0 \le V_{Bn}$	4.5 3.0					25 50	Ω	
		$\leq$ V <sub>CC</sub> $I_A = -8$ mA, $0 \leq$ V <sub>Bn</sub> $\leq$ V <sub>CC</sub>	2.3					100		
		$I_A = -4 \text{ mA}, 0 \le V_{Bn}$ $\le V_{CC}$	1.65					300		
$\Delta R_{ON}$	On Resistance Match Between Channels (Note 4) (Note 5) (Note 6)	$\begin{split} I_A &= -30 \text{ mA, V}_{Bn} = 3.15 \\ I_A &= -24 \text{ mA, V}_{Bn} = 2.1 \\ I_A &= -8 \text{ mA, V}_{Bn} = 1.6 \\ I_A &= -4 \text{ mA, V}_{Bn} = 1.15 \end{split}$	4.5 3.0 2.3 1.65		0.15 0.2 0.5 0.5				Ω	
R <sub>flat</sub>	On Resistance Flatness (Note 4)	$I_A = -30 \text{ mA}, 0 \le V_{Bn}$ $\le V_{CC}$	5.0		6				Ω	
	(Note 5) (Note 7)	$I_A = -24 \text{ mA}, 0 \le V_{Bn}$ $\le V_{CC}$	3.3		12					
		$\begin{split} I_A &= -8 \text{ mA}, \ 0 \le V_{Bn} \\ &\le V_{CC} \\ I_A &= -4 \text{ mA}, \ 0 \le V_{Bn} \\ &\le V_{CC} \end{split}$	2.5 1.8		28 125					

Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltages on the two (A or B Ports).
 Parameter is characterized but not tested in production.

ΔR<sub>ON</sub> = R<sub>ON</sub> max – R<sub>ON</sub> min measured at identical V<sub>CC</sub>, temperature and voltage levels.
 Flatness is defined as the difference between the maximum and minimum value of On Resistance over the specified range of conditions.

<sup>8.</sup> Guaranteed by Design.

#### **AC ELECTRICAL CHARACTERISTICS**

			V <sub>CC</sub>	T <sub>A</sub> = +25°C		$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			Figure	
Symbol	Parameter	Test Conditions	(V)	Min	Тур	Max	Min	Max	Unit	Number
t <sub>PHL</sub> t <sub>PLH</sub>	Propagation Delay Bus to Bus (Note 10)	V <sub>I</sub> = OPEN	1.65–1.95 2.3–2.7 3.0–3.6 4.5–5.5			1.2 0.8 0.3		1.2 0.8 0.3	ns	Figures 2, 3
<sup>†</sup> PZL <sup>†</sup> PZH	Output Enable Time Turn On Time (A to B <sub>n</sub> )	$V_I = 2 \times V_{CC}$ for $t_{PZL}$ $V_I = 0$ V for $t_{PZH}$	1.65–1.95 2.3–2.7 3.0–3.6 4.5–5.5	7 3.5 2.5 1.7		70 32 21 13	7 3.5 2.5 1.7	70 35 23 15	ns	Figures 2, 3
t <sub>PLZ</sub> t <sub>PHZ</sub>	Output Disable Time Turn Off Time (A Port to B Port)	$V_I = 2 \times V_{CC}$ for $t_{PLZ}$ $V_I = 0$ V for $t_{PHZ}$	1.65–1.95 2.3–2.7 3.0–3.6 4.5–5.5	3 2 1.5 0.8		13.7 8.1 6.7 6.1	3 2 1.5 0.8	13.8 8.2 7 6.2	ns	Figures 2, 3
t <sub>B-M</sub>	Break Before Make Time (Note 9)		1.65–1.95 2.3–2.7 3.0–3.6 4.5–5.5	0.5 0.5 0.5 0.5			0.5 0.5 0.5 0.5		ns	Figure 4
Q	Charge Injection (Note 9)	$C_L = 0.1 \text{ nF, V}_{GEN} = 0 \text{ V}$ $R_{GEN} = 0 \Omega$	5.0 3.3		7 3				рС	Figure 5
OIRR	Off Isolation (Note 11)	$R_L = 50 \Omega$ f = 10 MHz	1.65–5.5		-57				dB	Figure 6
Xtalk	Crosstalk	$R_L = 50 \Omega$ f = 10 MHz	1.65–5.5		-54				dB	Figure 7
BW	-3 dB Bandwidth	$R_L = 50 \Omega$	1.65–5.5		250				MHz	Figure 10
THD	Total Harmonic Distortion (Note 9)	$R_L = 600 \Omega$ 0.5 $V_{P-P}$ f = 600 Hz to 20 kHz	5		0.011				%	

## **CAPACITANCE** (Note 12)

Symbol	Parameter	Test Conditions	Тур	Max	Unit	Figure Number
C <sub>IN</sub>	Select Pin Input Capacitance	V <sub>CC</sub> = 0 V	2.3		pF	
C <sub>IO-B</sub>	B Port Off Capacitance	V <sub>CC</sub> = 5.0 V	6.5		pF	Figure 8
C <sub>IOA-ON</sub>	A Port Capacitance when Switch is Enabled	V <sub>CC</sub> = 5.0 V	18.5		pF	Figure 9

Guaranteed by Design.
 This parameter is guaranteed by design but not tested. The bus switch contributes no propagation delay other than the RC delay of the On Resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage source (zero output impedance).
 Off Isolation = 20 log<sub>10</sub> [V<sub>A</sub>/V<sub>Bn</sub>].
 T<sub>A</sub> = +25°C, f = 1 MHz, Capacitance is characterized but not tested in production.

## **AC Loading and Waveforms**

NOTE: Input driven by 50  $\Omega$  source terminated in 50  $\Omega$ 

NOTE:  $C_L$  includes load and stray capacitance NOTE: Input PRR = 1.0 MHz;  $t_W$  = 500 ns

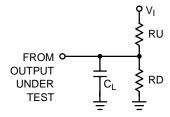
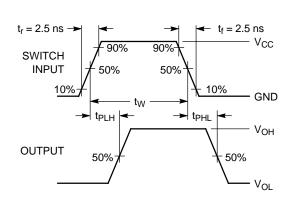


Figure 2. AC Test Circuit



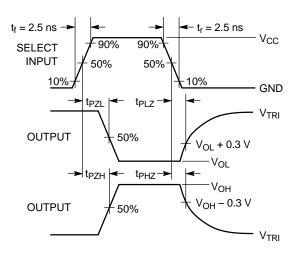
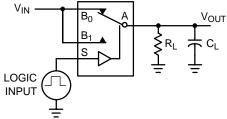


Figure 3. AC Waveforms



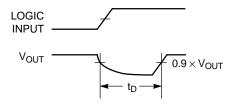


Figure 4. Break Before Make Interval Timing

## **AC Loading and Waveforms**

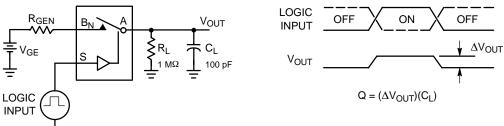


Figure 5. Charge Injection Test

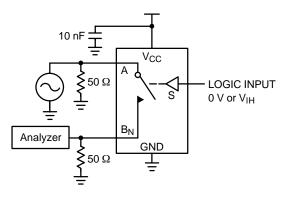


Figure 6. Off Isolation

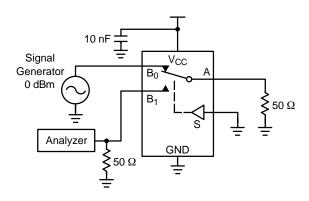


Figure 7. Crosstalk

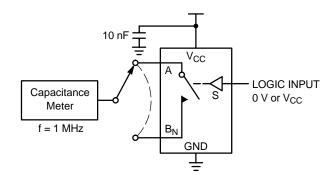


Figure 8. Channel Off Capacitance

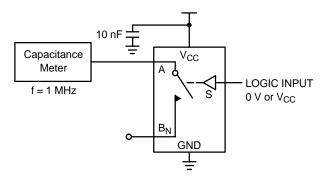


Figure 9. Channel On Capacitance

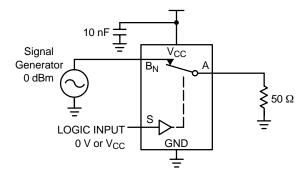
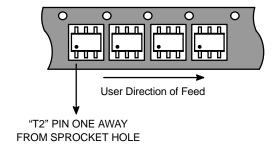


Figure 10. Bandwidth

# 32 DFT2 (SC88) Reel Configuration/Orientation

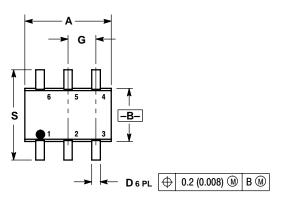


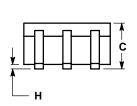
0.5 mm (min) 0.65 mm (9.65 mm

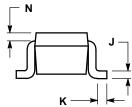
SC-88/SOT-363/SC70

#### PACKAGE DIMENSIONS

SC88/SOT-363/SC-70 **DF SUFFIX** CASE 419B-02 ISSUE K







#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
  3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	INC	HES	MILLIN	IETERS				
DIM	MIN	MAX	MIN	MAX				
Α	0.071	0.087	1.80	2.20				
В	0.045	0.053	1.15	1.35				
C	0.031	0.043	0.80	1.10				
D	0.004	0.012	0.10	0.30				
G	0.026	BSC	0.65 BSC					
H		0.004		0.10				
_	0.004	0.010	0.10	0.25				
K	0.004	0.012	0.10	0.30				
N	0.008 REF		0.20 REF					
٥	0.070	0.087	2 00	2 20				

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