2975 to 3045 MHz 50Ω

The Big Deal

- Fractional N synthesizer
- Low phase noise and spurious
- · Robust design and construction
- Small size 0.80" x 0.58" x 0.24"



CASE STYLE: DK1182

Product Overview

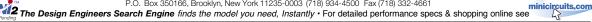
The KSN-3075A-119+ is a Frequency Synthesizer, designed to operate from 2975 to 3045 MHz for WiMAX 3.5GHz application. The KSN-3075A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.24") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -96 dBc/Hz typ. @ 10 kHz offset • Step Size Spurious: -98 dBc typ. • Comparison Spurious: -92 dBc typ. • Reference Spurious: -92 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-3075A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.24"	The small size enables the KSN-3075A-119+ to be used in compact designs.







Frequency Synthesizer

KSN-3075A-119+

 50Ω 2975 to 3045 MHz

Features

- · Fractional N synthesizer
- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3V)
- Small size 0.80" x 0.58" x 0.24"

Applications

• WiMAX 3.5GHz



CASE STYLE: DK1182 PRICE: \$29.95 ea. QTY (1-9)

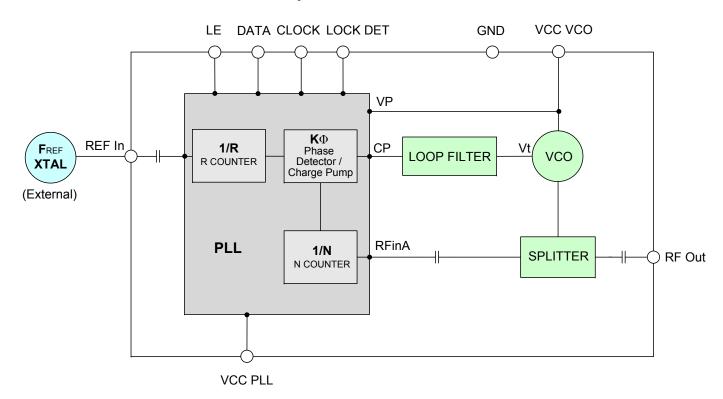
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

General Description

The KSN-3075A-119+ is a Frequency Synthesizer, designed to operate from 2975 to 3045 MHz for WiMAX 3.5GHz application. The KSN-3075A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.24") to shield against unwanted signals and noise. To enhance the robustness of KSN-3075A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic





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REV. A M129408 EDR-8753/1A KSN-3075A-119+ Category-A3 RAV 101027 Page 2 of 11

Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters		Test Conditions	Min.	Тур.	Max.	Units		
Frequency Range			2975	-	3045	MHz		
Step Size		-	-	250	-	kHz		
Comparison Frequency		-	-	20	-	MHz		
Settling Time		Within ± 1 kHz	-	6	-	mSec		
Output Power		-	0	+3	+6	dBm		
		@ 100 Hz offset	-	-76	-			
		@ 1 kHz offset	-	-91	-83	1		
SSB Phase Noise		@ 10 kHz offset	-	-96	-92	dBc/Hz		
		@ 100 kHz offset	-	-124	-120	1		
		@ 1 MHz offset	-	-144	-140			
Integrated SSB Phase Noise		@1 kHz to 5.5 kHz	-	-46	-43	dBc		
Step Size Spurious Suppress	ion	Step Size 250 kHz	-	-98	-82			
0.5 Step Size Spurious Suppl	ression	0.5 Step Size 125 kHz	-	-83	-65			
Reference & Comparison Spi	urious Suppression	Ref. & Comp. Freq. 20 MHz	-	-92	-70	dBc		
Non - Harmonic Spurious Sup	pression	-	-	-90	-]		
Harmonic Suppression		-	-	-30	-23	1		
VCO Supply Voltage		+5.00	+4.90	+5.00	+5.10	V		
PLL Supply Voltage		+3.00	+2.85	+3.00	+3.15] V		
VCO Supply Current		-	-	45	52			
PLL Supply Current		-	-	15 23		mA		
	Frequency	20 (square wave)	-	20	-	MHz		
Reference Input	Amplitude	1	-	1	-	V _{P-P}		
(External)	Input impedance	-	-	100	-	ΚΩ		
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz		
RF Output port Impedance		-	-	50	-	Ω		
Input Logic Level	Input high voltage	-	2.55	-	-	V		
input Logic Level	Input low voltage	-	-	-	0.55	V		
Digital Lock Detect	Locked	-	2.45	-	3.15	V		
Digital Lock Detect	Unlocked	-	-	-	0.40	V		
Frequency Synthesizer PLL		-	ADF4153					
PLL Programming		-	3-wire serial 3V CMOS					
	R0_Register	-	(MSB) 100	(MSB) 100110000000001010000 (LSB)				
Decister Man @ 2045 MU-	R1_Register	-	(MSB) 101000100000101000001 (LSB)			SB)		
Register Map @ 3045 MHz	R2_Register	-	(MSB) 1111100010 (LSB)					
	R3_Register	-	(MSB) 1111000111 (LSB)					

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	5.6V
PLL Supply Voltage	4.0V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.8V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	PO	POWER OUTPUT			VCO CURRENT			PLL CURRENT		
(MHz)		(dBm)			(mA)			(mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
2975	2.96	2.68	1.89	44.04	45.87	47.18	13.45	15.16	17.50	
2983	3.00	2.71	1.96	44.02	45.91	47.18	13.32	15.04	17.35	
2992	3.04	2.76	2.03	44.03	45.91	47.17	13.48	15.20	17.52	
3001	3.08	2.77	1.99	44.04	45.90	47.25	13.16	14.87	17.17	
3010	3.04	2.76	2.06	44.02	45.90	47.16	13.53	15.26	17.58	
3019	2.92	2.68	2.00	44.03	45.89	47.15	13.20	14.92	17.22	
3028	2.81	2.57	1.90	44.02	45.88	47.14	13.45	15.19	17.50	
3037	2.72	2.45	1.83	43.99	45.90	47.13	13.34	15.07	17.38	
3045	2.76	2.42	1.79	43.97	45.87	47.12	13.39	15.11	17.44	

FREQUENCY	HARMONICS (dBc)					
(MHz)		F2		F3		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
2975	-35.01	-45.20	-52.91	-30.98	-38.29	-35.29
2983	-34.62	-45.52	-51.53	-31.97	-38.26	-36.11
2992	-34.64	-46.04	-51.93	-31.99	-37.98	-36.08
3001	-34.94	-46.24	-49.47	-32.78	-38.21	-36.18
3010	-34.85	-45.90	-49.50	-33.54	-39.27	-37.03
3019	-34.80	-45.72	-49.15	-33.41	-39.64	-37.55
3028	-35.32	-46.70	-46.47	-33.45	-39.33	-37.79
3037	-36.38	-48.69	-44.92	-34.22	-39.27	-38.44
3045	-37.08	-50.20	-44.61	-34.49	-39.05	-38.54



FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS					
(MHz)			+25°C			
, ,	100Hz	1kHz	10kHz	100kHz	1MHz	
2975	-82.56	-91.56	-97.12	-125.19	-145.66	
2983	-82.35	-92.48	-96.78	-125.69	-145.64	
2992	-81.51	-92.39	-96.65	-125.21	-145.56	
3001	-81.98	-92.02	-96.44	-125.33	-145.56	
3010	-81.65	-91.59	-96.34	-125.30	-145.38	
3019	-80.93	-91.93	-95.72	-125.13	-145.59	
3028	-83.11	-91.24	-96.87	-125.32	-145.64	
3037	-81.73	-90.12	-96.52	-125.28	-145.36	
3045	-81.34	-90.37	-96.33	-125.14	-145.36	

FREQUENCY	PH	PHASE NOISE (dBc/Hz) @OFFSETS				
(MHz)						
, ,	100Hz	1kHz	10kHz	100kHz	1MHz	
2975	-80.88	-89.79	-97.49	-125.41	-146.19	
2983	-80.62	-90.21	-97.01	-125.39	-145.26	
2992	-80.30	-89.76	-96.89	-125.47	-145.94	
3001	-79.15	-89.75	-96.63	-125.39	-145.75	
3010	-79.25	-89.17	-96.22	-125.32	-145.64	
3019	-80.81	-88.20	-96.56	-125.19	-145.76	
3028	-79.49	-89.80	-96.60	-125.07	-145.49	
3037	-79.15	-88.54	-96.15	-125.13	-145.53	
3045	-79.80	-89.72	-96.35	-125.20	-145.45	

FREQUENCY	PH	PHASE NOISE (dBc/Hz) @OFFSETS								
(MHz)		+85°C								
, ,	100Hz	1kHz	10kHz	100kHz	1MHz					
2975	-82.23	-93.37	-97.07	-124.60	-144.71					
2983	-82.16	-93.59	-96.92	-124.54	-144.72					
2992	-80.93	-92.56	-96.47	-124.61	-144.85					
3001	-82.34	-93.29	-96.38	-124.46	-144.77					
3010	-81.99	-93.67	-96.42	-124.64	-144.46					
3019	-81.29	-93.53	-96.15	-124.38	-144.51					
3028	-82.26	-92.73	-96.57	-124.62	-144.57					
3037	-82.13	-92.93	-96.14	-124.55	-144.67					
3045	-81.43	-93.90	-96.93	-124.43	-144.78					









REFERENCE & COMPARISON SPURIOUS ORDER	REFERENCE & COMPARISON SPURIOUS @ Fcarrier 2975MHz+(n*Freference) (dBc) note 1			RISON SPURIOUS @Fcarrier SPURIOUS @Fcarrier IOUS 2975MHz+(n*Freference) 3010MHz+(n*Freference)			REFERENCE & COMPARISON SPURIOUS @Fcarrier 3045MHz+(n*Freference) (dBc) note 1		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-98.22	-96.45	-101.73	-98.66	-94.93	-100.28	-98.94	-96.08	-98.10
-4	-96.73	-92.94	-94.89	-95.68	-93.07	-93.84	-96.14	-93.89	-92.85
-3	-100.44	-95.70	-97.28	-98.09	-97.26	-94.21	-95.98	-94.90	-93.78
-2	-107.56	-106.37	-100.76	-113.24	-104.22	-100.71	-109.26	-101.61	-99.87
-1	-87.48	-91.38	-87.97	-88.58	-90.74	-88.49	-90.22	-92.56	-88.94
o ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-91.57	-94.20	-88.35	-95.28	-92.75	-90.12	-97.83	-95.40	-92.09
+2	-101.07	-105.54	-98.25	-104.73	-98.53	-100.16	-105.90	-99.86	-97.66
+3	-106.91	-100.08	-100.39	-104.52	-103.60	-98.08	-107.11	-101.39	-97.51
+4	-96.45	-95.72	-94.59	-97.94	-96.13	-93.83	-101.69	-94.67	-96.60
+5	-95.08	-98.65	-96.43	-96.15	-93.91	-97.81	-105.28	-95.17	-96.91

Note 1: Comparison frequency = reference frequency = 20 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

STEP SIZE SPURIOUS ORDER	0.5 STEP SIZE & STEP SIZE SPURIOUS @ Fcarrier 2975MHz+(n*Fstep size) (dBc) note 3			RIOUS @Fcarrier SPURIOUS @Fcarrier MHz+(n*Fstep size) 3010MHz+(n*Fstep size)			0.5 STEP SIZE & STEP SIZE SPURIOUS @Fcarrier 3045MHz+(n*Fstep size) (dBc) note 3		
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5.0	-108.74	-111.32	-111.44	-110.76	-115.58	-108.55	-114.50	-112.84	-114.06
-4.5	-110.96	-112.69	-109.06	-109.26	-109.25	-111.76	-109.52	-108.79	-111.31
-4.0	-114.54	-115.33	-112.88	-112.88	-113.10	-114.23	-113.54	-110.54	-112.14
-3.5	-112.81	-113.91	-110.36	-106.03	-110.45	-107.49	-111.48	-109.46	-109.13
-3.0	-111.39	-109.44	-112.04	-111.16	-113.46	-112.31	-108.76	-106.94	-111.83
-2.5	-111.98	-109.17	-109.91	-110.54	-108.98	-107.30	-106.52	-104.56	-112.50
-2.0	-93.91	-99.75	-97.01	-97.82	-103.86	-105.92	-104.43	-101.75	-109.36
-1.5	-102.47	-101.19	-105.70	-101.49	-107.41	-100.72	-101.94	-104.47	-101.84
-1.0	-98.26	-94.60	-98.91	-97.54	-100.48	-97.48	-97.47	-100.47	-99.06
-0.5	-83.95	-86.44	-85.46	-84.35	-83.29	-84.03	-86.67	-83.57	-86.71
0 ^{note 4}	-	-	-	-	-	-	-	-	-
+0.5	-84.00	-85.37	-85.18	-86.08	-85.78	-84.71	-84.23	-86.23	-87.54
+1.0	-96.40	-99.58	-97.80	-95.80	-97.83	-99.13	-96.98	-94.83	-94.45
+1.5	-103.86	-106.91	-105.79	-103.32	-106.20	-101.47	-103.08	-102.93	-102.85
+2.0	-95.32	-98.95	-97.00	-100.85	-108.33	-107.14	-104.07	-101.75	-106.69
+2.5	-110.80	-111.52	-111.17	-110.56	-110.47	-109.22	-106.77	-103.45	-107.53
+3.0	-109.58	-112.19	-110.49	-106.49	-112.00	-112.30	-107.60	-109.22	-113.20
+3.5	-109.72	-115.14	-109.49	-106.43	-111.87	-109.28	-111.63	-107.84	-113.09
+4.0	-112.38	-113.47	-112.01	-112.88	-114.26	-113.23	-111.69	-111.37	-113.71
+4.5	-110.93	-113.69	-113.70	-110.12	-104.54	-110.68	-112.39	-107.89	-111.84
+5.0	-111.70	-115.00	-109.93	-111.91	-112.65	-111.46	-112.35	-109.63	-112.79

Note 3: Step size frequency 250 kHz

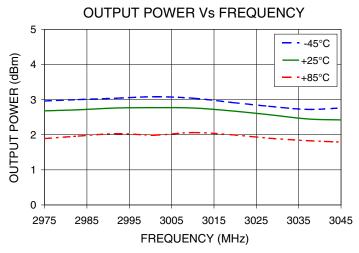
Note 4: All spurs are referenced to carrier signal (n=0).

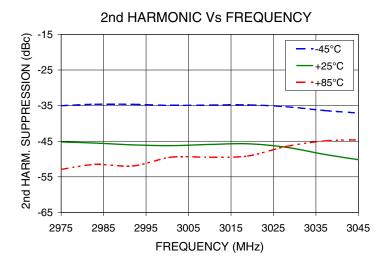


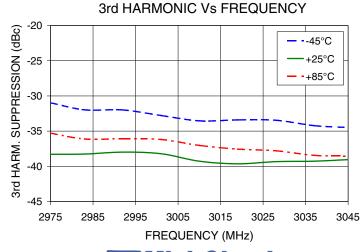
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Typical Performance Curves

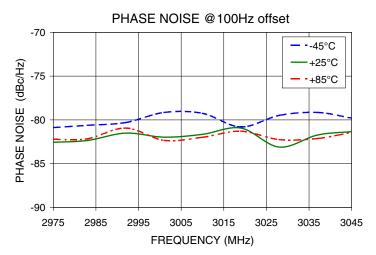


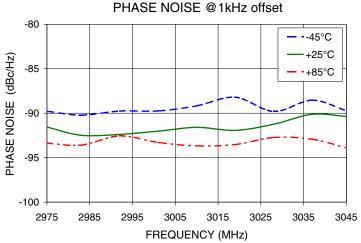


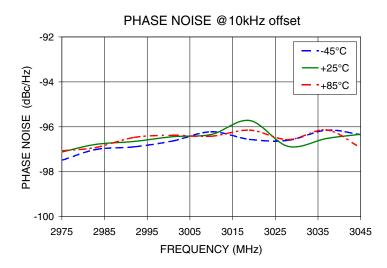


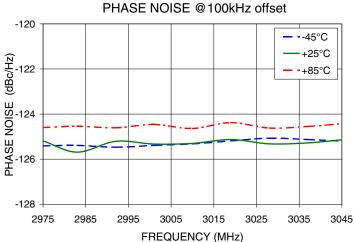
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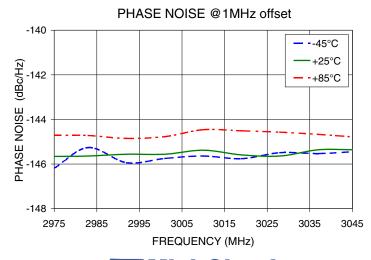
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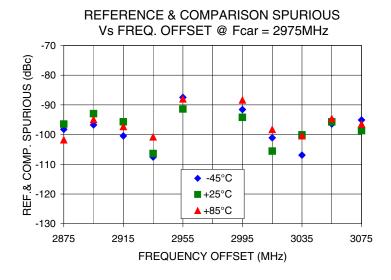
Mini-Circuits

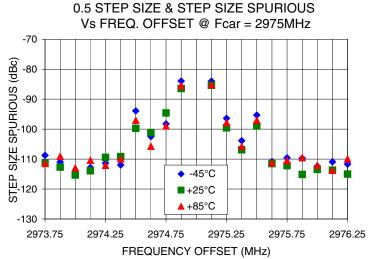
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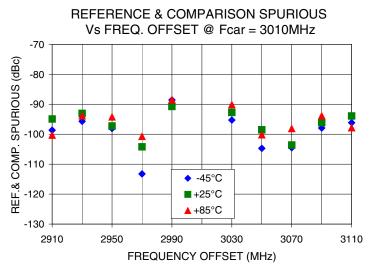
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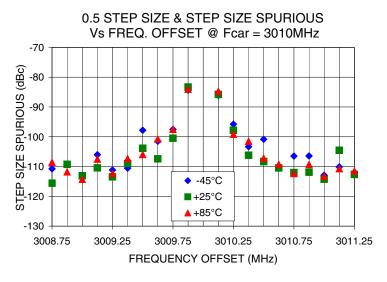
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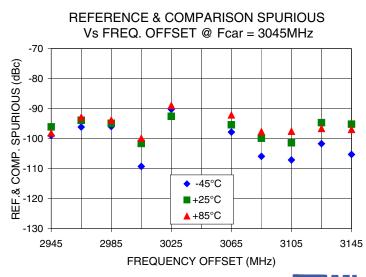


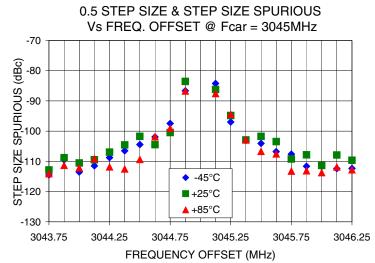












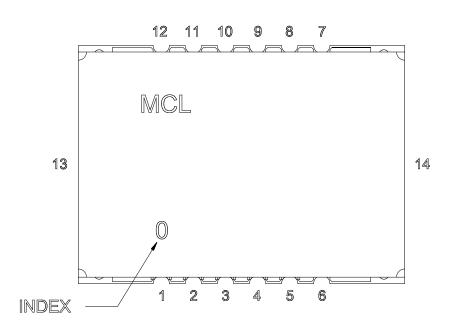
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Pin Configuration

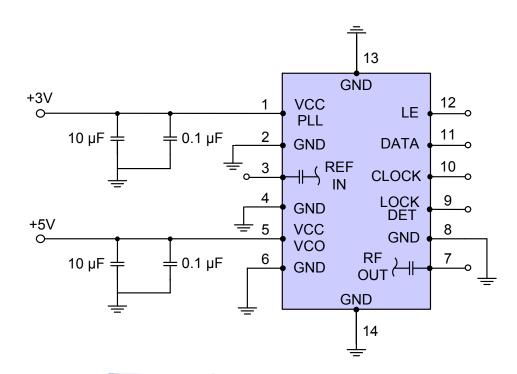


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.

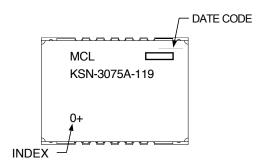




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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1182

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-2+

Environment Ratings: ENV03T2

