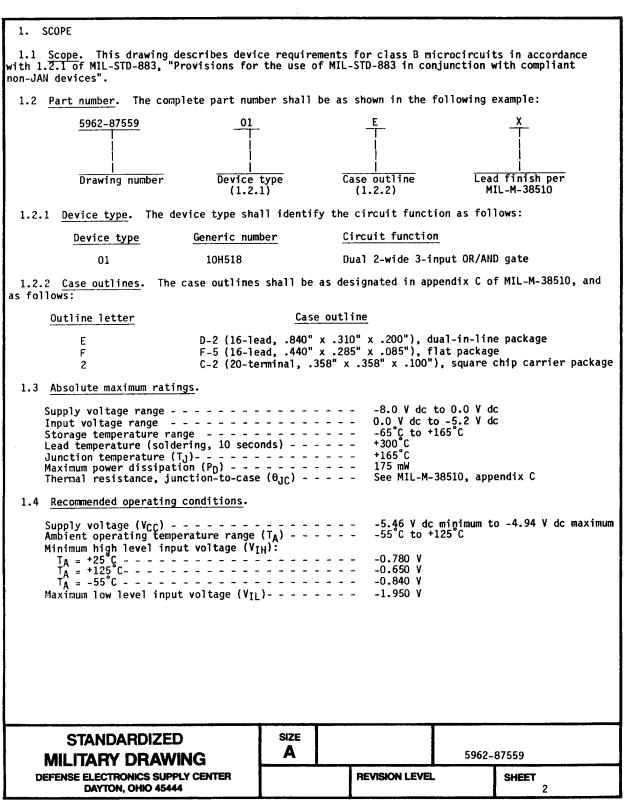
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		٩R	Y	D		PREPARED BY David W. Quenan CHECKED BY Ray Morrin APPROVE BY							DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444 MICROCIRCUIT, DIGITAL, ECL, OR/AND GATE, MONOLITHIC SILICON													
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DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.



2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510

- Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883

Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

- 2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.
 - 3. REQUIREMENTS
- 3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.
- 3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.
 - 3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.
 - 3.2.2 Truth table. The truth table shall be as specified on figure 2.
 - 3.2.3 <u>Logic diagram</u>. The logic diagram shall be as specified on figure 3.
 - 3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.
- 3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full ambient operating temperature range.
- 3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.
- 3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.
- 3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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	TABLE I	. <u>Electri</u>	ical pe	rforma	ance	characte	ristics.				
Test	Symbol		Condit	ions	.		Group A	Limits			Unit
		-55°C unless	: +125 vise s	pec	ified	subgroups	Min	! 	Max		
Cases E, F, and 2											
High level output	V _{OH}	Outputs				ΛΙΓ	<u> </u>				
voltage		terminat through 100Ω to $V_{CC} = 0$.	-2 V 0 V	-0.7 -0.6 -0.8	50	-1.950 -1.950 -1.950	1 2 3	-1.0 -0.1	860 J		ν
Low level output voltage	v _{OL}	VEE = -5 <u>2</u> / 	= -5.2 V		80 550 840	-1.950 -1.950 -1.950	1 2 3	-1. -1. -1.	950 j		
High level threshold output voltage	V _{OHA}	 		-1.1 -0.9 -1.1	960	-1.480 -1.465 -1.510	 1 2 3	-0.	010 860 060	-0.650	٧
Low level threshold output voltage	V _{OLA}	 		-1.1 -0.9 -1.1	60	-1.480 -1.465 -1.510	 1 2 3	-1.	950 950 950	-1.565	V
Power supply drain current	I IEE	VEE = -5.46 V VCC = 0.0 V VIH = -0.780 V at +2! -0.650 V at +1: -0.840 V at -5!			12 5 1	С	1 2, 3	 -26 -29 			mA
High level input current	I _{IH1}	 				E, and F inputs	 1,2 3] 		275 465	μА
	 1 _{1H2} 	† † !				and C inputs	1, 2 3	[[]		320 545	μ A
	I I I H3	† 			A	input	1, 2			415 710	μ Α
Low level input current I_{IL} $V_{EE} = -4$ $V_{IL} = -1$ $V_{CC} = 0$.				1.950 V					:	0.5	μ Α
See footnotes at end of table.											
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Test	 Symbol	Con	Conditions -55°C ≤ TA ≤ +125°C				Lin	Unit		
	! 	l -55 C <u><</u> I unless oth 	less otherwise specified			subgroups 	Min	Max	 	
Cases E and F			Rapid 1	tests	3/	<u> </u>				
		Outputs	VII	V _{IH} V _{IL}		<u> </u>				
voltage	! ! !	terminated through 100Ω to -2 V V _{CC} = 0.0 V	-0. -0.	. 787 . 658 . 858	-1.950 -1.950 -1.950	1 2 3	-1.017 -0.867 -1.067	-0.658	Ì	
Low level output voltage	 v _{OL} 	VEE = -5.2 V	-0.	. 787 . 658 . 848	-1.950 -1.950 -1.950	1 2 3	-1.950 -1.950 -1.950	-1.567	٧	
High level threshold output voltage	I I V _{OHA} I	 	ĺ -0.	117 967 167	-1.482 -1.467 -1.512	1 2 3	-1.017 -0.867 -1.067	-0.658	V	
Low level threshold output voltage	V _{OLA}		ļ -0.	117 967 167	-1.482 -1.467 -1.512	1 2 3	-1.950 -1.950 -1.950	-1.567	٧	
Power supply drain current	I I E E			·125°(·	1 2, 3	-25 -28		mA	
High level input current	I _{IH1}	 			E, and F	1, 2		260 450	μА	
	I IH2	Г 			nd C inputs	1, 2		305 530	μА	
	I 1H3	<u> </u>		A i	nput	1, 2		400 695	μА	
Low level input current	IIL	VEE = -4.94 VIL = -1.950 VCC = 0.0 V	v v	· :		1, 3		0.5	μА	
ee footnotes at end of t	able.					<u> </u>		<u>'</u>		
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TABLE I. Electrical performance characteristics - Continued. Symbol Conditions $-55^{\circ}C < T_A < +125^{\circ}C$ unless otherwise specified Unit Test Group A Limits subgroups Min Max Rapid tests 3/ Case 2 $\overline{V_{IH}}$ VIL High level output VOH **Outputs** terminated voltage -1.950 -1.021 -0.792 ٧ -0.792 through -0.872 -1.950 -0.663 -0.853 100Ω to -2 V -0.663 | 2 $V_{CC} = 0.0 \text{ V}$ $V_{EE} = -5.2 \text{ V}$ -1.950 3 -1.072 -0.853 -1.584 -0.792 -1.950 -1.950 VOL Low level output -1.950 -1.950 -1.5691 ٧ 2 voltage -0.663 -1.950 -1.614 -0.853 -1.950 3 -1.484 -1.469 -1.021 -0.792 -0.663 High level threshold -1.121 1 2 VOHA -0.872 ٧ -0.972 output voltage -1.172 -1.514 -1.072 -0.853 -1.484 -1.950 -1.584 -1.569 -1.121 1 2 Low level threshold VOLA -0.972 | -1.469 -1.950 ٧ output voltage -1.950 -1.614 -1.172 | -1.514 3 VEE = -5.46 V 1 2, 3 -25 mΑ Power supply drain IEE $V\overline{CC} = 0.0 V$ -28 current VIH = -0.792 V at +25°C -0.663 V at +125°C -0.853 V at -55°C 1, 2 D, E, and F 260 μΑ High level input |IIH1 450 inputs current B and C 305 μΑ I_{IH2} 530 inputs A input 1, 2 400 μΑ IH3 695 VEE = -4.94 V VIL = -1.950 V VCC = 0.0 V 1, 3 0.5 μΑ Low level input current | IIL 0.3 See footnotes at end of table. SIZE **STANDARDIZED** Α 5962-87559 MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER **REVISION LEVEL.** SHEET DAYTON, OHIO 45444 6

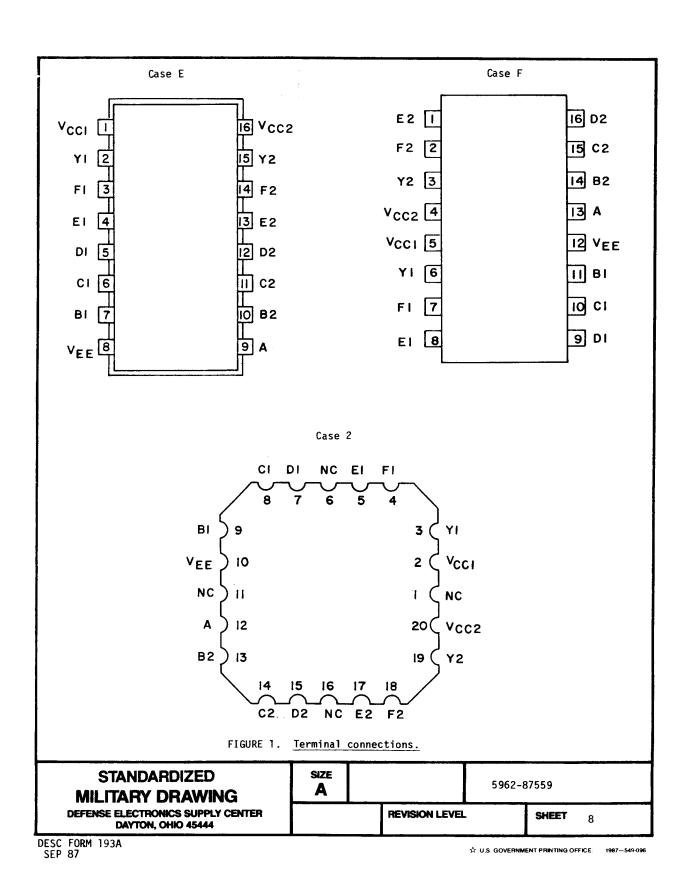
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Test	Symbol	Conditions	Group A	Lim	Unit	
-	! 	-55°C < TA < +125°C unless otherwise specified	subgroups 	Min	Max	<u> </u>
Cases E, F, and 2		AC tests				
Transition time	 t _{TLH} , tTHL	V _{EE} = -2.94 V V _{CC} = 2.0 V CL ≤ 5 pF	9 10 11	0.50 0.50 0.50	1.60 1.70 1.50	ns
Propagation delay time, A, B, C, D, F to Y	tpLH, tpHL, tpLL, tpHH	R _L = 100Ω <u>4</u> / 	9 10 11	0.50 0.50 0.50	1.70 1.85 1.70	ns

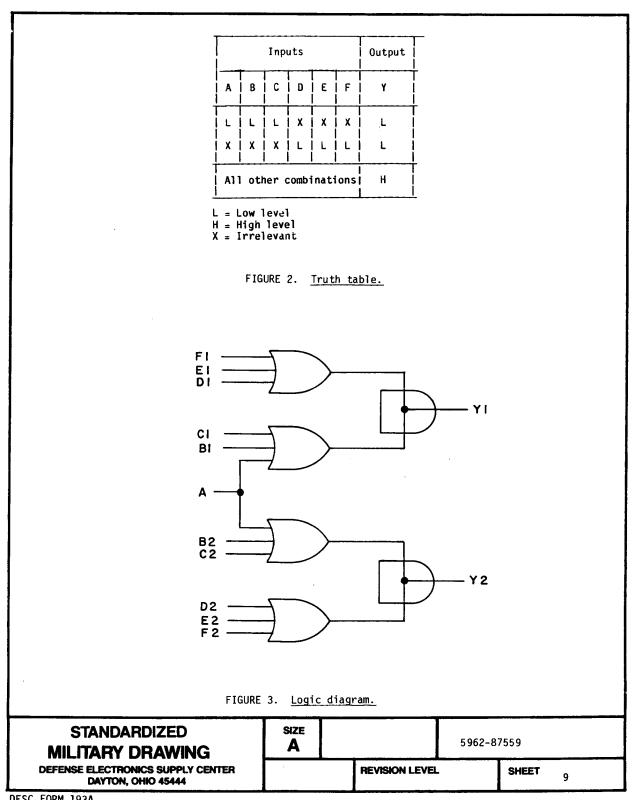
- 1/ The quiescent limits are determined after a device has reached thermal equilibrium. This is defined as the reading taken with the device in a socket with > 500 LFPM of +25°C, +125°C or -55°C (as applicable), air blowing on the unit in a transverse direction with power applied for at least four minutes before the reading is taken. This method was used for theoretical limit establishment only. All devices shall be tested to the delta V (rapid test) conditions specified herein. The rapid test method is an equivalent method of testing quiescent conditions.
- Z/ The high and low level output current varies with temperature and can be calculated using the following formula: $I_{OH} = (V_{OH} 2 V)/100\Omega$; $I_{OL} = (V_{OL} 2 V)/100\Omega$.
- 3/ The dc rapid test forcing functions and limits are used for all dc testing. These limits are determined for each device type based on the power dissipation and package type. The rapid test (delta V) limits and forcing functions are skewed allowing rapid testing to be performed at standard temperatures without the addition of delta T's.
- The load circuit shall consist of the output under test going through $R_{\rm L}$ to GND and through $C_{\rm L}$ to the tester. $C_{\rm L}$ includes scope probe, wiring, and stray capacitance without the package in the test fixture. tp_L shall be measured from the midpoint of a low input to the midpoint of a low output. tp_H shall be measured from the midpoint of a high input to the midpoint of a high output. tp_H and tp_H voltage waveforms shall be in accordance with method 3003 of MIL-STD-883, with threshold voltage points at 50 percent. tr_L and tr_H voltage waveforms shall be in accordance with method 3004 of MIL-STD-883, with measurement points at 20 percent and 80 percent.
- 3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).
- 3.8 <u>Verification and review.</u> DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

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- 4. QUALITY ASSURANCE PROVISIONS
- 4.1 <u>Sampling and inspection</u>. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).
- 4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125$ °C, minimum.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

TABLE II. Electrical test requirements.

	Subgroups
MIL-STD-883 test requirements	(per method
	5005, table I)
 Interim electrical parameters (method 5004)	1
Final electrical test parameters	1*, 2, 3, 7*,
(method 5004)	9
Group A test requirements	1, 2, 3, 7, 9,
(method 5005)	10, 11
 Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

- * PDA applies to subgroups 1 and 7.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method $\overline{5005}$ of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.
 - 4.3.1 Group A inspection.
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 4, 5, 6, and 8 in table I, method 5005 of MIL-STD-883 shall be omitted.
 - c. Subgroup 7 tests shall verify the truth table specified on figure 2 herein.

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- 4.3.2 Groups C and D inspections.
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test conditions, method 1005 of MIL-STD-883:
 - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.
- 5. PACKAGING
- 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.
 - 6. NOTES
- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.
- 6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

 Military drawing part number 	Vendor CAGE number	Vendor similar part number <u>1</u> /
5962-87 559 01EX	04713	10H518/BEAJC
 5962-8755901FX 	04713	10H518/BFAJC
5962-87559012X	04713	10H518M/B2AJC

Caution: Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

> Vendor CAGE number

Vendor name and address

04713

Motorola, Incorporated 7402 South Price Road Tempe, AZ 85283

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